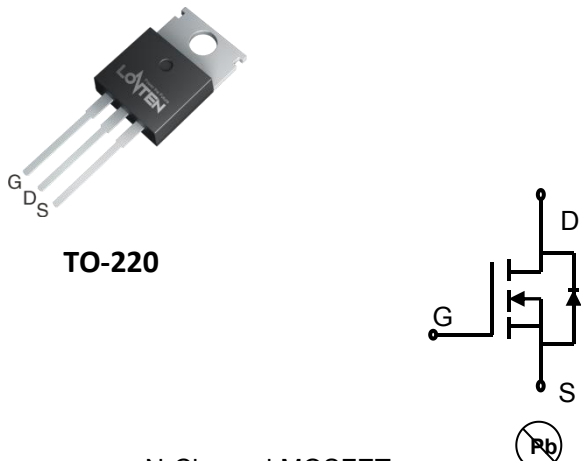


Lonten N-channel 80V, 45A, 22mΩ Power MOSFET

<p>Description</p> <p>These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and with stand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.</p> <p>Features</p> <ul style="list-style-type: none"> ◆ 80V,45A,$R_{DS(ON),max}=22m\Omega@V_{GS}=10V$ ◆ Improved dv/dt capability ◆ Fast switching ◆ 100% EAS Guaranteed ◆ Green device available <p>Applications</p> <ul style="list-style-type: none"> ◆ Motor Drives ◆ UPS ◆ DC-DC Converter 	<p>Product Summary</p> <table style="width: 100%; border: none;"> <tr> <td style="padding: 2px 5px;">V_{DSS}</td> <td style="padding: 2px 5px;">80V</td> </tr> <tr> <td style="padding: 2px 5px;">$R_{DS(on),max}@V_{GS}=10V$</td> <td style="padding: 2px 5px;">22mΩ</td> </tr> <tr> <td style="padding: 2px 5px;">I_D</td> <td style="padding: 2px 5px;">45A</td> </tr> </table> <p>Pin Configuration</p> <div style="text-align: center;">  <p>TO-220</p> <p>N-Channel MOSFET</p> </div>	V_{DSS}	80V	$R_{DS(on),max}@V_{GS}=10V$	22mΩ	I_D	45A
V_{DSS}	80V						
$R_{DS(on),max}@V_{GS}=10V$	22mΩ						
I_D	45A						

Absolute Maximum Ratings $T_C = 25^\circ C$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	80	V
Continuous drain current ($T_C = 25^\circ C$)	I_D	45	A
Continuous drain current ($T_C = 100^\circ C$)		28	A
Pulsed drain current ¹⁾	I_{DM}	180	A
Gate-Source voltage	V_{GSS}	± 20	V
Avalanche energy ²⁾	E_{AS}	64	mJ
Power Dissipation ($T_C = 25^\circ C$)	P_D	92	W
Storage Temperature Range	T_{STG}	-55 to +150	$^\circ C$
Operating Junction Temperature Range	T_J	-55 to +150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.36	$^\circ C/W$

Package Marking and Ordering Information

Device	Device Package	Marking
LNC08R220	TO-220	LNC08R220

Electrical Characteristics
 $T_J = 25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV_{DSS}	$V_{GS}=0\text{ V}, I_D=250\mu\text{A}$	80	---	---	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1	1.6	2.5	V
Drain-source leakage current	I_{DSS}	$V_{DS}=80\text{V}, V_{GS}=0\text{V}, T_J = 25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=64\text{V}, V_{GS}=0\text{V}, T_J = 125^\circ\text{C}$	---	---	10	μA
Gate leakage current, Forward	I_{GSSF}	$V_{GS}=20\text{V}, V_{DS}=0\text{ V}$	---	---	100	nA
Gate leakage current, Reverse	I_{GSSR}	$V_{GS}=-20\text{V}, V_{DS}=0\text{ V}$	---	---	-100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=10\text{V}, I_D=30\text{A}$	---	17	22	m Ω
		$V_{GS}=4.5\text{V}, I_D=20\text{A}$	---	22	29	m Ω
Forward transconductance	g_{fs}	$V_{DS} = 5\text{V}, I_D=30\text{A}$	---	40	---	S
Dynamic characteristics						
Input capacitance	C_{iss}	$V_{DS} = 25\text{V}, V_{GS} = 0\text{V},$ $F = 1\text{MHz}$	---	2200	---	pF
Output capacitance	C_{oss}		---	130	---	
Reverse transfer capacitance	C_{riss}		---	101	---	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 40\text{V}, V_{GS}=10\text{V}, I_D = 20\text{A}$	---	7.6	---	ns
Rise time	t_r		---	14	---	
Turn-off delay time	$t_{d(off)}$		---	107.6	---	
Fall time	t_f		---	20.4	---	
Gate resistance	R_g	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, F=1\text{MHz}$	---	2.2	---	Ω
Gate charge characteristics						
Gate to source charge	Q_{gs}	$V_{DS}=40\text{V}, I_D=20\text{A},$ $V_{GS}= 10\text{V}$	---	10.5	---	nC
Gate to drain charge	Q_{gd}		---	8.2	---	
Gate charge total	Q_g		---	47.6	---	
Drain-Source diode characteristics and Maximum Ratings						
Continuous Source Current	I_S		---	---	65	A
Pulsed Source Current ³⁾	I_{SM}		---	---	260	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0\text{V}, I_S=20\text{A}, T_J=25^\circ\text{C}$	---	---	1.2	V
Reverse Recovery Time	t_{rr}	$I_S=20\text{A}, di/dt=100\text{A}/\mu\text{s}, T_J=25^\circ\text{C}$	---	41.6	---	ns
Reverse Recovery Charge	Q_{rr}		---	48.7	---	nC

Notes:

1: Repetitive Rating: Pulse width limited by maximum junction temperature.

 2: $V_{DD}=50\text{V}, V_{GS}=10\text{V}, L=0.5\text{mH}, I_{AS}=16\text{A}, R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$.

 3: Pulse Test: Pulse Width $\leq 300\ \mu\text{s}$, Duty Cycle $\leq 2\%$.

Electrical Characteristics Diagrams

Figure 1. Typ. Output Characteristics

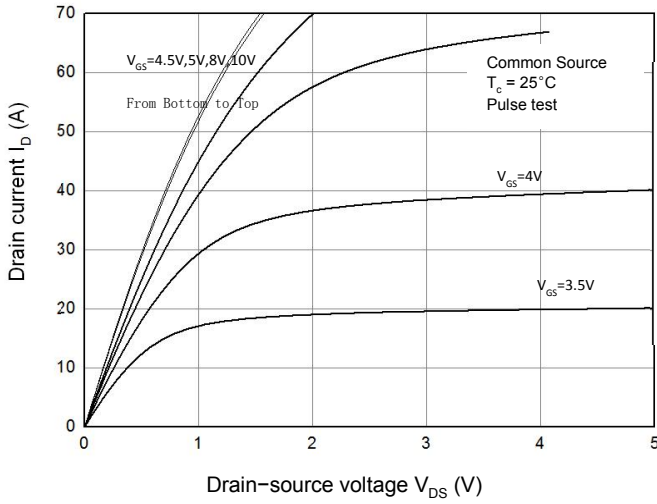


Figure 2. Transfer Characteristics

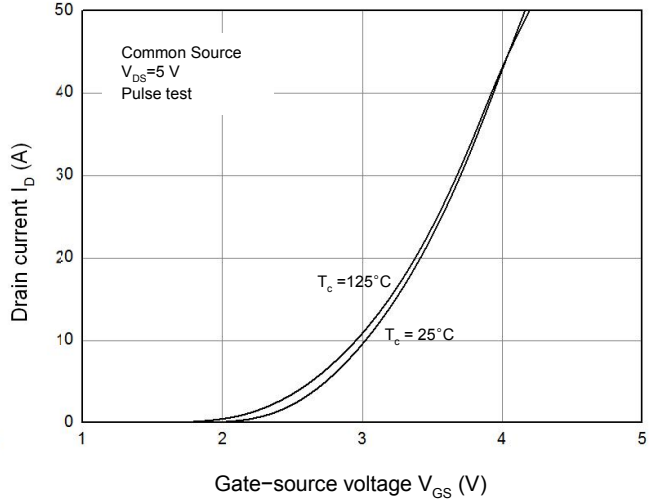


Figure 3. Capacitance Characteristics

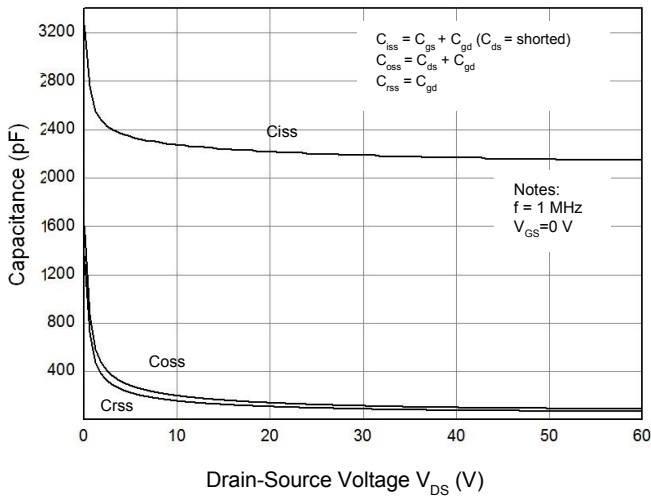


Figure 4. Gate Charge Waveform

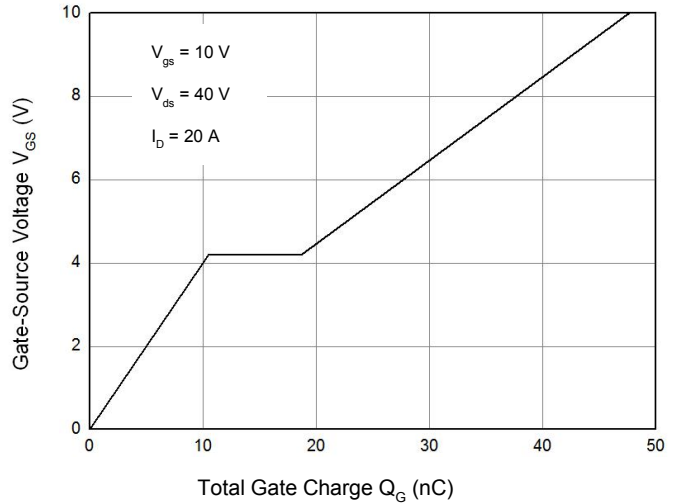


Figure 5. Body-Diode Characteristics

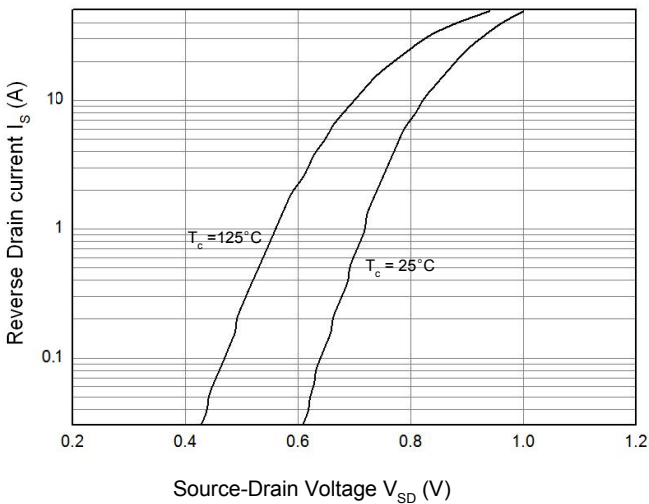


Figure 6. R_{ds(on)}-Drain Current

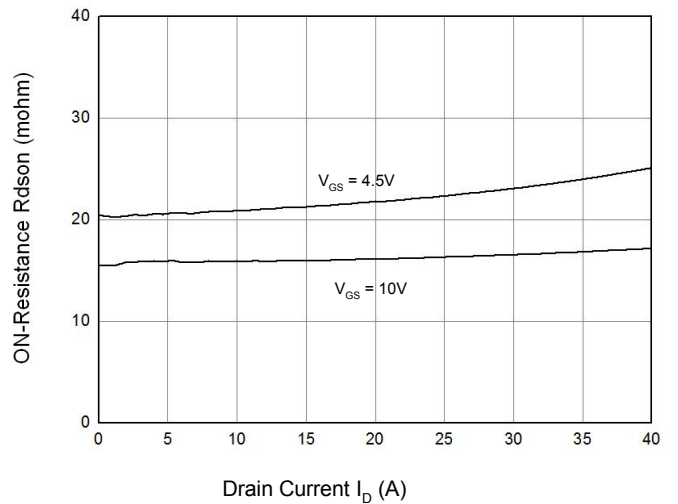


Figure 7. R_{ds(on)}-Junction Temperature(°C)

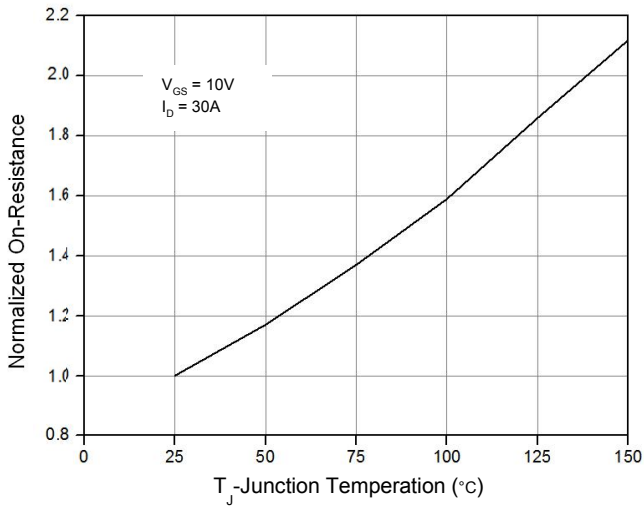


Figure 8. Maximum Safe Operating Area

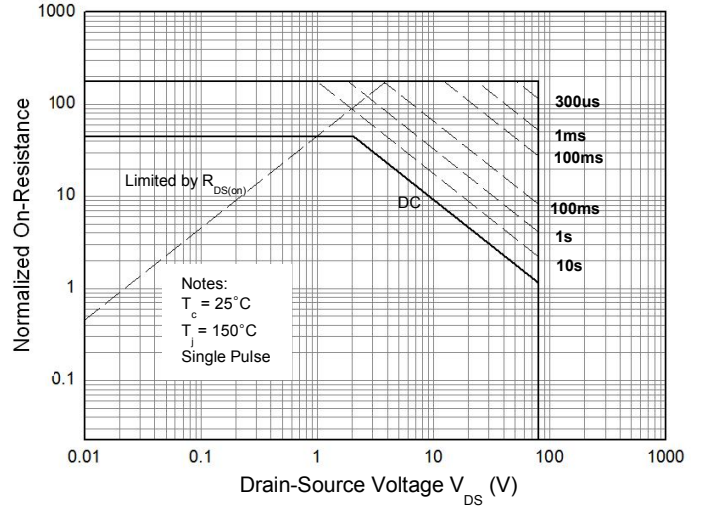
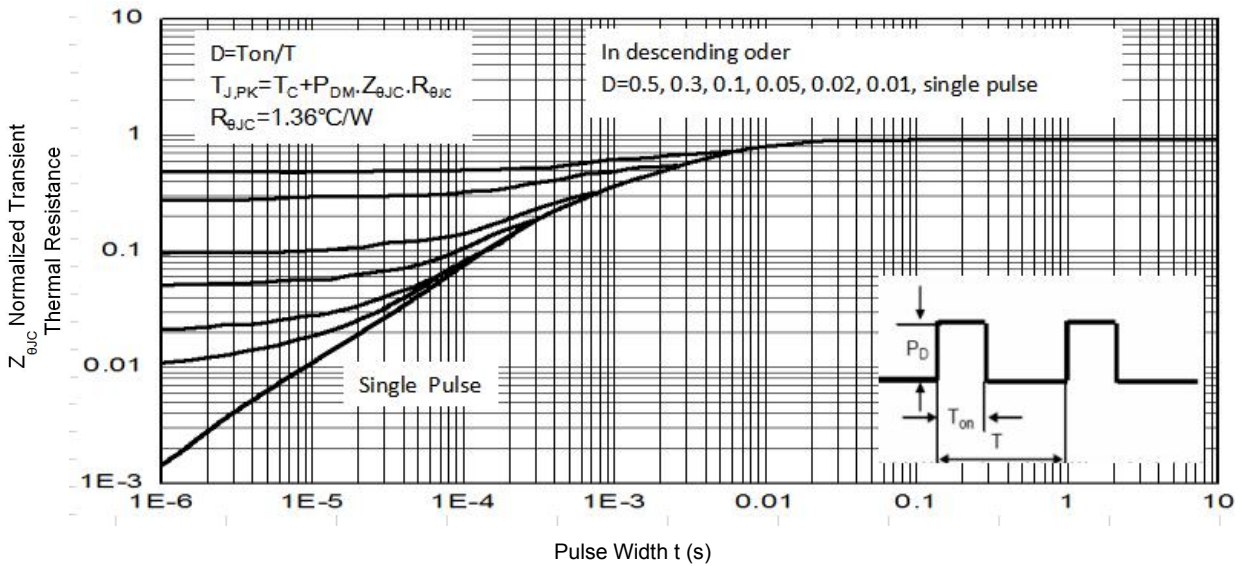


Figure 9. Normalized Maximum Transient Thermal Impedance (R_{thJC})



Test Circuit & Waveform

Figure 8. Gate Charge Test Circuit & Waveform

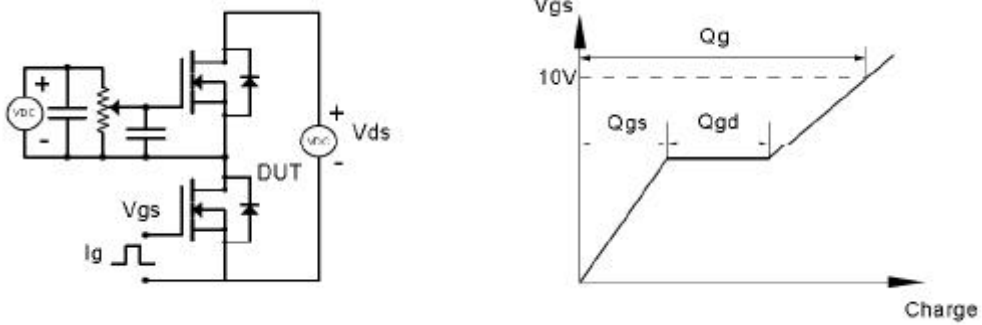


Figure 9. Resistive Switching Test Circuit & Waveforms

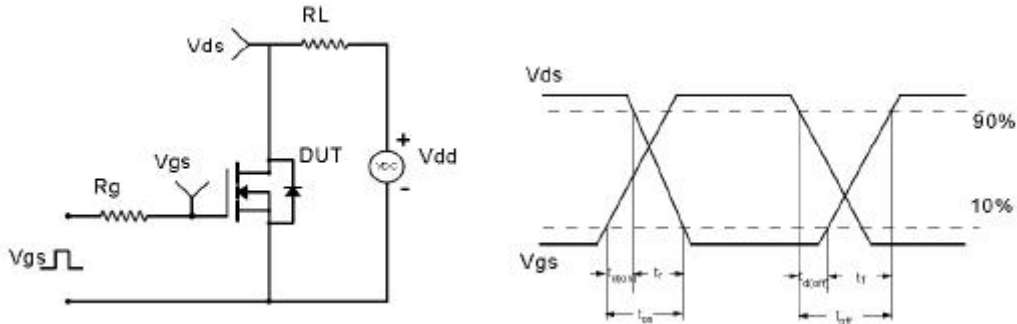


Figure 10. Unclamped Inductive Switching (UIS) Test Circuit & Waveform

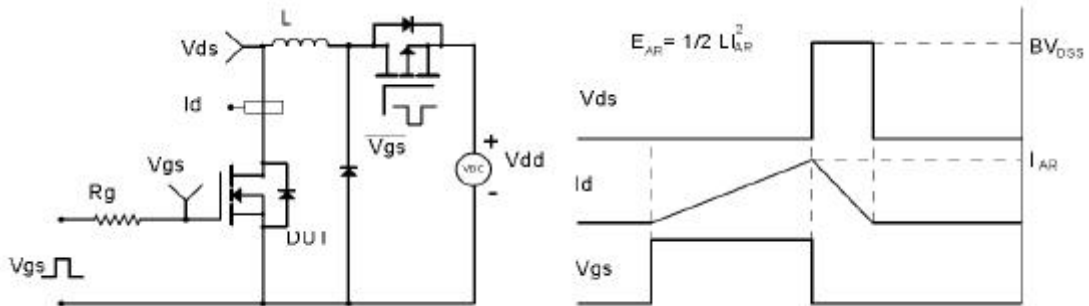
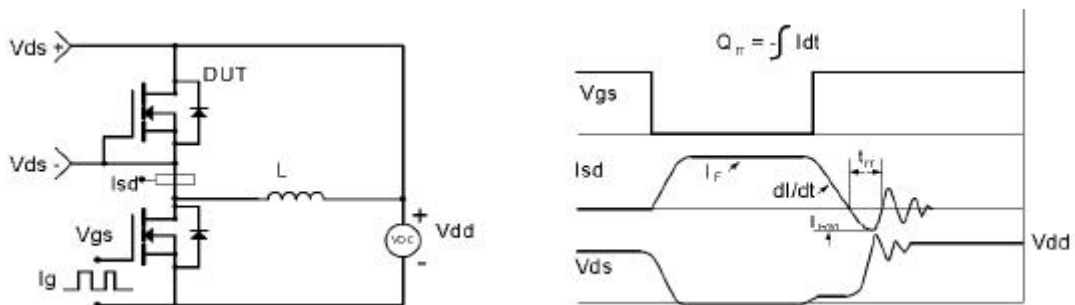
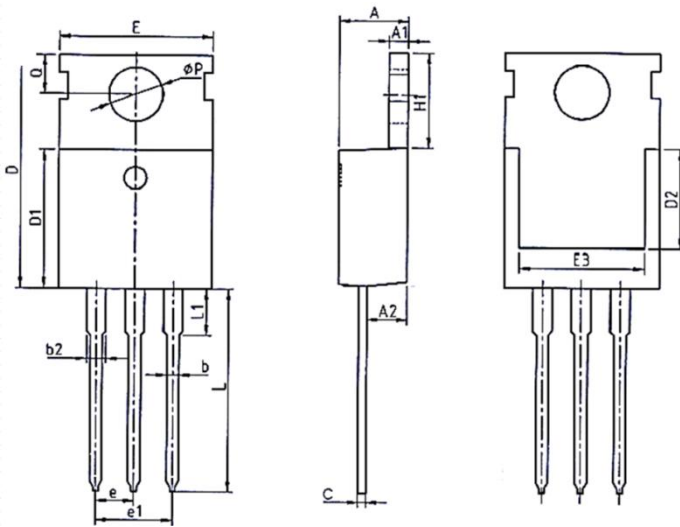


Figure 11. Diode Recovery Circuit & Waveform

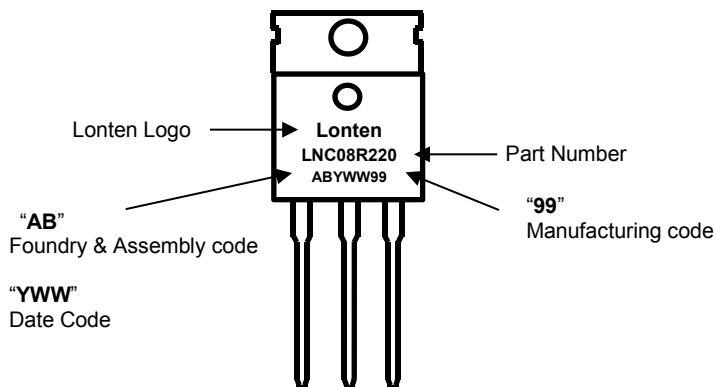


TO-220 PACKAGE INFORMATION



COMMON DIMENSIONS						
SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX
A	4.37	4.57	4.70	0.172	0.180	0.185
A1	1.25	1.30	1.40	0.049	0.051	0.055
A2	2.20	2.40	2.60	0.087	0.094	0.102
b	0.70	0.80	0.95	0.028	0.031	0.037
b2	1.17	1.27	1.47	0.046	0.050	0.058
c	0.45	0.50	0.60	0.018	0.020	0.024
D	15.10	15.60	16.10	0.594	0.614	0.634
D1	8.80	9.10	9.40	0.346	0.358	0.370
D2	5.50	-	-	0.217	-	-
E	9.70	10.00	10.30	0.382	0.394	0.406
E3	7.00	-	-	0.276	-	-
e	2.54BCS			0.1BSC		
e1	5.08BCS			0.2REF		
H1	6.25	6.50	6.85	0.246	0.256	0.270
L	12.75	13.50	13.80	0.502	0.531	0.543
L1	-	3.10	3.40	-	0.122	0.134
ØP	3.40	3.60	3.80	0.134	0.142	0.150
Q	2.60	2.80	3.00	0.102	0.110	0.118

TO-220 Part Marking Information



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