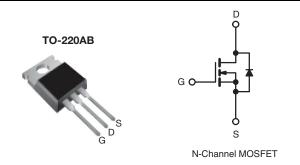


### **Power MOSFET**

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	90	900				
$R_{DS(on)}(\Omega)$	V <sub>GS</sub> = 10 V	8.0				
Q <sub>g</sub> (Max.) (nC)	38	38				
Q <sub>gs</sub> (nC)	4.7	4.7				
Q <sub>gd</sub> (nC)	21	21				
Configuration	Sing	Single				



### **FEATURES**

- Dynamic dV/dt Rating
- Repetitve Avalanche Rated
- · Fast Switching
- · Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC



### **DESCRIPTION**

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION			
Package	TO-220AB		
Lead (Pb)-free	IRFBF20PbF		
Lead (FD)-life	SiHFBF20-E3		
SnPb	IRFBF20		
SIFD	SiHFBF20		

ABSOLUTE MAXIMUM RATINGS ( $T_C$	= 25 °C, unl	ess otherwis	se noted)			
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V <sub>DS</sub>	900	.,	
Gate-Source Voltage			$V_{GS}$	± 20	V	
Continuous Drain Current	V -140V	T <sub>C</sub> = 25 °C		1.7	А	
Continuous Drain Current	V <sub>GS</sub> at 10 V	T <sub>C</sub> = 100 °C	ID	1.1		
Pulsed Drain Current <sup>a</sup>			I <sub>DM</sub>	6.8	1	
Linear Derating Factor				0.43	W/°C	
Single Pulse Avalanche Energy <sup>b</sup>			E <sub>AS</sub>	180	mJ	
Repetitive Avalanche Current <sup>a</sup>			I <sub>AR</sub>	1.7	Α	
Repetitive Avalanche Energy <sup>a</sup>			E <sub>AR</sub>	5.4	mJ	
Maximum Power Dissipation $T_C = 25  ^{\circ}C$			P <sub>D</sub>	54	W	
Peak Diode Recovery dV/dt <sup>c</sup>	dV/dt	1.5	V/ns			
Operating Junction and Storage Temperature Rang	T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 150				
Soldering Recommendations (Peak Temperature) for 10 s				300 <sup>d</sup>	°C	
Mounting Taxaus	6-32 or M3 screw			10	lbf ⋅ in	
Mounting Torque				1.1	N⋅m	

### **Notes**

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b.  $V_{DD}$  = 50 V, starting  $T_J$  = 25 °C, L = 117 mH,  $R_g$  = 25  $\Omega$ ,  $I_{AS}$  = 1.7 A (see fig. 12). c.  $I_{SD} \le 1.7$  A,  $dI/dt \le 70$  A/µs,  $V_{DD} \le 600$ ,  $T_J \le 150$  °C.
- d. 1.6 mm from case.

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply



THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYP.	MAX.	UNIT		
Maximum Junction-to-Ambient	R <sub>thJA</sub>	-	62			
Case-to-Sink, Flat, Greased Surface	R <sub>thCS</sub>	0.50	-	°C/W		
Maximum Junction-to-Case (Drain)	R <sub>thJC</sub>	-	2.3			

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		900	-	-	V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	e to 25 °C, I <sub>D</sub> = 1 mA		1.1	-	V/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_D = 250 \mu A$		-	4.0	V
Gate-Source Leakage	$I_{GSS}$	$V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 900 V, V <sub>GS</sub> = 0 V		-	-	100	μA
ű			$V_{\rm r}, V_{\rm GS} = 0 \text{ V}, T_{\rm J} = 125 ^{\circ}\text{C}$	-	-	500	μΛ
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	$I_D = 1.0 A^b$	-	-	8.0	Ω
Forward Transconductance	9fs	V <sub>DS</sub> =	= 100 V, I <sub>D</sub> = 1.0 A	0.60	-	-	S
Dynamic				1	ı	1	_
Input Capacitance	C <sub>iss</sub>		$V_{GS} = 0 V$ ,	-	490	-	pF
Output Capacitance	C <sub>oss</sub>		$V_{DS} = 25 \text{ V},$	-	55	-	
Reverse Transfer Capacitance	$C_{rss}$	T = 1	0 MHz, see fig. 5	-	18	-	
Total Gate Charge	Qg			-	=	38	nC
Gate-Source Charge	$Q_{gs}$	V <sub>GS</sub> = 10 V	$I_D = 1.7 \text{ A}, V_{DS} = 360 \text{ V},$ see fig. 6 and 13 <sup>b</sup>	-	-	4.7	
Gate-Drain Charge	$Q_{gd}$		, and the second	-	=	21	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 450 V, I <sub>D</sub> = 1.7 A,		-	8.0	-	ns
Rise Time	t <sub>r</sub>			-	21	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_g = 18 \Omega$ ,	$R_g = 18 \Omega$ , $R_D = 280 \Omega$ , see fig. $10^b$		56	-	
Fall Time	t <sub>f</sub>				32	-	
Internal Drain Inductance	$L_D$	6 mm (0.25")	Between lead, 6 mm (0.25") from		4.5	-	ml l
Internal Source Inductance	L <sub>S</sub>	package and center of die contact		-	7.5	-	- nH
Drain-Source Body Diode Characteristic	s			•		•	
Continuous Source-Drain Diode Current	I <sub>S</sub>	MOSFET symbol showing the integral reverse p - n junction diode		_	-	1.7	A
Pulsed Diode Forward Current <sup>a</sup>	I <sub>SM</sub>			-	-	6.8	
Body Diode Voltage	V <sub>SD</sub>	$T_J = 25  ^{\circ}\text{C},  I_S = 1.7  \text{A},  V_{GS} = 0  \text{V}^{\text{b}}$		-	-	1.5	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	$T_J = 25 ^{\circ}\text{C}$ , $I_F = 1.7 \text{A}$ , $dI/dt = 100 \text{A/}\mu\text{s}$		-	350	530	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			-	0.85	1.3	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by L <sub>S</sub> and L <sub>D</sub> )					L <sub>D</sub> )

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width  $\leq$  300  $\mu$ s; duty cycle  $\leq$  2 %.



### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

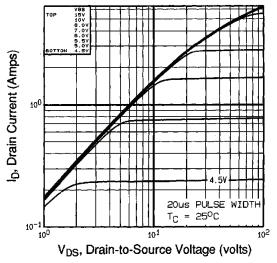


Fig. 1 - Typical Output Characteristics, T<sub>C</sub> = 25 °C

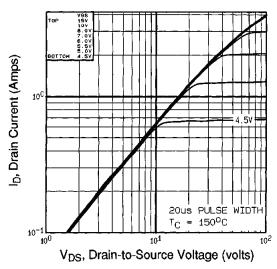


Fig. 2 - Typical Output Characteristics, T<sub>C</sub> = 150 °C

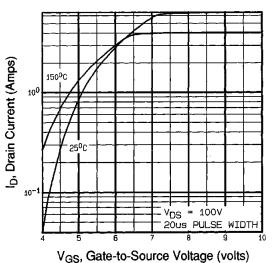


Fig. 3 - Typical Transfer Characteristics

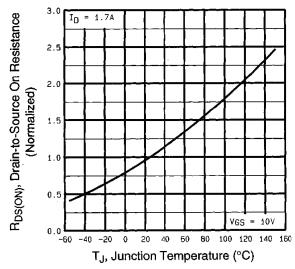


Fig. 4 - Normalized On-Resistance vs. Temperature



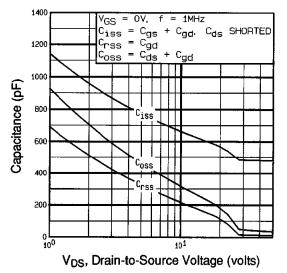


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

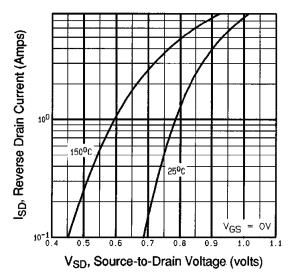


Fig. 7 - Typical Source-Drain Diode Forward Voltage

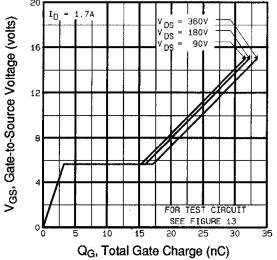


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

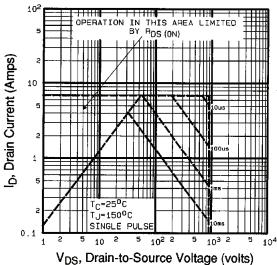


Fig. 8 - Maximum Safe Operating Area



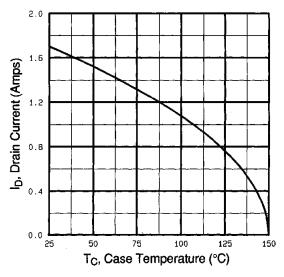


Fig. 9 - Maximum Drain Current vs. Case Temperature

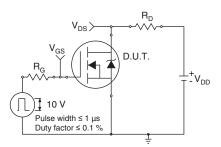


Fig. 10a - Switching Time Test Circuit

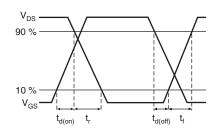


Fig. 10b - Switching Time Waveforms

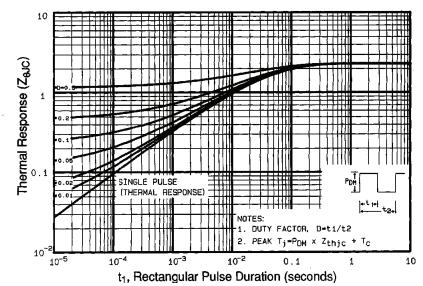


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

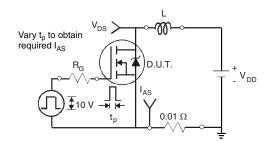


Fig. 12a - Unclamped Inductive Test Circuit

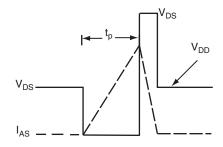


Fig. 12b - Unclamped Inductive Waveforms



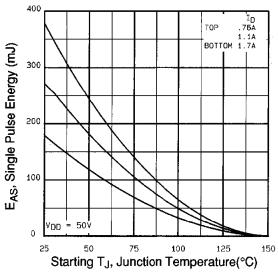


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

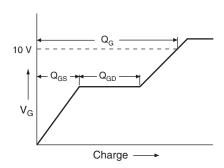


Fig. 13a - Basic Gate Charge Waveform

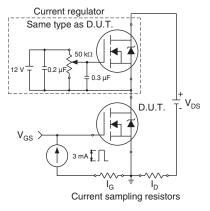
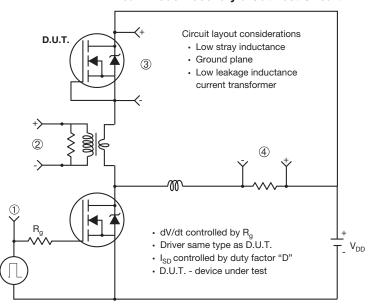


Fig. 13b - Gate Charge Test



#### Peak Diode Recovery dV/dt Test Circuit



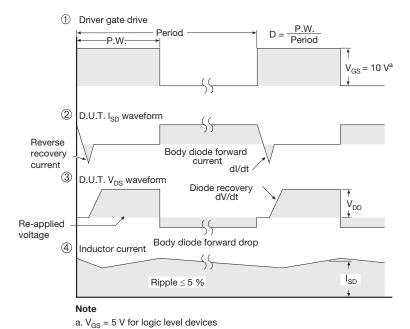


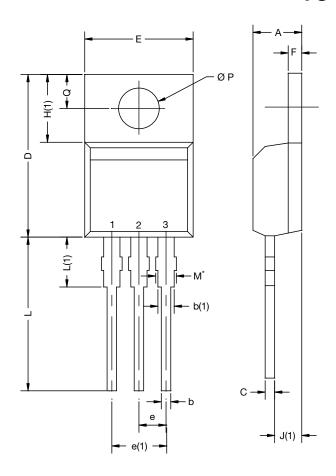
Fig. 14 - For N-Channel

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?91120.





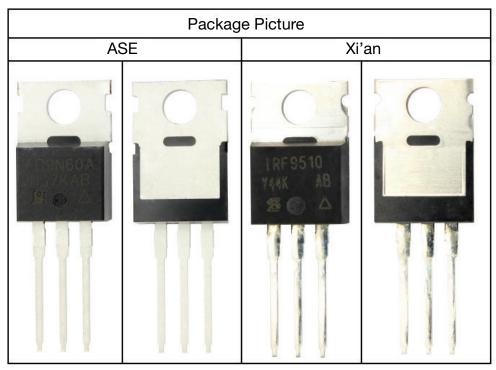
# TO-220-1



DIM	MILLIN	IETERS	INCHES			
DIM.	MIN.	MAX.	MIN.	MAX.		
Α	4.24	4.65	0.167	0.183		
b	0.69	1.02	0.027	0.040		
b(1)	1.14	1.78	0.045	0.070		
С	0.36	0.61	0.014	0.024		
D	14.33	15.85	0.564	0.624		
E	9.96	10.52	0.392	0.414		
е	2.41	2.67	0.095	0.105		
e(1)	4.88	5.28	0.192	0.208		
F	1.14	1.40	0.045	0.055		
H(1)	6.10	6.71	0.240	0.264		
J(1)	2.41	2.92	0.095	0.115		
L	13.36	14.40	0.526	0.567		
L(1)	3.33	4.04	0.131	0.159		
ØР	3.53	3.94	0.139	0.155		
Q	2.54	3.00	0.100	0.118		
ECN: X15-0364-Rev. C, 14-Dec-15 DWG: 6031						

### Note

 $\bullet$   $M^{\star}=0.052$  inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM



Revison: 14-Dec-15 1 Document Number: 66542



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Vishay

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