

UNISONIC TECHNOLOGIES CO., LTD

MC4580

LINEAR INTEGRATED CIRCUIT

DUAL OPERATIONAL AMPLIFIER

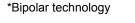
DESCRIPTION

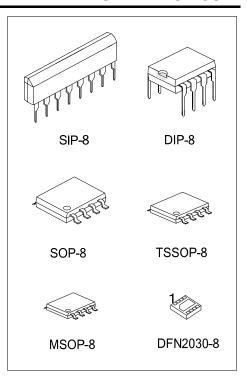
The UTC MC4580 is the dual operational amplifier, specially designed for improving the tone control, which is most suitable for the audio application.

Featuring noiseless, higher gain bandwidth, high output current and low distortion ratio, and it is most suitable not only for acoustic electronic parts of audio pre-amp and active filter, but also for the industrial measurement tools. It is also suitable for the head phone amp at higher output current, and further more, it can be applied for the handy type set operational amplifier of general purpose in application of low voltage single supply type which is properly biased of the input low voltage source.

FEATURES

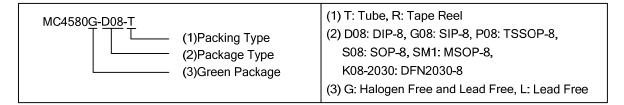
*Operating voltage $(\pm 2V \sim \pm 18V)$ *Low input noise voltage (0.8µVrms typ.) *Wide gain bandwidth product (15MHz typ.) *Low distortion (0.0005% typ.) *Slew rate $(5V/\mu s typ.)$





ORDERING INFORMATION

Ordering Number		Dookogo	Dooking	
Lead Free	Halogen Free	Package	Packing	
MC4580L-D08-T	MC4580G-D08-T	DIP-8	Tube	
MC4580L-G08-T	MC4580G-G08-T	SIP-8	Tube	
MC4580L-S08-R	MC4580G-S08-R	SOP-8	Tape Reel	
MC4580L-P08-R	MC4580G-P08-R	TSSOP-8	Tape Reel	
MC4580L-SM1-R	MC4580G-SM1-R	MSOP-8	Tape Reel	
MC4580L-K08-2030-R	MC4580G-K08-2030-R	DFN2030-8	Tape Reel	

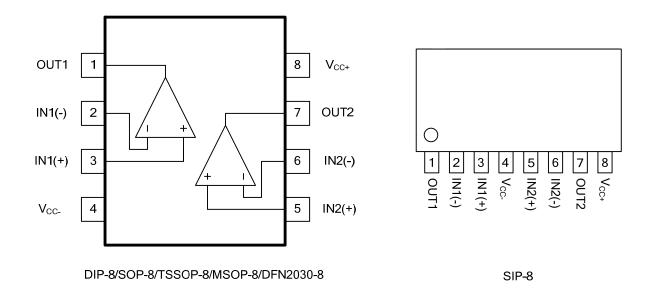


www.unisonic.com.tw 1 of 7

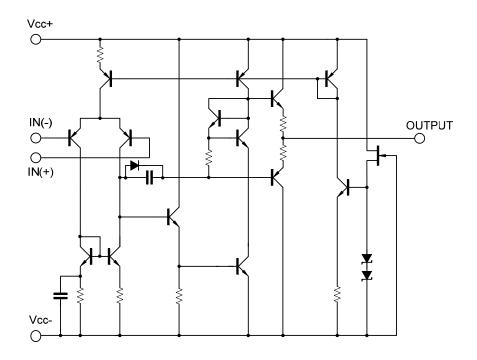
■ MARKING

PACKAGE	MARKING			
DIP-8	Date Code UTC			
SIP-8	Data Code L: Lead Free G: Halogen Free Lot Code 12345678			
SOP-8	B 7 6 5 UTC DDDD MC4580 Date Code L: Lead Free G: Halogen Free Lot Code			
MSOP-8	Date Code UTC CODE MC4580 CHalogen F Lot Code 1 2 3 4			
TSSOP-8	Date Code UTC GOOD 7 MC4580 G: Halogen Free Lot Code			
DFN2030-8	MC 4580 ◆ □□□□ → Date Code			

■ PIN CONFIGURATION



■ TEST CIRCUIT



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C)

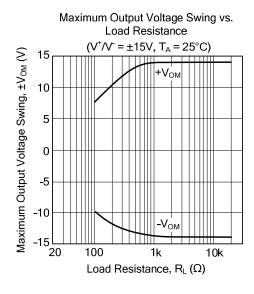
PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage		V ⁺ /V ⁻	±18	V
Input Voltage		V_{IN}	±15	V
Differential Input Voltage		$V_{I(DIFF)}$	±30	V
Output Current		I _{OUT}	±50	mA
	DIP-8 SIP-8	P□	750	mW
Davies Dissination	SOP-8		440	
Power Dissipation	TSSOP-8		360	
	MSOP-8		300	
	DFN2030-8		1300	
Junction Temperature		TJ	+125	°C
Operating Temperature		T_OPR	-40 ~ +85	°C
Storage Temperature		T _{STG}	-40 ~ +125	°C

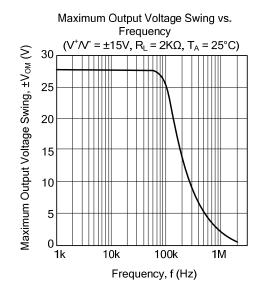
Note Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

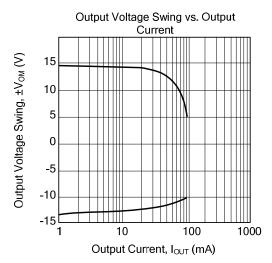
■ ELECTRICAL CHARACTERISTICS (V+ /V-=±15V, T_A=25°C)

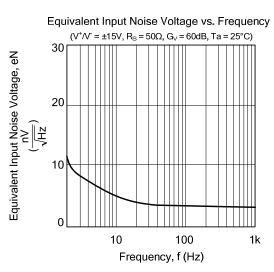
PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Input Offset Voltage	$V_{I(OFF)}$	$R_S \leq 10k\Omega$		0.5	3	mV
Input Offset Current	I _{I(OFF)}			5	200	nA
Input Bias Current	I _{I(BIAS)}			100	500	nA
Large Signal Voltage Gain	Gv	V_{OUT} =±10V, $R_L \ge 2k\Omega$	90	110		dB
Output Voltage Swing	V_{OM}	$R_L \ge 2k\Omega$	±12	±13.5		V
Input Common Mode Voltage	$V_{I(CM)}$		±12	±13.5		V
Common Mode Rejection Ratio	CMRR	$R_S \leq 10k\Omega$	80	110		dB
Supply Voltage Rejection Ratio	SVR	$Rs \le 10k\Omega$	80	110		dB
Operating Current	Icc			6	9	mA
Slew Rate	SR	$R_L \ge 2k\Omega$		5		V/µs
Gain bandwidth Product	GB	f=10KHz		15		MHz
Total Harmonic Distortion	THD	Gv=20dB, V_{OUT} =5V, R_L =2k Ω , f=1KHz		0.0005		%
Input Noise Voltage	eN	RIAA Rs=2.2 kΩ, 30kHzLPF		0.8		μVrms

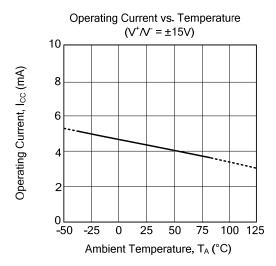
■ TYPICAL CHARACTERISTICS

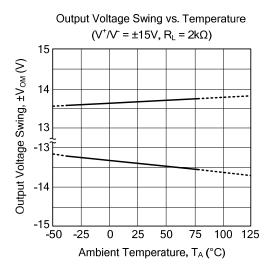




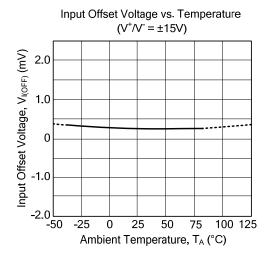


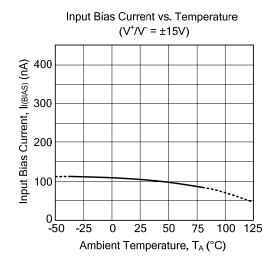


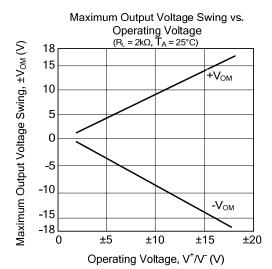


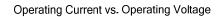


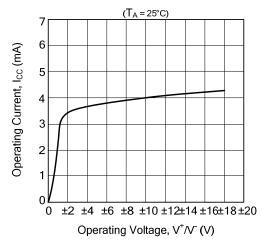
■ TYPICAL CHARACTERISTICS(Cont.)

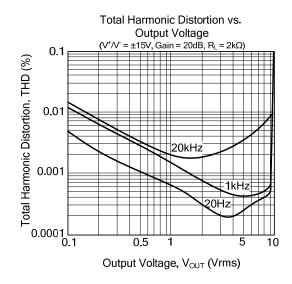


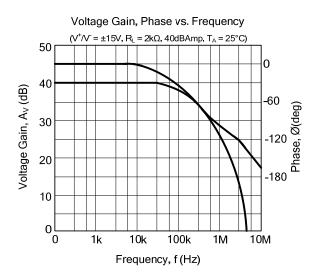












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