# MGSF1N02L, MVGSF1N02L

# MOSFET - Power: 750 mAmps, 20 Volts

# N-Channel SOT-23

These miniature surface mount MOSFETs low  $R_{DS(on)}$  assure minimal power loss and conserve energy, making these devices ideal for use in space sensitive power management circuitry. Typical applications are dc–dc converters and power management in portable and battery–powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

### Features

- Low R<sub>DS(on)</sub> Provides Higher Efficiency and Extends Battery Life
- Miniature SOT-23 Surface Mount Package Saves Board Space
- MVGSF Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable\*
- These Devices are Pb-Free and are RoHS Compliant

### **MAXIMUM RATINGS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise noted)

| Rating   | Symbol                            | Value       | Unit |
|--|-----------------------------------|-------------|------|
| Drain-to-Source Voltage  | V <sub>DSS</sub>                  | 20          | Vdc  |
| Gate-to-Source Voltage - Continuous  | V <sub>GS</sub>                   | ± 20        | Vdc  |
| Drain Current<br>– Continuous @ $T_A = 25^{\circ}C$<br>– Pulsed Drain Current ( $t_p \le 10 \ \mu s$ ) | I <sub>D</sub><br>I <sub>DM</sub> | 750<br>2000 | mA   |
| Total Power Dissipation @ $T_A = 25^{\circ}C$  | PD                                | 400         | mW   |
| Operating and Storage Temperature Range  | T <sub>J</sub> , T <sub>stg</sub> | – 55 to 150 | °C   |
| Thermal Resistance, Junction-to-Ambient  | $R_{\thetaJA}$                    | 300         | °C/W |
| Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds                         | ΤL                                | 260         | °C   |

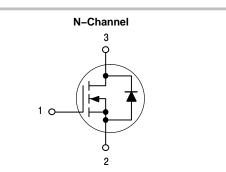
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



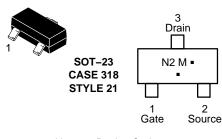
# **ON Semiconductor®**

www.onsemi.com

# 750 mAMPS, 20 VOLTS R<sub>DS(on)</sub> = 90 mΩ



MARKING DIAGRAM/ PIN ASSIGNMENT



N2 = Device Code

M = Date Code\*

= Pb–Free Package

(Note: Microdot may be in either location) \*Date Code orientation and overbar may vary depending upon manufacturing location.

### ORDERING INFORMATION

| Device         | Package             | Shipping <sup>†</sup> |
|----------------|---------------------|-----------------------|
| MGSF1N02LT1G   | SOT-23<br>(Pb-Free) | 3000 / Tape &<br>Reel |
| MVGSF1N02LT1G* | SOT-23<br>(Pb-Free) | 3000 / Tape &<br>Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MGSF1N02L, MVGSF1N02L

| Chai  | Symbol   | Min                 | Тур            | Max            | Unit |    |
|---|--|---------------------|----------------|----------------|------|----|
| OFF CHARACTERISTICS   |  |                     |                |                |      |    |
| Drain-to-Source Breakdown Voltage ( $V_{GS} = 0$ Vdc, $I_D = 10 \mu$ Adc)   | V <sub>(BR)DSS</sub>                               | 20                  | -              | -              | Vdc  |    |
| Zero Gate Voltage Drain Current<br>( $V_{DS} = 20$ Vdc, $V_{GS} = 0$ Vdc)<br>( $V_{DS} = 20$ Vdc, $V_{GS} = 0$ Vdc, $T_J = 0$   | I <sub>DSS</sub>                                   |                     |                | 1.0<br>10      | μAdc |    |
| Gate-Body Leakage Current (V <sub>GS</sub>  | I <sub>GSS</sub>                                   | -                   | -              | ±100           | nAdc |    |
| ON CHARACTERISTICS (Note 1)   |  |                     |                |                |      |    |
| Gate Threshold Voltage $(V_{DS} = V_{GS}, I_D = 250 \ \mu Adc)$   | V <sub>GS(th)</sub>                                | 1.0                 | 1.7            | 2.4            | Vdc  |    |
| $      Static Drain-to-Source On-Resista \\ (V_{GS} = 10 \ Vdc, \ I_D = 1.2 \ Adc) \\ (V_{GS} = 4.5 \ Vdc, \ I_D = 1.0 \ Adc) $ | r <sub>DS(on)</sub>                                |                     | 0.075<br>0.115 | 0.090<br>0.130 | Ω    |    |
| DYNAMIC CHARACTERISTICS   |  |                     |                |                |      |    |
| Input Capacitance   | (V <sub>DS</sub> = 5.0 Vdc)                        | C <sub>iss</sub>    | -              | 125            | -    | pF |
| Output Capacitance  | (V <sub>DS</sub> = 5.0 Vdc)                        | C <sub>oss</sub>    | -              | 120            | -    |    |
| Transfer Capacitance  | (V <sub>DG</sub> = 5.0 Vdc)                        | C <sub>rss</sub>    | -              | 45             | -    |    |
| SWITCHING CHARACTERISTICS   | (Note 2)   |                     |                |                |      |    |
| Turn-On Delay Time  |  | t <sub>d(on)</sub>  | _              | 2.5            | _    | ns |
| Rise Time   | $(V_{DD} = 15 \text{ Vdc}, I_D = 1.0 \text{ Adc},$ | t <sub>r</sub>      | -              | 1.0            | -    |    |
| Turn-Off Delay Time   | R <sub>L</sub> = 50 Ω)                             | t <sub>d(off)</sub> | -              | 16             | -    |    |
| Fall Time   |  | t <sub>f</sub>      | -              | 8.0            | -    |    |
| Gate Charge (See Figure 6)  |  | QT                  | -              | 6000           | -    | рС |
| SOURCE-DRAIN DIODE CHARAC   | TERISTICS  |                     |                |                |      |    |
| Continuous Current  | ۱ <sub>S</sub>                                     | -                   | -              | 0.6            | А    |    |
| Pulsed Current  | I <sub>SM</sub>                                    | -                   | -              | 0.75           | _    |    |
|   |  | 1                   |                | 1              | t    | 1  |

 Pulsed Current
 I<sub>SM</sub>
 0.75

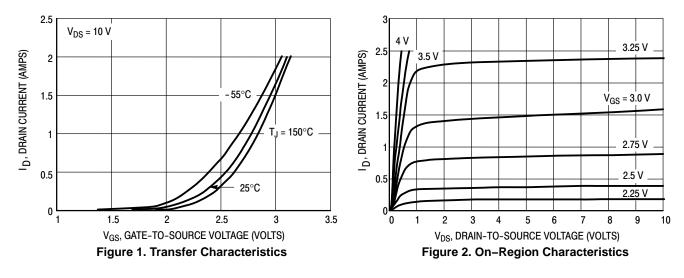
 Forward Voltage (Note 2)
 V<sub>SD</sub>
 0.8
 V

 Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product

performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2%.

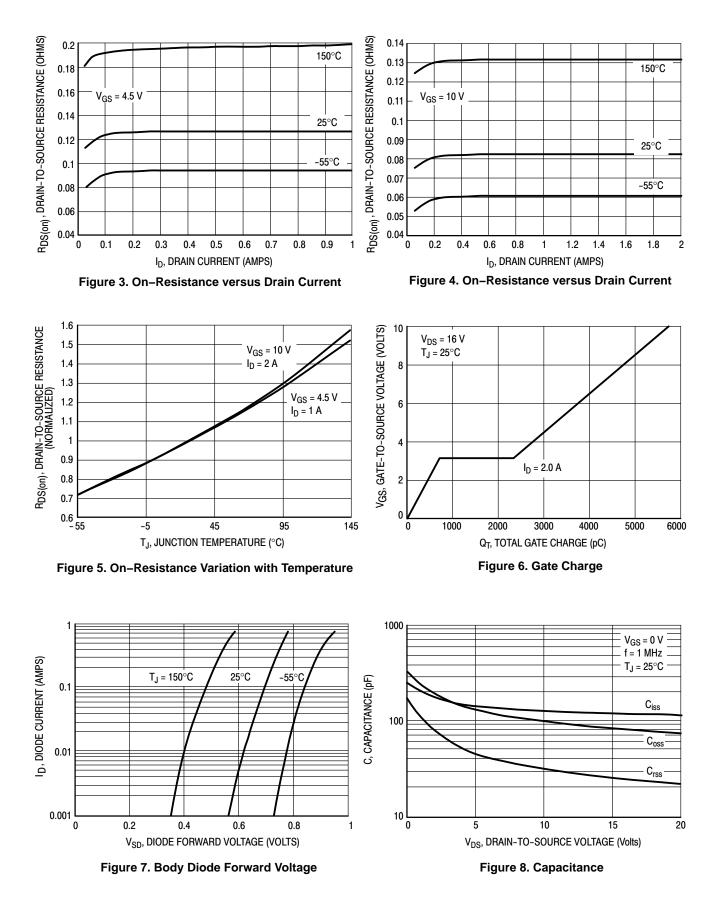
2. Switching characteristics are independent of operating junction temperature.



## **TYPICAL ELECTRICAL CHARACTERISTICS**

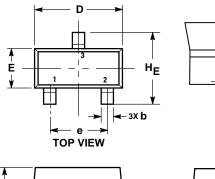
# MGSF1N02L, MVGSF1N02L

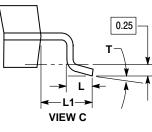
## TYPICAL ELECTRICAL CHARACTERISTICS

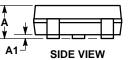


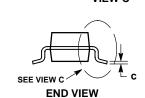
#### PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AR** 









NOTES:

- 1
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF З.
- THE BASE MATERIAL. 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS

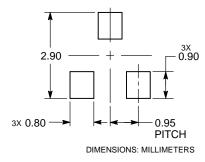
| PROTRUSIONS, OR GATE BURRS. |             |      |      |        |       |       |
|-----------------------------|-------------|------|------|--------|-------|-------|
|                             | MILLIMETERS |      |      | INCHES |       |       |
| DIM                         | MIN         | NOM  | MAX  | MIN    | NOM   | MAX   |
| Α                           | 0.89        | 1.00 | 1.11 | 0.035  | 0.039 | 0.044 |
| A1                          | 0.01        | 0.06 | 0.10 | 0.000  | 0.002 | 0.004 |
| b                           | 0.37        | 0.44 | 0.50 | 0.015  | 0.017 | 0.020 |
| c                           | 0.08        | 0.14 | 0.20 | 0.003  | 0.006 | 0.008 |
| D                           | 2.80        | 2.90 | 3.04 | 0.110  | 0.114 | 0.120 |
| Е                           | 1.20        | 1.30 | 1.40 | 0.047  | 0.051 | 0.055 |
| е                           | 1.78        | 1.90 | 2.04 | 0.070  | 0.075 | 0.080 |
| L                           | 0.30        | 0.43 | 0.55 | 0.012  | 0.017 | 0.022 |
| L1                          | 0.35        | 0.54 | 0.69 | 0.014  | 0.021 | 0.027 |
| HE                          | 2.10        | 2.40 | 2.64 | 0.083  | 0.094 | 0.104 |
| Т                           | 0°          |      | 10 ° | 0 °    |       | 10 °  |

STYLE 21: PIN 1. GATE

2. SOURCE

DRAIN 3.

RECOMMENDED SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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