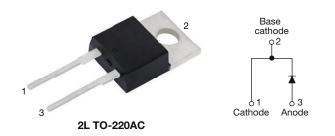
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# Hyperfast Rectifier, 8 A FRED Pt<sup>®</sup>



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	8 A				
V <sub>R</sub>	600 V				
V <sub>F</sub> at I <sub>F</sub>	1.3 V				
t <sub>rr</sub> (typ.)	16 ns				
T <sub>J</sub> max.	175 °C				
Package	2L TO-220AC				
Circuit configuration	Single				

### FEATURES

- Hyperfast soft recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- True 2 pin package
- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **DESCRIPTION / APPLICATIONS**

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V
Average rectified forward current in DC	I <sub>F(AV)</sub>	T <sub>C</sub> = 146 °C	8	
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	80	А
Repetitive peak surge current Square wave 20 kHz duty cycle (50 %)	I <sub>FRM</sub>	T <sub>C</sub> = 137 °C	16	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-		
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 8 A	-	2.0	2.65	V	
		I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	-	1.3	1.85		
De construction de la constructi		$V_{R} = V_{R}$ rated	-	0.02	12		
Reverse leakage current	IR	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	15	100	μA	
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	6	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH	

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## Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 100$	A/ $\mu$ s, V <sub>R</sub> = 30 V	-	16	23		
Reverse recovery time	+	$I_F = 8 \text{ A}, \ dI_F/dt = 100$	A/ $\mu$ s, V <sub>R</sub> = 30 V	-	20	28	20	
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	21	-	ns	
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 8 A, dI <sub>F</sub> /dt = 200 A/μs, V <sub>R</sub> = 390 V	-	39	-		
Peak recovery current		T <sub>J</sub> = 25 °C		-	3	-	A	
Feak recovery current	IRRM	T <sub>J</sub> = 125 °C		-	5	-		
	0	T <sub>J</sub> = 25 °C		-	36	-		
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	108	-	nC	
Reverse recovery time	t <sub>rr</sub>		I <sub>F</sub> = 8 A,	-	30	-	ns	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	$dI_F/dt = 600 A/\mu s$ ,	-	13	-	А	
Reverse recovery charge	Q <sub>rr</sub>		V <sub>R</sub> = 390 V	-	205	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C	
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	2.0	2.6		
Thermal resistance, junction-to-ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W	
Typical thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-		
Weight			-	2	-	g	
Weight			-	0.07	-	oz.	
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)	
Marking device		Case style 2L TO-220AC		ETH	0806		



# VS-ETH0806-M3

### **Vishay Semiconductors**

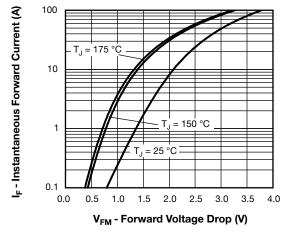


Fig. 1 - Typical Forward Voltage Drop Characteristics

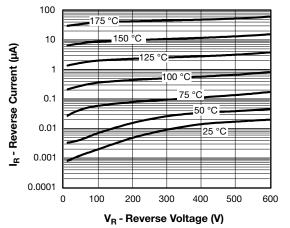


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

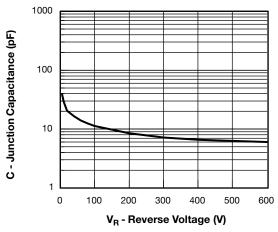


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

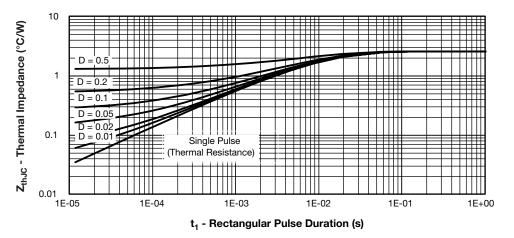
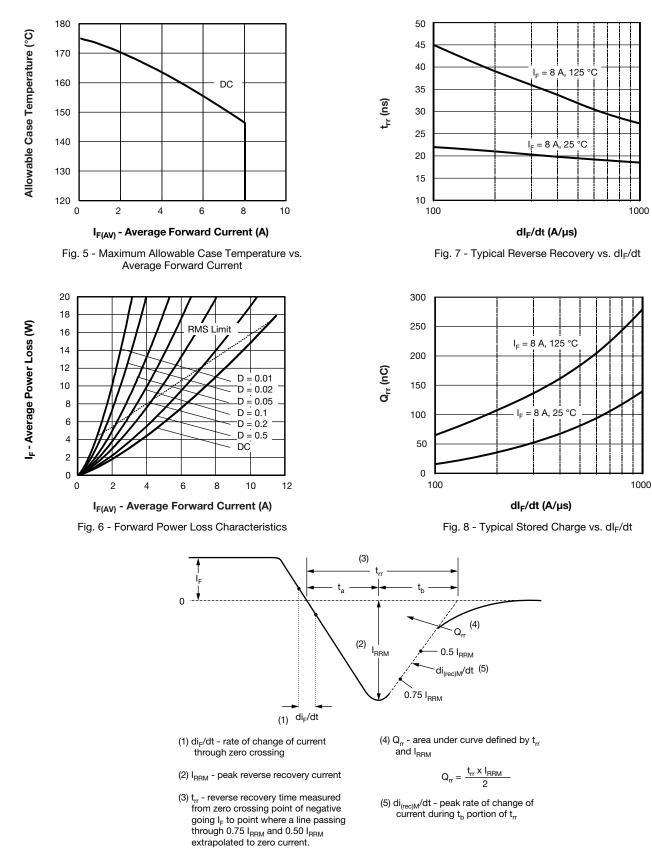


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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Fig. 9 - Reverse Recovery Waveform and Definitions

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### **ORDERING INFORMATION TABLE**

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VISHA

Device code	VS-	E	т	н	08	06	-M3
	V3-	<b>-</b>	<b>'</b>				
	1	2	3	4	5	6	7
	1	- Visl	nay Sem	niconduc	ctors pro	oduct	
	2	- Circ	uit conf	iguratior	ו:		
		E =	single				
	3	- T=	2L TO-2	220AC			
	4	- H=	hyperfa	ist recov	very time	е	
	5	- Cur	rent cod	le: 08 =	8 A		
	6	- Vol	tage coo	le: 06 =	600 V		
	7	- Env	rironmer	ntal digit	:		
		-M3	3 = halog	gen-free	, RoHS	-complia	ant, and

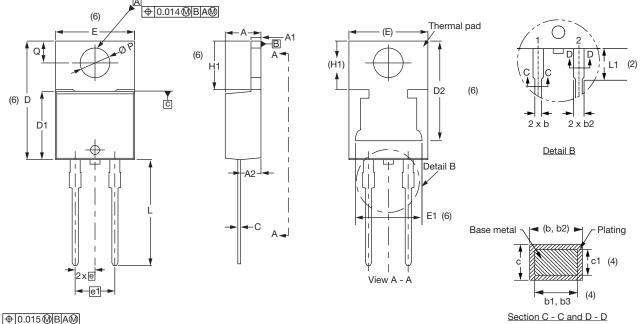
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-ETH0806-M3	50	1000	Antistatic plastic tube			

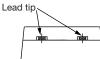
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96156			
Part marking information	www.vishay.com/doc?95391			



# 2L TO-220AC

#### **DIMENSIONS** in millimeters and inches





SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.50	2.92	0.098	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.35	0.585	0.604	3
D1	8.38	9.02	0.330	0.355	

Conforms to JEDEC®	outline	TO-220AC
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SYMBOL	MILLIN	IETERS	INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	11.68	13.30	0.460	0.524	6, 7
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØР	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

#### Notes

 $^{(1)}\,$  Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Lead dimension and finish uncontrolled in L1

<sup>(4)</sup> Dimension b1, b3, and c1 apply to base metal only

(5) Controlling dimensions: inches

- <sup>(6)</sup> Thermal pad contour optional within dimensions E, H1, D2, and E1
- <sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> TO-220, except D2

Revision: 13-Jun-2019

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<sup>&</sup>lt;sup>(3)</sup> Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body



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