

# NCE40P07S

#### NCE P-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE40P07S uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

V<sub>DS</sub> =-40V,I<sub>D</sub> =-6.2A

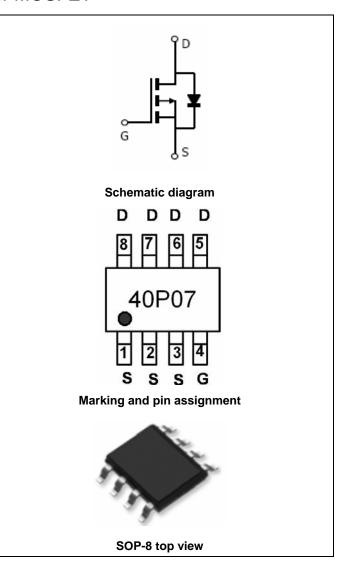
 $R_{DS(ON)}$  <25m $\Omega$  @  $V_{GS}$ =-10V

 $R_{DS(ON)}$  <30m $\Omega$  @  $V_{GS}$ =-4.5V

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

#### **Application**

- Power switching application
- Hard switched and high frequency circuits
- DC-DC converter



#### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
40P07	NCE40P07S	SOP-8	Ø330mm	12mm	2500 units

### Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Abootate maximum reatings (TA=25 Camoos Carot vice noted)						
Symbol	Limit	Unit				
V <sub>DS</sub>	-40	V				
V <sub>GS</sub>	±20	V				
I <sub>D</sub>	-6.2	А				
I <sub>D</sub> (100℃)	-4	Α				
I <sub>DM</sub>	40	Α				
P <sub>D</sub>	2.5	W				
$T_{J}$ , $T_{STG}$	-55 To 150	$^{\circ}$ C				
	Symbol  VDS  VGS  ID  ID(100°C)  IDM  PD	Symbol         Limit           VDS         -40           VGS         ±20           ID         -6.2           ID(100°C)         -4           IDM         40           PD         2.5				



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#### **Thermal Characteristic**

Thermal Resistance ,Junction-to-Ambient <sup>(Note 2)</sup>	$R_{ heta JA}$	50	°C/W
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## Electrical Characteristics (T<sub>A</sub>=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-40	-	-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-40V,V <sub>GS</sub> =0V	-	-	1	μA	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-1.1	-1.7	-2.5	V	
Drain-Source On-State Resistance	В	V <sub>GS</sub> =-10V, I <sub>D</sub> =-5A	V I <sub>D</sub> =-250μA -40 -40 -40 -40 -40 -40 -40 -40 -40 -40	16	25	mΩ	
Diam-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5A		21	30	mΩ	
Forward Transconductance	<b>g</b> FS	$V_{DS}$ =-5 $V$ , $I_{D}$ =-5 $A$	20	-	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	C <sub>lss</sub>	V <sub>DS</sub> =-20V,V <sub>GS</sub> =0V,	-	1750	-	PF	
Output Capacitance	C <sub>oss</sub>		-	215	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>	r-1.UIVIAZ	-	180	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t <sub>d(on)</sub>		-	9	-	nS	
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-20V, , $R_L$ =2 $\Omega$	-	8	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10 $V$ , $R_{GEN}$ =3 $\Omega$	-	28	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	10	-	nS	
Total Gate Charge	Qg	V - 20VI - FA	-	24	-	nC	
Gate-Source Charge	Q <sub>gs</sub>		-	3.5	-	nC	
Gate-Drain Charge	$Q_{gd}$			6	-	nC	
Drain-Source Diode Characteristics			-			-	
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	$V_{GS}$ =0 $V$ , $I_{S}$ =-6 $A$	-	-	1.2	V	
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	-6.2	Α	

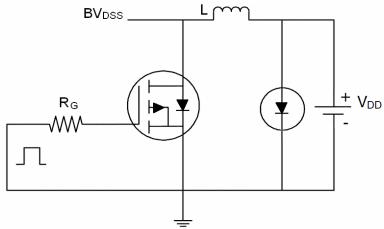
### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse Test: Pulse Width ≤  $300\mu$ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

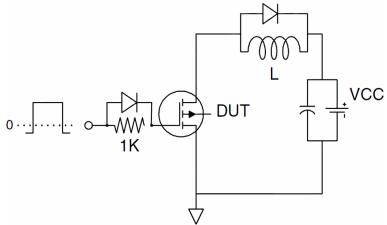


### **Test Circuit**

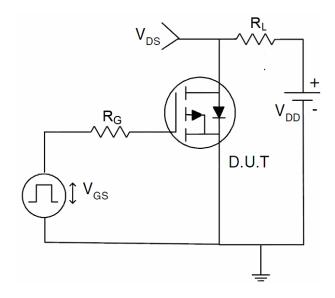
## 1) E<sub>AS</sub> Test Circuit



## 2) Gate Charge Test Circuit

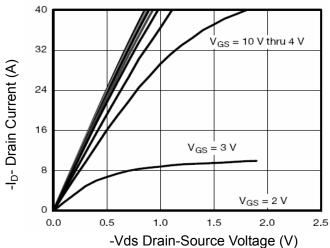


## 3) Switch Time Test Circuit

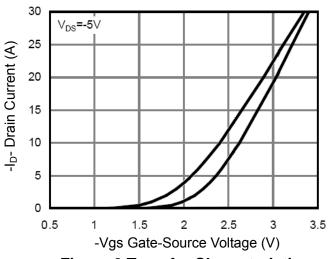




### Typical Electrical and Thermal Characteristics (Curves)



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

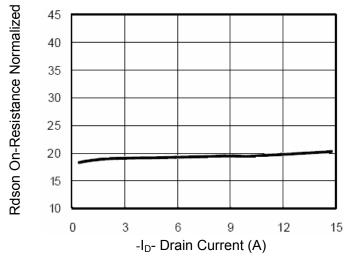
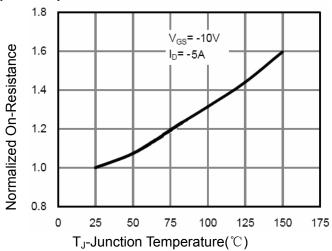


Figure 3 Rdson- Drain Current



**Figure 4 Rdson-Junction Temperature** 

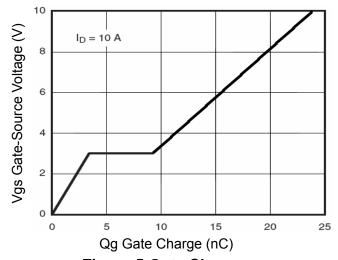


Figure 5 Gate Charge

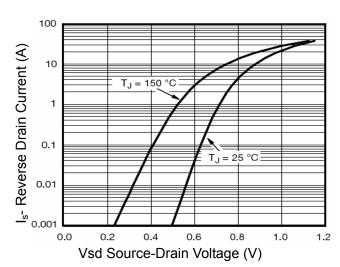


Figure 6 Source- Drain Diode Forward



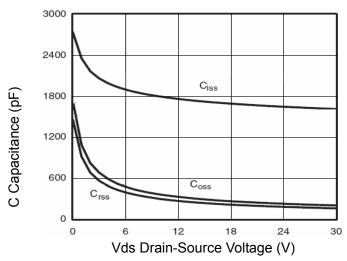


Figure 7 Capacitance vs Vds

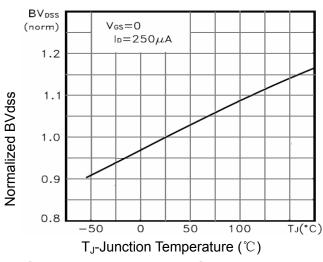
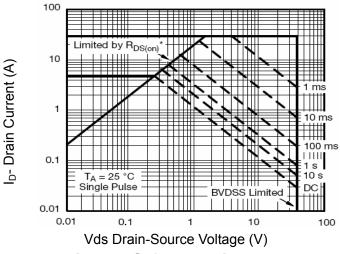


Figure 9 BV<sub>DSS</sub> vs Junction Temperature



**Figure 8 Safe Operation Area** 

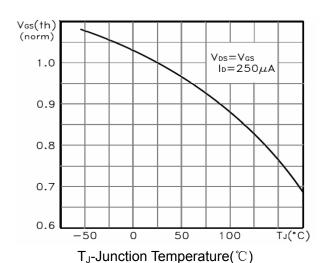
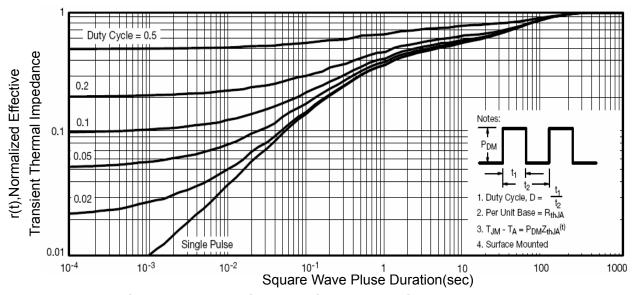


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

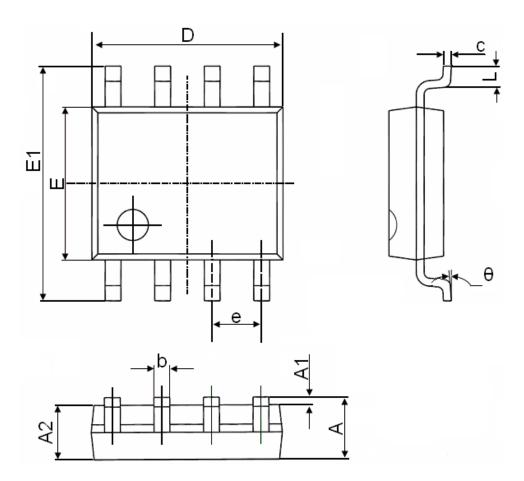


**Figure 11 Normalized Maximum Transient Thermal Impedance** 

**Pb Free Product** 



# **SOP-8 Package Information**



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	1.270(BSC)		(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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