



CS12N06 AE-G

General Description:

CS12N06 AE-G, the silicon N-channel Enhanced VDMOSFETs, is obtained by the high density Trench technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is SOP-8, which accords with the RoHS standard.

Features:

- | **Fast Switching**
- | **Low ON Resistance**
- | **Low Gate Charge**
- | **Low Reverse transfer capacitances**
- | **100% Single Pulse avalanche energy Test**
- | **Halogen free**

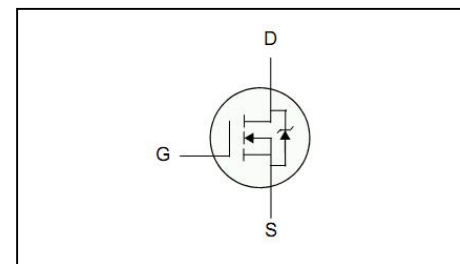
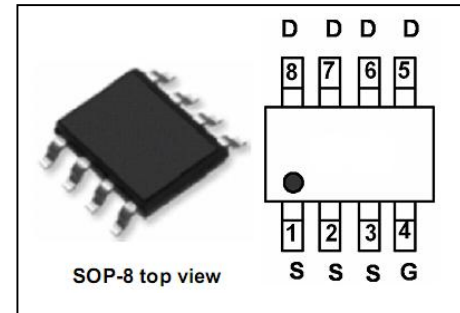
Applications:

Power switch circuit of adaptor and charger.

Absolute (T_A= 25°C unless otherwise specified):

Symbol	Parameter	Rating	Units
V _{DSS}	Drain-to-Source Voltage	60	V
I _D	Continuous Drain Current	12	A
	Continuous Drain Current T _A = 100 °C	8	A
I _{DM} ^{a1}	Pulsed Drain Current	48	A
V _{GS}	Gate-to-Source Voltage	±20	V
E _{AS} ^{a2}	Single Pulse Avalanche Energy	135	mJ
P _D	Power Dissipation	3.2	W
T _J , T _{stg}	Operating Junction and Storage Temperature Range	150, -55 to 150	°C

V _{DSS}	60	V
I _D (Silicon limited current)	12	A
P _D (T _C =25°C)	3.2	W
R _{DS(ON)Typ}	10.5	mΩ



Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise specified):

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	--	--	V
I_{DSS}	Drain to Source Leakage Current	$V_{DS} = 60V, V_{GS} = 0V,$ $T_a = 25^\circ\text{C}$	--	--	1	μA
		$V_{DS} = 48V, V_{GS} = 0V,$ $T_a = 125^\circ\text{C}$	--	--	100	
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS} = +20V$	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS} = -20V$	--	--	-100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=10V, I_D=12A$	--	10.5	13.5	$m\Omega$
		$V_{GS}=4.5V, I_D=9A$		13	16	$m\Omega$
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.9	1.4	1.9	V
Pulse width $t_p \leq 300\mu s, \delta \leq 2\%$						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
C_{iss}	Input Capacitance	$V_{DS}=30V, V_{GS}=0V,$ $f=1.0MHz$	--	2370	--	pF
C_{oss}	Output Capacitance		--	164	--	
C_{rss}	Reverse Transfer Capacitance		--	123	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD} = 30V, I_D = 10A,$ $R_G = 3\Omega, V_{GS} = 10V$	--	13.1	--	ns
t_r	Rise Time		--	25.1	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	60.8	--	
t_f	Fall Time		--	9.0	--	
Q_g	Total Gate Charge	$V_{DS}=30V, I_D=12A,$ $V_{GS}=10V$	--	50.7		nC
Q_{gs}	Gate to Source Charge		--	7.0	--	
Q_{gd}	Gate to Drain ("Miller") Charge		--	12.3	--	



Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V_{SD}	Diode Forward Voltage	$I_S=12A, V_{GS}=0V$	--	--	1.2	V
T_{rr}	Reverse Recovery Time	$V_{GS}=0V, I_S=12A,$	--	22		ns
Q_{rr}	Reverse Recovery Charge	$di/dt=100A/us$	--	19.5		nC
Pulse width $t_p \leq 300\mu s, \delta \leq 2\%$						

Symbol	Parameter	Max.	Units
$R_{\theta JA}$	Junction-to-Ambient	40	$^{\circ}C/W$

^{a1}: Repetitive rating; pulse width limited by maximum junction temperature

^{a2}: $V_{dd}=25V, L=1mH, I_D=17A, Start T_J=25^{\circ}C$

^{a3}: Recommend soldering temperature defined by IPC/JEDEC J-STD 020

Characteristics Curve:

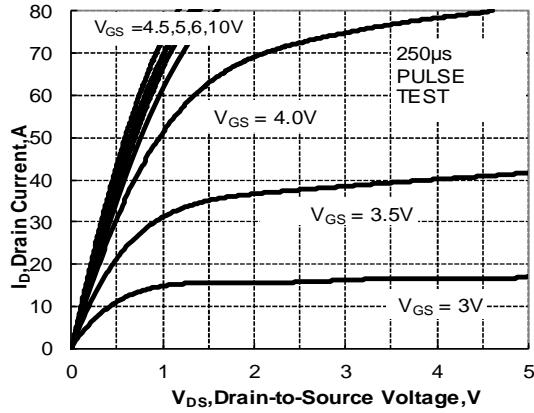


Figure 1. Output Characteristics

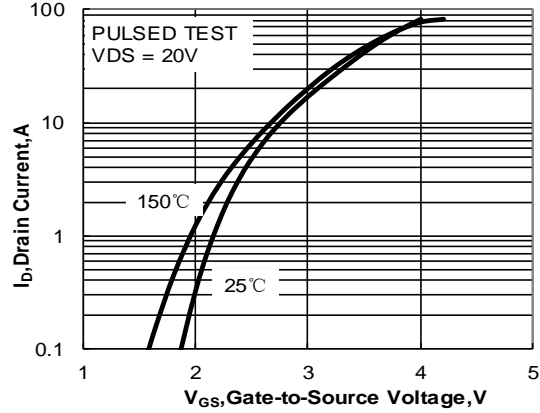


Figure 2. Transfer Characteristics

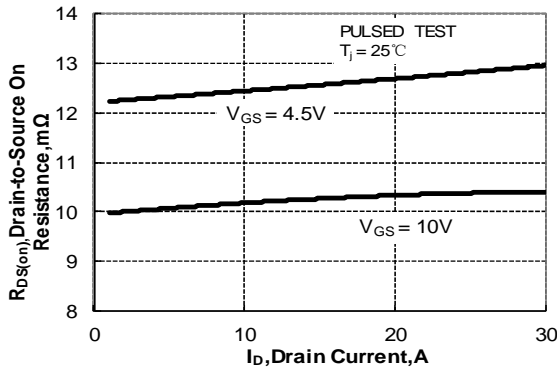


Figure 3. Drain-to-Source On Resistance vs Drain Current

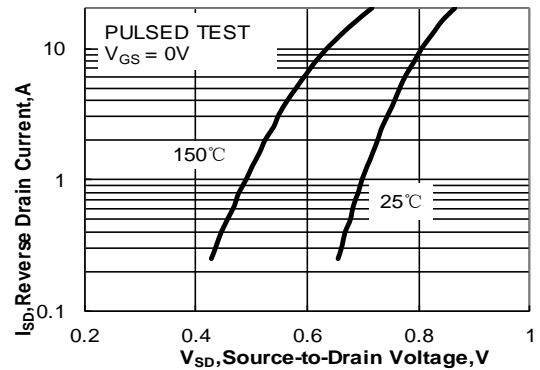


Figure 4. Typical Body Diode Transfer Characteristics

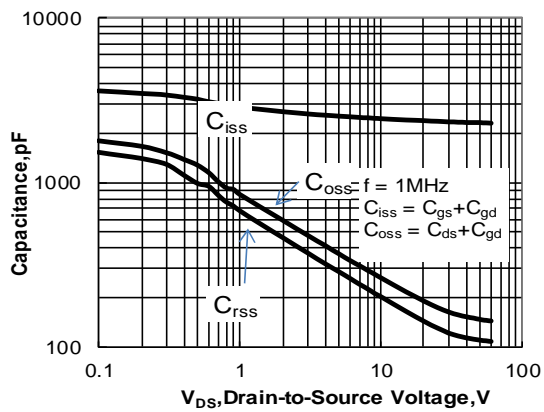


Figure 5. Capacitance Characteristics

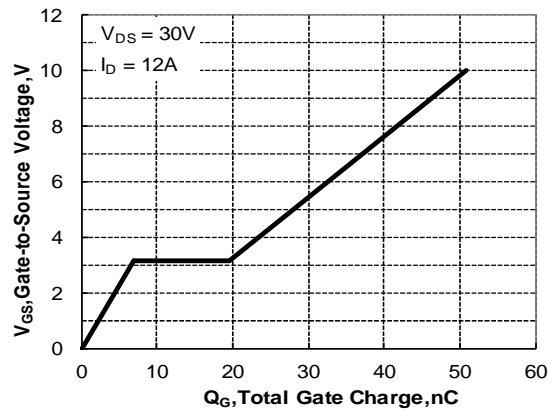


Figure 6. Gate Charge Characteristics

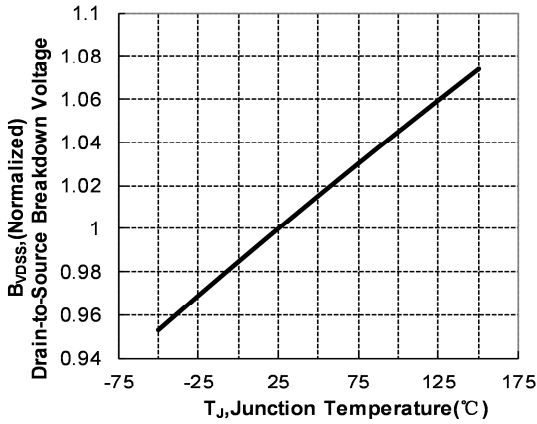


Figure 7. Normalized Breakdown Voltage vs Junction Temperature

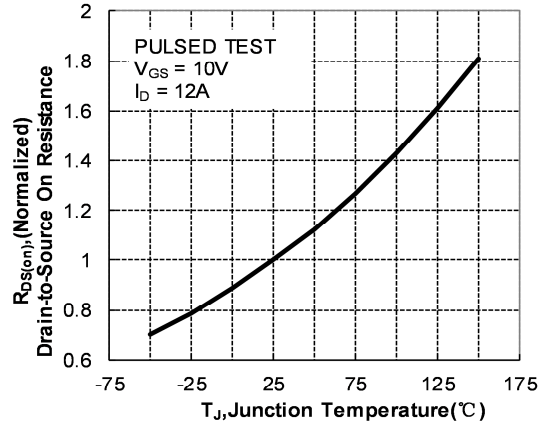


Figure 8. Normalized On Resistance vs Junction Temperature

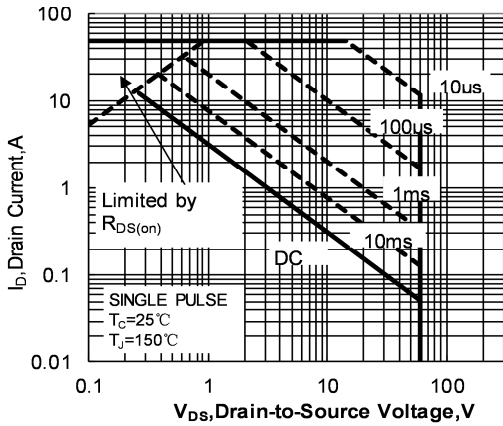


Figure 9. Maximum Safe Operating

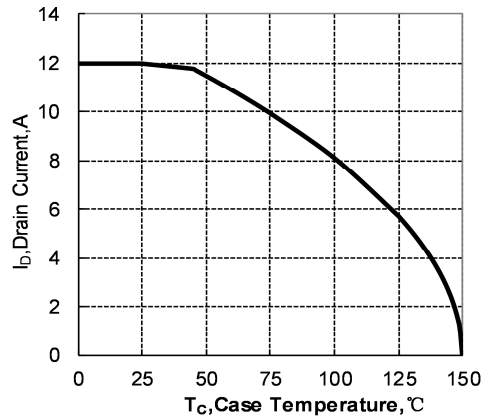


Figure 10. Maximum Continuous Drain Current vs Case Temperature

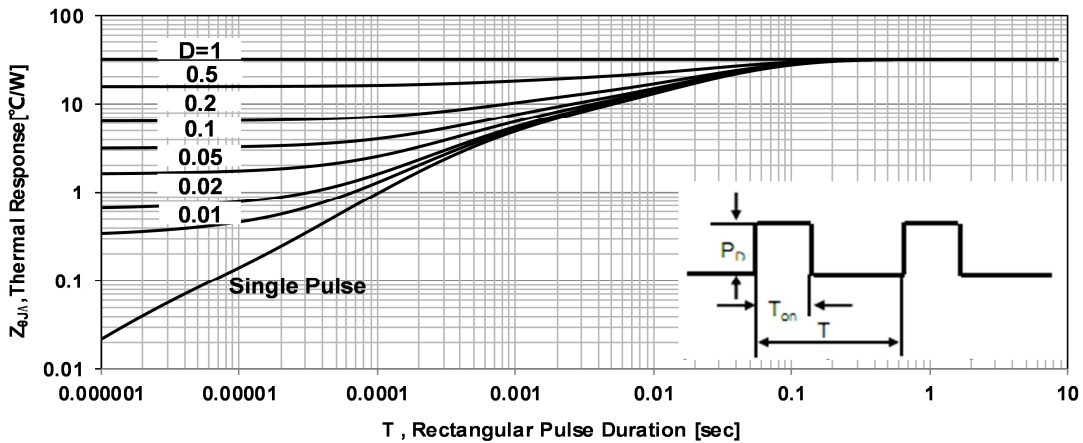


Figure 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

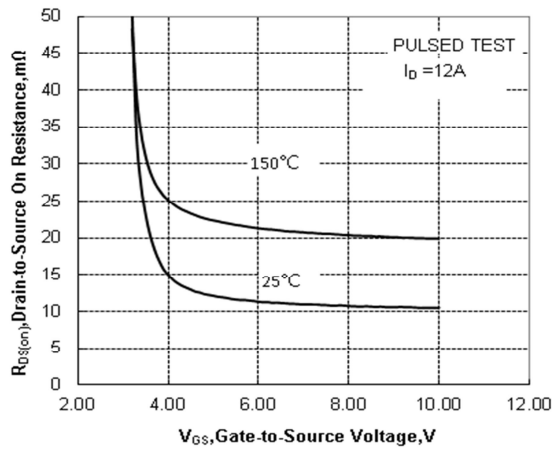


Figure 12. Drain-to-Source On Resistance vs Gate Voltage and Drain Current

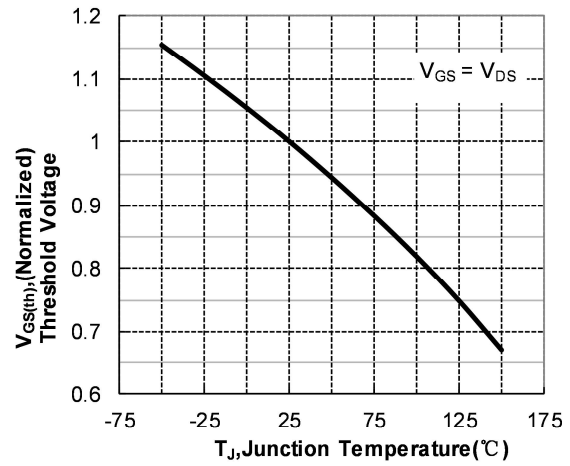


Figure 13. Normalized Threshold Voltage vs Junction Temperature

Test Circuit and Waveform

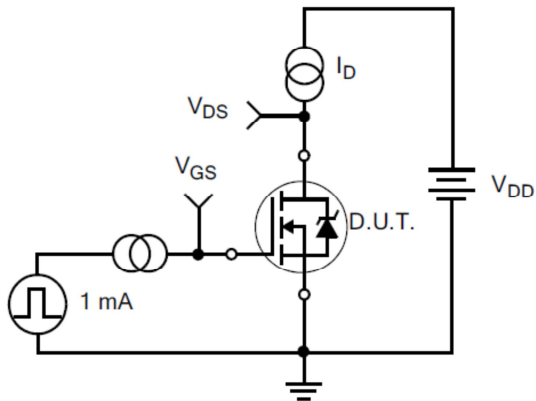


Figure 14. Gate Charge Test Circuit

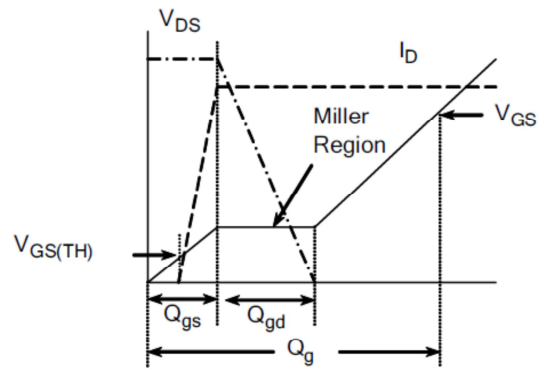


Figure 15. Gate Charge Waveforms

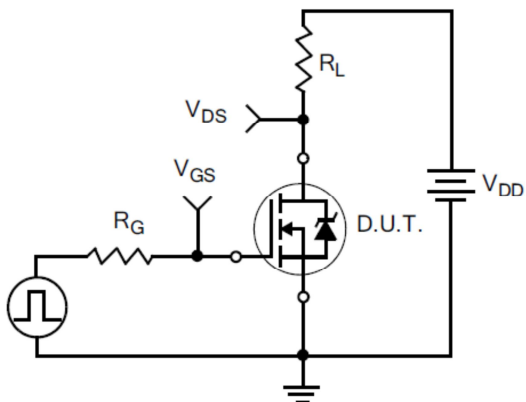


Figure 16. Resistive Switching Test Circuit

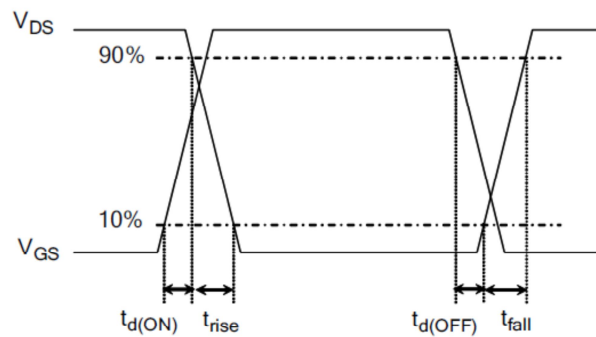


Figure 17. Resistive Switching Waveforms

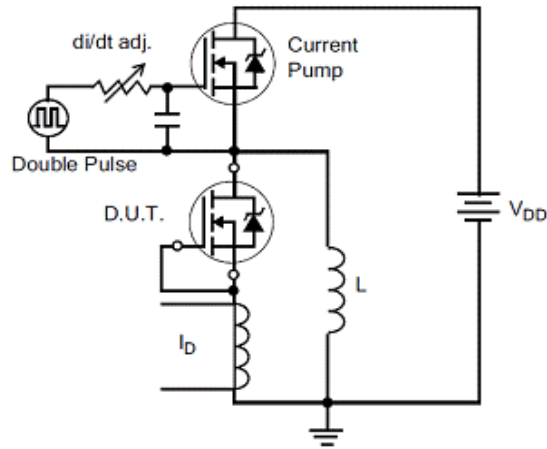


Figure 18. Diode Reverse Recovery Test Circuit

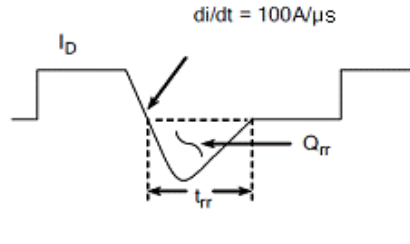


Figure 19. Diode Reverse Recovery Waveform

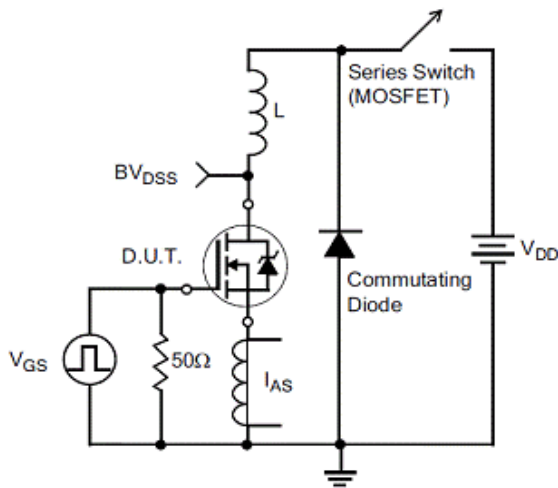


Figure20.Unclamped Inductive Switching Test Circuit

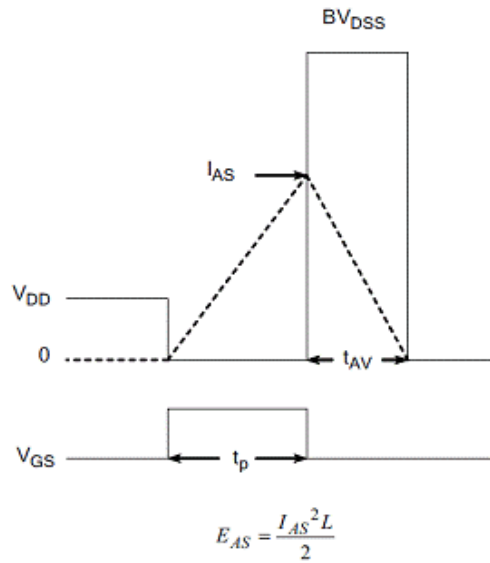
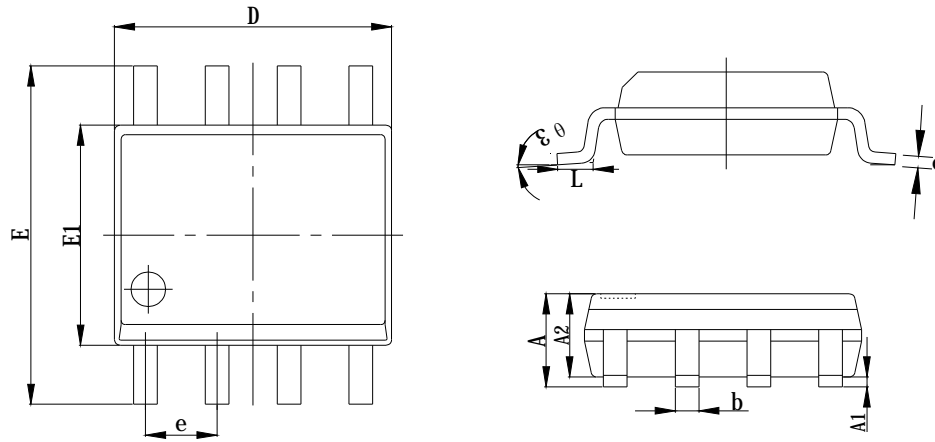


Figure21.Unclamped Inductive Switching Waveform

Package Information:



Items	Values(mm)	
	MIN	MAX
A	1.30	1.80
A1	0.10	0.25
A2	1.30	1.50
E	5.80	6.20
E1	3.80	4.00
D	4.80	5.00
L	0.40	0.90
e	1.27TYP	
b	0.37	0.47
c	0.20 TYP	
θ3	0°	8°

SOP-8Package

