



General Description:

CS12N06 AE-G, the silicon N-channel Enhanced VDMOSFETs, is obtained by the high density Trenchtechnology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is SOP-8, which accords with the RoHS standard.

Features:

- | Fast Switching
- | Low ON Resistance
- | Low Gate Charge
- | Low Reverse transfer capacitances
- | 100% Single Pulse avalanche energy Test
- | Halogen free

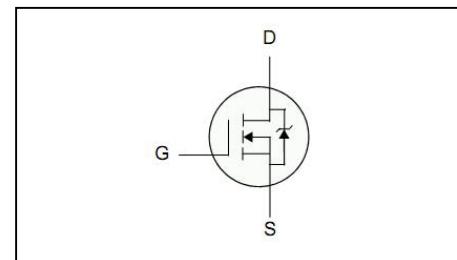
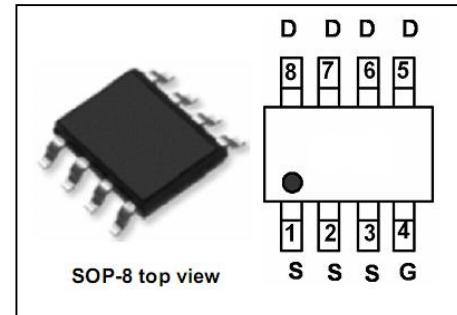
Applications:

Power switch circuit of adaptor and charger.

Absolute ($T_A = 25^\circ\text{C}$ unless otherwise specified):

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-to-Source Voltage	60	V
I_D	Continuous Drain Current	12	A
	Continuous Drain Current $T_A = 100^\circ\text{C}$	8	A
I_{DM}^{a1}	Pulsed Drain Current	48	A
V_{GS}	Gate-to-Source Voltage	± 20	V
E_{AS}^{a2}	Single Pulse Avalanche Energy	135	mJ
P_D	Power Dissipation	3.2	W
T_J, T_{stg}	Operating Junction and Storage Temperature Range	150, -55 to 150	°C

V_{DSS}	60	V
I_D (Silicon limited current)	12	A
$P_D(T_C=25^\circ\text{C})$	3.2	W
$R_{DS(ON)}^{\text{Typ}}$	10.5	mΩ



**Electrical Characteristics (T_A = 25°C unless otherwise specified):**

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V _{DSS}	Drain to Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	60	--	--	V
I _{DSS}	Drain to Source Leakage Current	V _{DS} =60V, V _{GS} =0V, T _a = 25°C	--	--	1	μA
		V _{DS} =48V, V _{GS} =0V, T _a = 125°C	--	--	100	
I _{GSS(F)}	Gate to Source Forward Leakage	V _{GS} =+20V	--	--	100	nA
I _{GSS(R)}	Gate to Source Reverse Leakage	V _{GS} =-20V	--	--	-100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R _{DS(ON)}	Drain-to-Source On-Resistance	V _{GS} =10V, I _D =12A	--	10.5	13.5	mΩ
		V _{GS} =4.5V, I _D =9A		13	16	mΩ
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	0.9	1.4	1.9	V
Pulse width t _p ≤300μs, δ ≤2%						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
C _{iss}	Input Capacitance	V _{DS} =30V, V _{GS} =0V, f=1.0MHz	--	2370	--	pF
C _{oss}	Output Capacitance		--	164	--	
C _{rss}	Reverse Transfer Capacitance		--	123	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =30V, I _D =10A, R _G = 3Ω, V _{GS} =10V	--	13.1	--	ns
tr	Rise Time		--	25.1	--	
t _{d(OFF)}	Turn-Off Delay Time		--	60.8	--	
t _f	Fall Time		--	9.0	--	
Q _g	Total Gate Charge	V _{DS} =30V, I _D =12A, V _{GS} =10V	--	50.7	--	nC
Q _{gs}	Gate to Source Charge		--	7.0	--	
Q _{gd}	Gate to Drain ("Miller")Charge		--	12.3	--	

**Source-Drain Diode Characteristics**

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V _{SD}	Diode Forward Voltage	I _S =12A, V _{GS} =0V	--	--	1.2	V
T _{rr}	Reverse Recovery Time	V _{GS} =0V, I _S =12A,	--	22		ns
Q _{rr}	Reverse Recovery Charge	di/dt=100A/us	--	19.5		nC
Pulse width tp≤300μs, δ ≤2%						

Symbol	Parameter	Max.	Units
R _{θ JA}	Junction-to-Ambient	40	°C/W

^{a1}: Repetitive rating; pulse width limited by maximum junction temperature

^{a2}: Vdd=25V, L=1mH, I_D=17A, Start T_j=25°C

^{a3}: Recommend soldering temperature defined by IPC/JEDEC J-STD 020

Characteristics Curve:

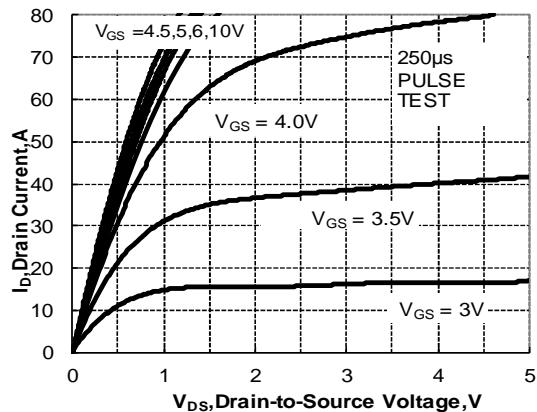


Figure 1. Output Characteristics

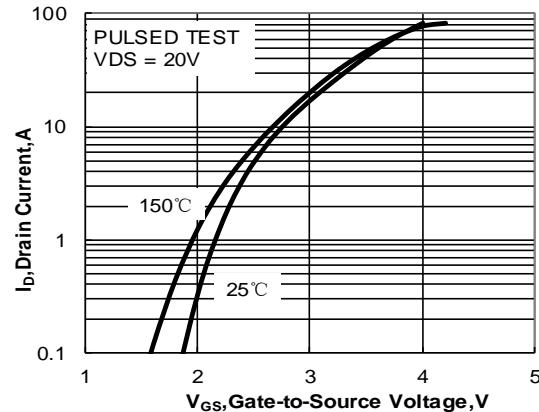


Figure 2. Transfer Characteristics

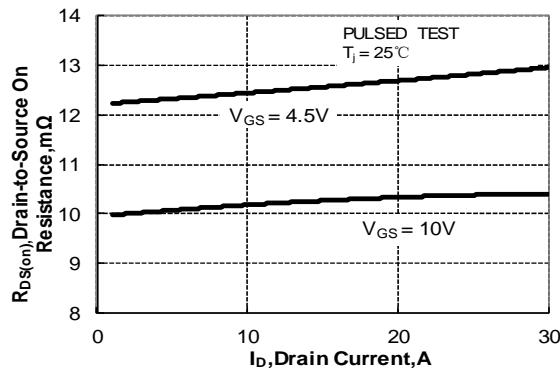


Figure 3. Drain-to-Source On Resistance vs Drain Current

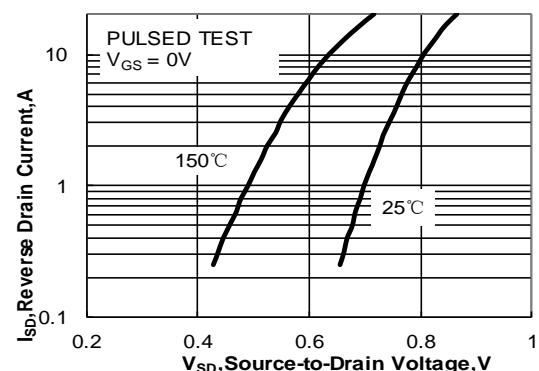


Figure 4. Typical Body Diode Transfer Characteristics

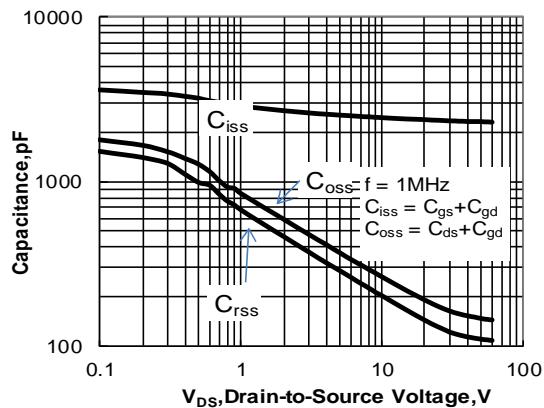


Figure 5. Capacitance Characteristics

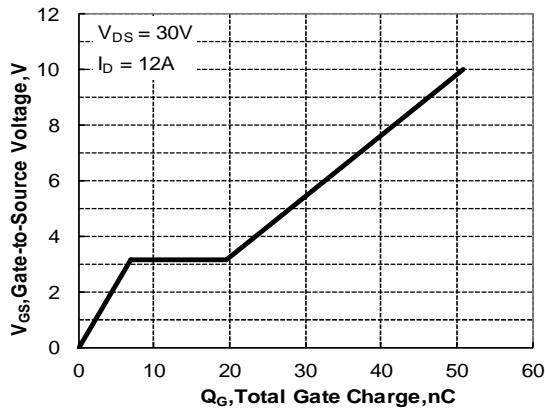


Figure 6. Gate Charge Characteristics

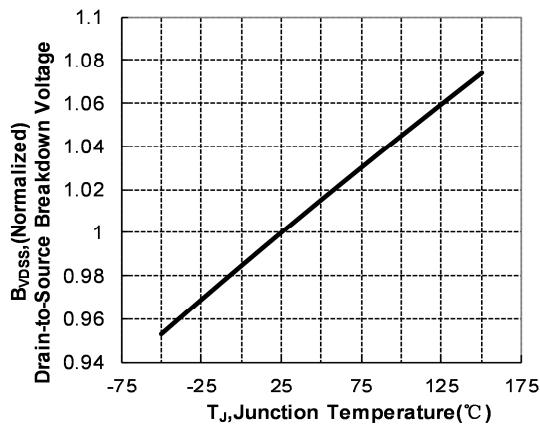


Figure 7. Normalized Breakdown Voltage vs Junction Temperature

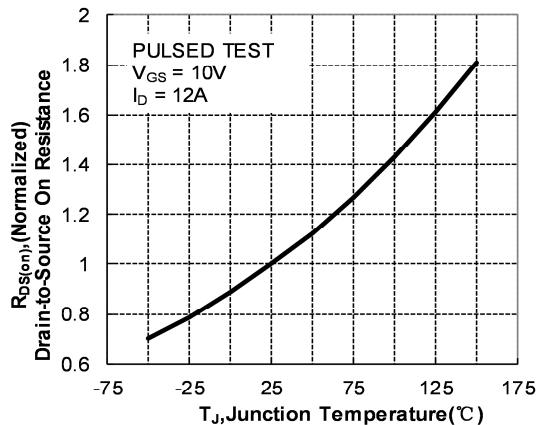


Figure 8. Normalized On Resistance vs Junction Temperature

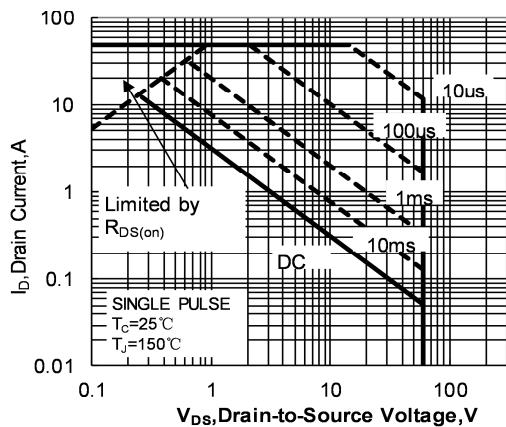


Figure 9. Maximum Safe Operating

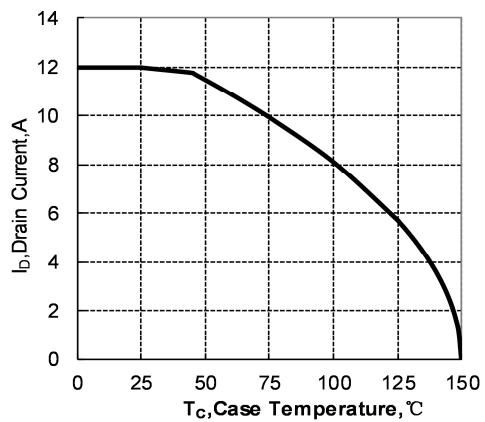


Figure 10. Maximum Continuous Drain Current vs Case Temperature

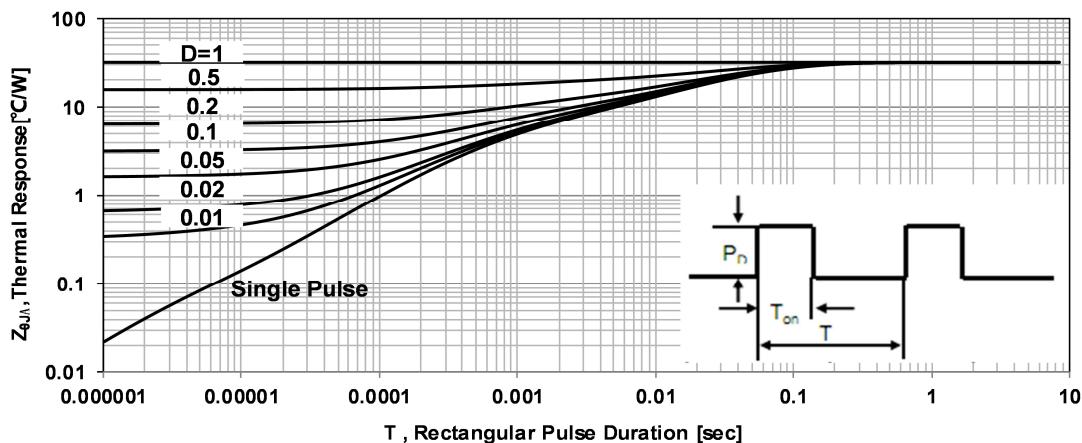


Figure 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

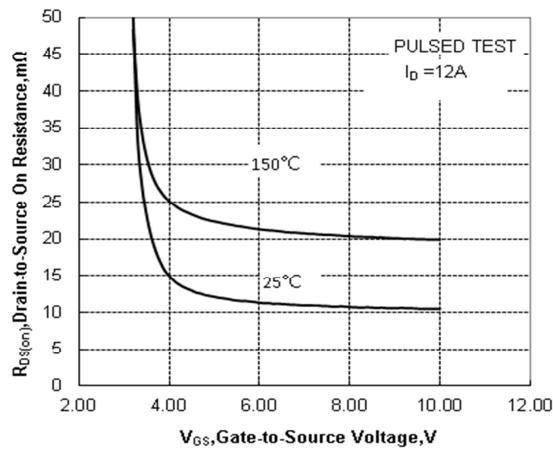


Figure 12. Drain-to-Source On Resistance vs Gate Voltage and Drain Current

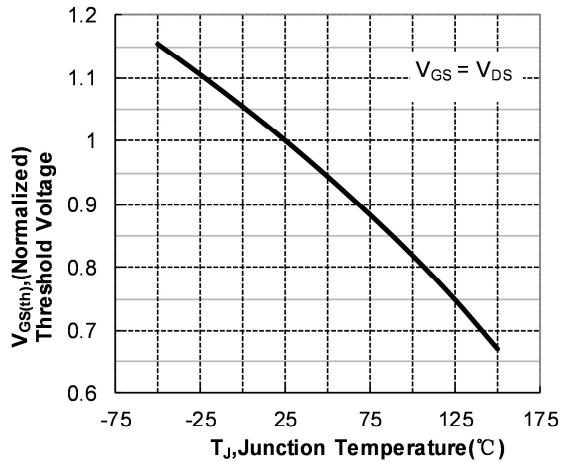


Figure 13. Normalized Threshold Voltage vs Junction Temperature

Test Circuit and Waveform

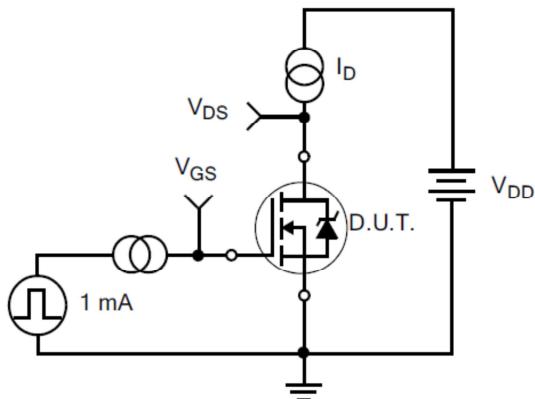


Figure 14. Gate Charge Test Circuit

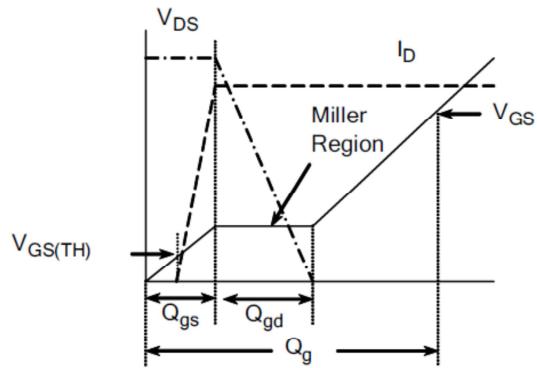


Figure 15. Gate Charge Waveforms

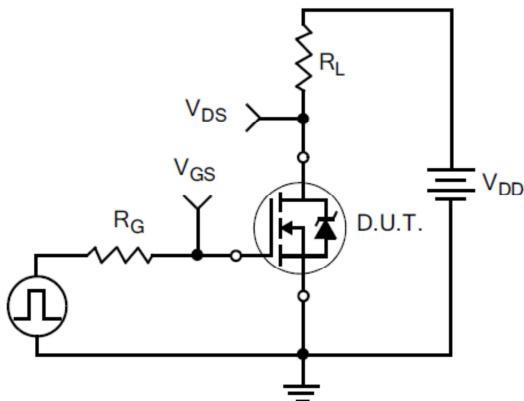


Figure 16. Resistive Switching Test Circuit

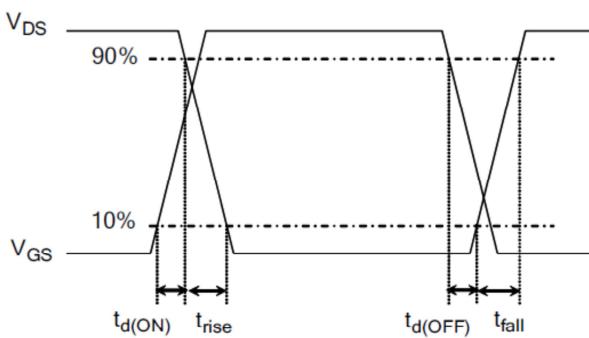


Figure 17. Resistive Switching Waveforms

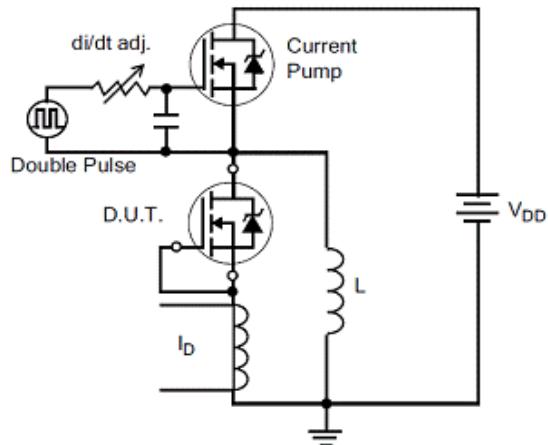


Figure 18. Diode Reverse Recovery Test Circuit

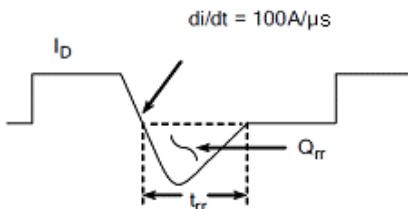


Figure 19. Diode Reverse Recovery Waveform

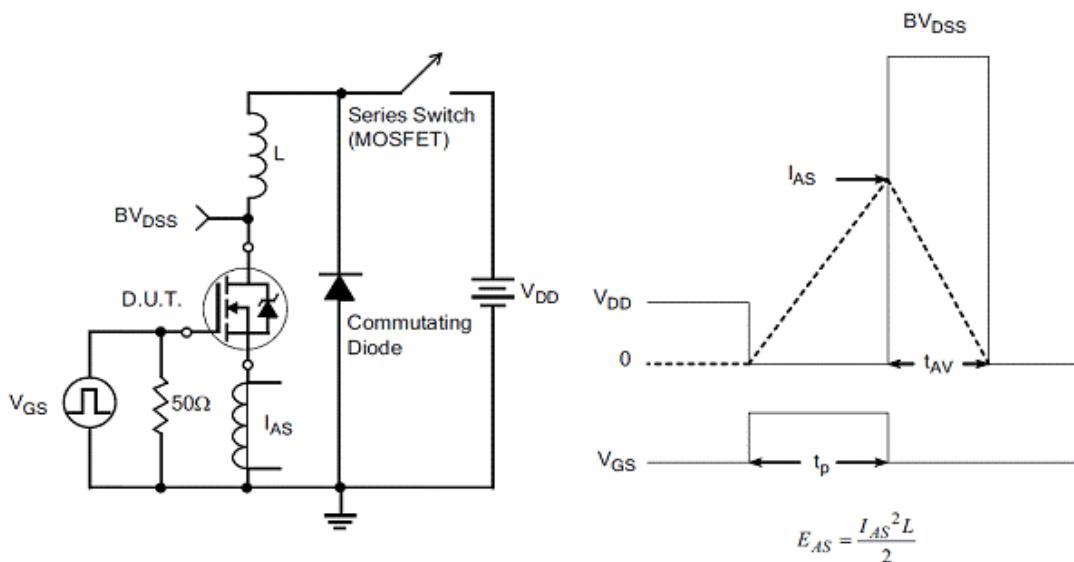


Figure20.Unclamped Inductive Switching Test Circuit

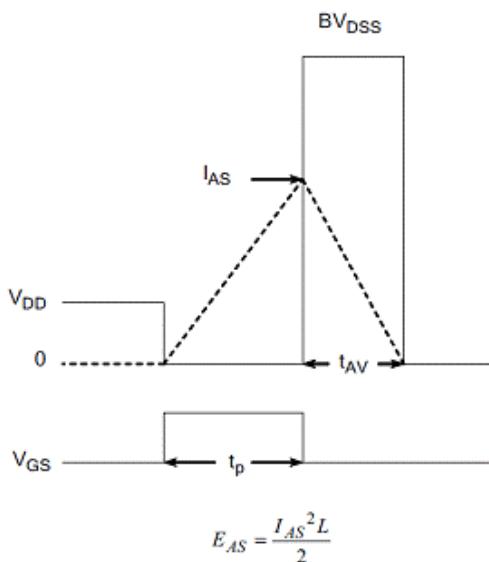
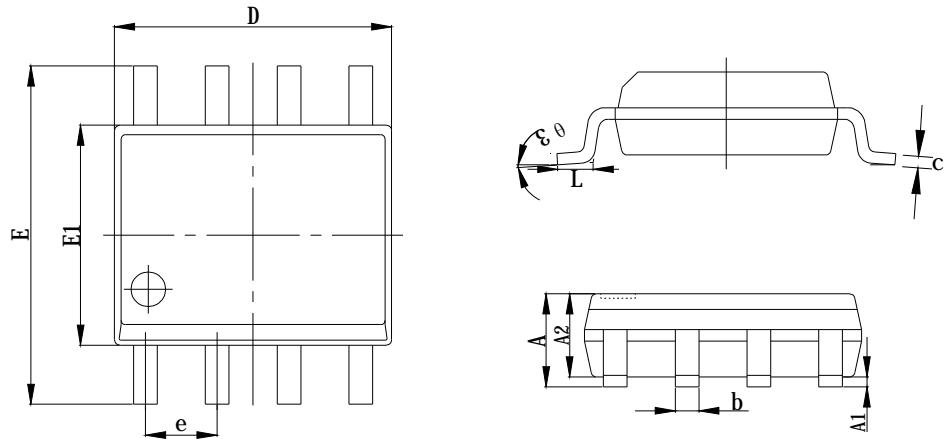


Figure21.Unclamped Inductive Switching Waveform

**Package Information:**

Items	Values(mm)	
	MIN	MAX
A	1.30	1.80
A1	0.10	0.25
A2	1.30	1.50
E	5.80	6.20
E1	3.80	4.00
D	4.80	5.00
L	0.40	0.90
e	1.27TYP	
b	0.37	0.47
c	0.20 TYP	
θ3	0°	8°

SOP-8 Package

**The name and content of poisonous and harmful material in products**

Part's Name	Hazardous Substance									
	Pb	Hg	Cd	Cr(VI)	PBB	PBDE	DIBP	DEHP	DBP	BBP
Limit	≤ 0.1%	≤ 0.1%	≤ 0.01%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%
Lead Frame	○	○	○	○	○	○	○	○	○	○
Molding	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
Wire Bonding	○	○	○	○	○	○	○	○	○	○
Solder	×	○	○	○	○	○	○	○	○	○
Note	<p>○: Means the hazardous material is under the criterion of 2011/65/EU.</p> <p>×: Means the hazardous material exceeds the criterion of 2011/65/EU.</p> <p>The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS.</p>									

Warnings

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. VDMOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. This publication is made by Huajing Microelectronics and subject to regular change without notice.

WUXI CHINA RESOURCES HUAJING MICROELECTRONICS CO., LTD.

Add: No.14 Liangxi RD. Wuxi, Jiangsu, China Mail:214061 <http://www.crhj.com.cn>
Tel: +86 0510-85807228 Fax: +86- 0510-85800864

Marketing Part: Post: 214061 Tel: +86 0510-81805277/81805336
Fax: +86 0510-85800360/85803016

Application and Service: Post: 214061 Tel / Fax: +86- 0510-81805243/81805110