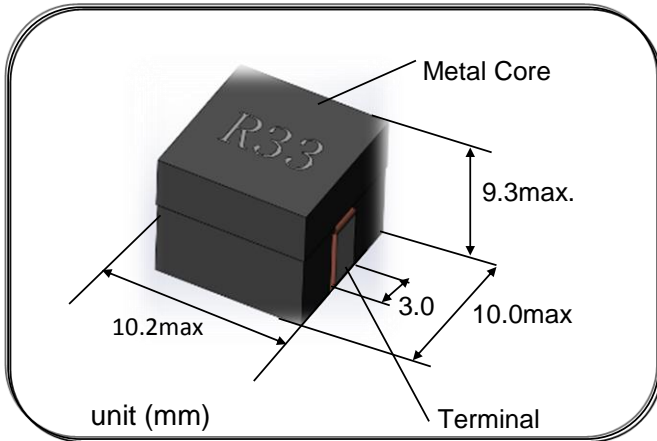


**Component
Image & Dimension**

Simulation Data



- a) Low Profile Design :
Mount Area : 10.2X10.0mm square
Low Profile : 9.3mm Max. Height
- b) High Power Handling Capability :
Small Copper Loss
Using Large Saturation Induction of Fe- based metals
- c) Wide Range Operating Temperature due to
High Curie Temperature of around 550 deg-C
- d) Automatic Mounting in Tape&Reel Package.

Applications :

Note Book & Mobile Computer,
VRM, etc.

Electrical Specification

TDK Identification	Inductance		Test	DC Resistance		Rated DC	Current
	at 0A (uH Typ.)	Tol. (%)	Freq. (kHz)	Spec. (m-ohm)	Tol. (%)	Idc 1 (A Typ.)	Idc 2 (A Typ.)
VLB10090HT-R33K	0.33	+/-10%	100	0.30	+/-10%	45.0	37
VLB10090HT-R50K	0.50	+/-10%	100	0.30	+/-10%	28.0	37

Note. **Idc 1** : Based on the inductance change (drop30% typ. from Lo)
Idc 2 : Based on the self temperature rise. (+40 degC typ.)

Caution: The cleaning agent can not be used for these parts.

