500V N-Channel MOSFET

General Features

- Proprietary New Planar Technology
- > $R_{DS(ON),typ.}=0.28 \ \Omega @V_{GS}=10V$
- Low Gate Charge Minimize Switching Loss
- Fast Recovery Body Diode

Applications

- Adaptor
- TV Main Power
- SMPS Power Supply
- LCD Panel Power

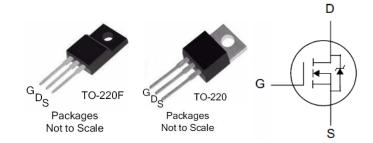
Ordering Information

| Part Number | Package | Brand |
|-------------|---------|-------|
| PTP17N50 | TO-220 | ľ |
| PTA17N50 | TO-220F | ï |

Absolute Maximum Ratings

| (Pb) | Lead F | ree F | Package | and | Finish |
|------|--------|-------|---------|-----|--------|
| | Load | | aonago | and | |

| BV _{DSS} | R _{DS(ON),typ} . | I _D |
|-------------------|---------------------------|----------------|
| 500V | 0.28Ω | 17A |



 $T_C{=}25\,^\circ\!\mathrm{C}$ unless otherwise specified

| Symbol | Parameter | PTP17N50 | PTA17N50 | Unit |
|------------------------------------|--|-------------|----------|------|
| V _{DSS} | Drain-to-Source Voltage ^[1] | 500 | | V |
| V _{GSS} | Gate-to-Source Voltage | ± | 30 | V |
| I _D | Continuous Drain Current | 1 | 7 | |
| I _{D @ Tc =100} °C | Continuous Drain Current @ Tc=100℃ | °C Figure 3 | | А |
| I _{DM} | Pulsed Drain Current at V _{GS} =10V ^[2] | Figure 6 | | |
| E _{AS} | Single Pulse Avalanche Energy | 1000 | | mJ |
| dv/dt | Peak Diode Recovery dv/dt ^[3] | 5.0 | | V/ns |
| P _D | Power Dissipation | 150 | 45 | W |
| T _L T _{PAK} | Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10 seconds, Package Body for 10 seconds | 300 260 | | C |
| T _J & T _{STG} | Operating and Storage Temperature Range | -55 to | o 150 | |

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

Thermal Characteristics

| Symbol | Parameter | PTP17N50 | PTA17N50 | Unit |
|------------------|---|----------|----------|--------|
| R _{θJC} | Thermal Resistance, Junction-to-Case | 0.84 | 2.78 | 20.111 |
| R _{θJA} | Thermal Resistance, Junction-to-Ambient | 62 | 100 | ĈŴ |

Electrical Characteristics

OFF Characteristics $T_J = 25^{\circ}C$ unless otherwise specified

| Symbol | Parameter | Min. | Тур. | Max. | Unit | Test Conditions |
|------------------|---------------------------------------|------|------|------|------------|---|
| BV_{DSS} | Drain-to-Source Breakdown Voltage | 500 | | | V | V_{GS} =0V, I _D =250uA |
| | Desire to Oscarso Las la se Oscarso t | | | 1 | | V _{DS} =500V, V _{GS} =0V |
| IDSS | Drain-to-Source Leakage Current | | | 100 | uA | V _{DS} =400V, V _{GS} =0V, T _J =125℃ |
| | Cata ta Sauraa Laakaga Currant | | | +100 | n 4 | V_{GS} =+30V, V_{DS} =0V |
| I _{GSS} | Gate-to-Source Leakage Current | | | -100 | nA | V _{GS} =-30V, V _{DS} =0V |

| ON Characteristics | |
|---------------------------|--|
|---------------------------|--|

| DN Characteristics $T_J = 25^{\circ} \odot$ unless otherwise specified | | | | | | |
|---|--|------|------|------|------|--|
| Symbol | Parameter | Min. | Тур. | Max. | Unit | Test Conditions |
| R _{DS(ON)} | Static Drain-to-Source On-Resistance ^[4] | | 0.28 | 0.38 | Ω | V _{GS} =10V, I _D =8.5A |
| V _{GS(TH)} | Gate Threshold Voltage | 2.0 | | 4.0 | V | $V_{DS}=V_{GS}, I_{D}=250uA$ |
| gfs | Forward Transconductance ^[4] | | 15 | | S | V _{DS} =30V,ID=17A |

Dynamic Characteristics

Essentially independent of operating temperature

| Symbol | Parameter | Min. | Тур. | Max. | Unit | Test Conditions |
|------------------|-------------------------------|------|------|------|------|---|
| C _{iss} | Input Capacitance | | 2500 | | | V 0V |
| C _{rss} | Reverse Transfer Capacitance | | 280 | | pF | V _{GS} =0V, V _{DS} =25V, f=1.0MH _Z |
| C _{oss} | Output Capacitance | | 800 | | | |
| Q _g | Total Gate Charge | | 45 | | | |
| Q _{gs} | Gate-to-Source Charge | | 10 | | nC | V_{DD} =250V, I _D =17A, V_{GS} =0 to 10V |
| Q _{gd} | Gate-to-Drain (Miller) Charge | | 18 | | | |

Resistive Switching Characteristics

Essentially independent of operating temperature

| Symbol | Parameter | Min. | Тур. | Max. | Unit | Test Conditions |
|---------|---------------------|------|------|------|------|--|
| td(ON) | Turn-on Delay Time | | 15 | | | |
| trise | Rise Time | | 35 | | nS | $V_{DD}=250V,$ $I_{D}=17A,$ $V_{GS}=10V$ $R_{G}=6.1 \Omega$ |
| td(OFF) | Turn-Off Delay Time | | 52 | | | |
| tfall | Fall Time | | 40 | | | |

Source-Drain Body Diode Characteristics

 $T_J=25^{\circ}C$ unless otherwise specified

| Symbol | Parameter | Min | Тур. | Max. | Unit | Test Conditions |
|-----------------|--|-----|------|------|------|--|
| I _{SD} | Continuous Source Current ^[4] | | | 17 | ^ | Integral PN-diode in |
| I _{SM} | Pulsed Source Current ^[4] | | | 68 | A | MOSFET |
| V _{SD} | Diode Forward Voltage | | | 1.5 | V | I _S =17A, V _{GS} =0V |
| trr | Reverse recovery time | | 220 | | ns | V _{GS} =0V ,IF=17A, |
| Qrr | Reverse recovery charge | | 2.5 | | uC | diғ/dt=100A/µs |

Note:

^[1] T_{J} =+25 $^{\circ}\!\!\!C$ to +150 $^{\circ}\!\!\!C$ [2] Repetitive rating; pulse width limited by maximum junction temperature.

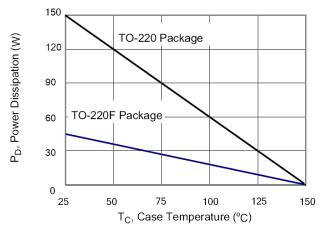
^[3] ISD= 17A di/dt < 100 A/µs, VDD < BVDSS, TJ=+150°C.

^[4] Pulse width≤380µs; duty cycle≤2%.

Typical Characteristics Figure 1. Maximum Effective Thermal Impedance, Junction-to-Case Duty Factor 1.000 50% -20% Z_{0JC}, Thermal Impedance 10% 0.100 5% (Normalized) 2% P_{DM} 0.010 1% 0.001 NOTES single pulse –NOTES. –DUTY FACTOR: D=t1/t2 _PEAK TJ=PDM × Z₀JC × R₀JC+TC 0.0001 1E-3 1E-6 10E-6 100E-3 100E-6 10E-3 1E+0 10E+0

t_p, Rectangular Pulse Duration (s)

Figure 2. Maximum Power Dissipation vs Case Temperature



40

30

20

10

0

0

I_D, Drain Current (A)

PULSE DURATION = 250 µS

10

DUTY FACTOR = 0.5%MAX, T_C = 25° C

5

Figure 4. Typical Output Characteristics

Figure 3. Maximum Continuous Drain Current vs Case Temperature

PTP17N50 PTA17N50

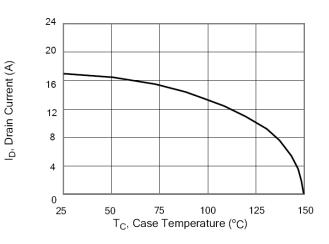
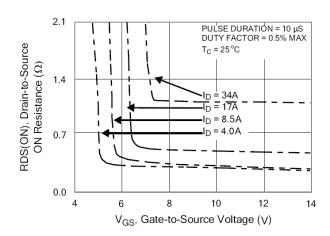


Figure 5. Typical Drain-to-Source ON Resistance vs Gate Voltage and Drain Current



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20

15

V_{DS}, Drain-to-Source Voltage (V)

15V

= 7.0

GS = 6.0V

GS = 5.5∖

VGS = 5.0V

25

30

Typical Characteristics(Cont.)

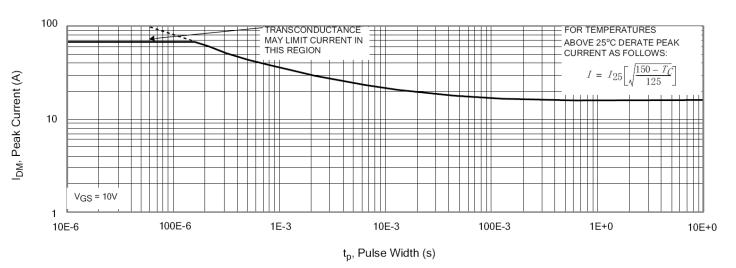
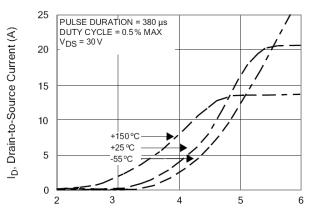
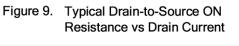


Figure 6. Maximum Peak Current Capability





V_{GS}, Gate-to-Source Voltage (V)



1.8

1.5

1.2

0.9

0.6

0.3

0

R_{DS(ON)}, Drain-to-Source

ON Resistance (Ω)

Figure 8. Unclamped Inductive Switching Capability

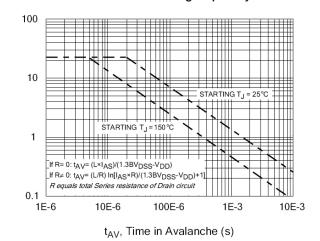
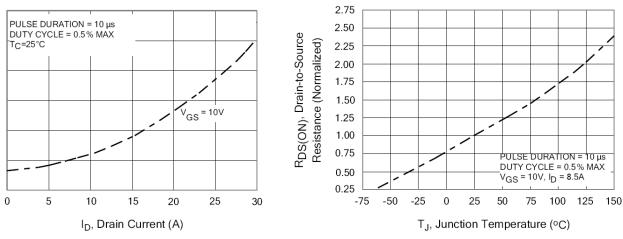


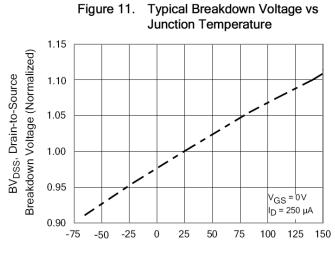
Figure 10. Typical Drain-to-Source ON Resistance vs Junction Temperature



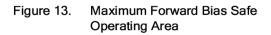
I_{AS}, Avalanche Current (A)

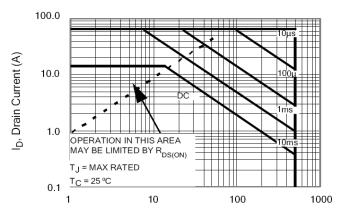


Typical Characteristics(Cont.)



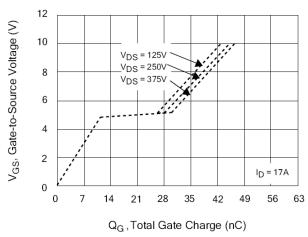
 $T_J,$ Junction Temperature (°C)

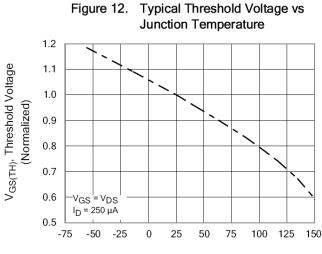




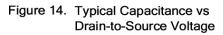
V_{DS}, Drain-to-Source Voltage (V)

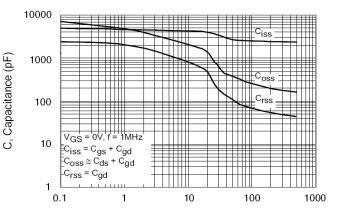
Figure 15. Typical Gate Charge vs Gate-to-Source Voltage





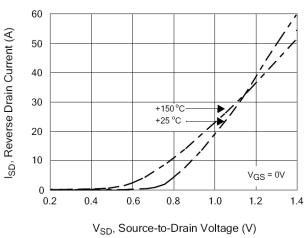
T_J, Junction Temperature (°C)



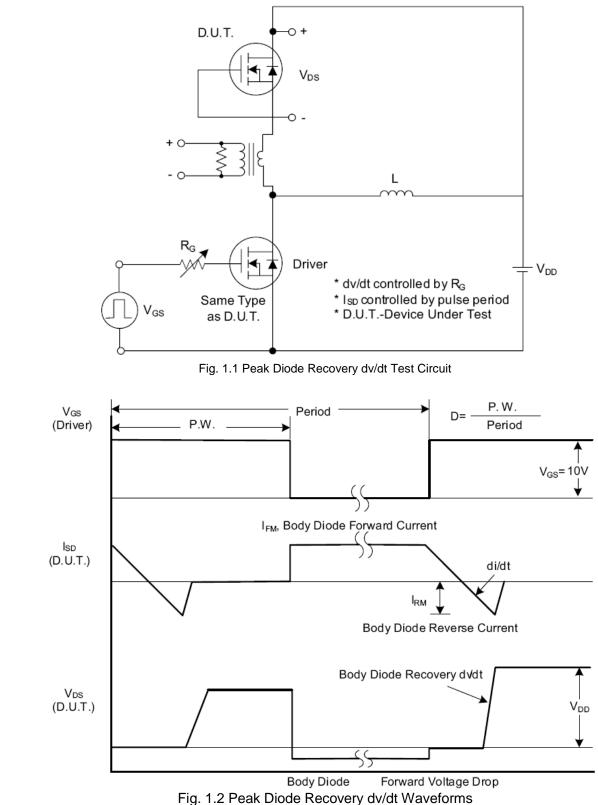


V_{DS}, Drain Voltage (V)

Figure 16. Typical Body Diode Transfer Characteristics







PTP17N50 PTA17N50

Test Circuits and Waveforms (Cont.)

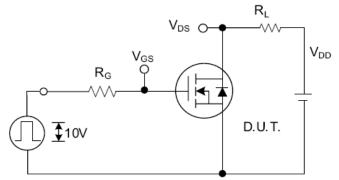


Fig. 2.1 Switching Test Circuit

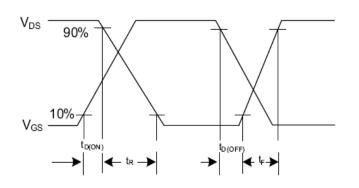


Fig. 2.2 Switching Waveforms

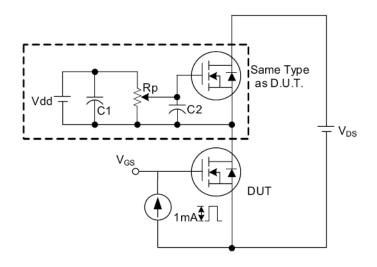


Fig. 3 . 1 Gate Charge Test Circuit

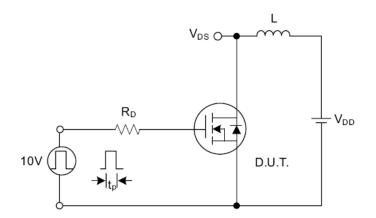


Fig. 4.1 Unclamped Inductive Switching Test Circuit

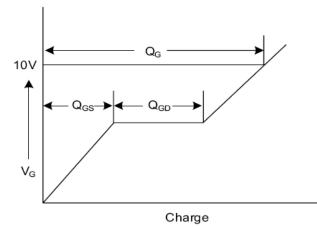
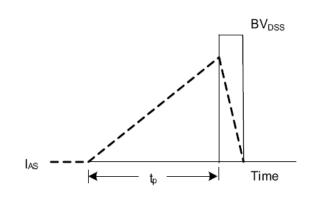


Fig. 3.2 Gate Charge Waveform





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