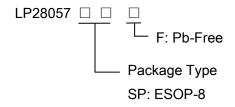
1.2A Single-chip Li-ion Charge with Protection of Reverse Connection of Battery

General Description

The LP28057 is a complete constant-current/ constant- voltage linear charger for single cell lithium-ion batteries. Its ESOP-8 package and low external component count make the LP28057 ideally suited for portable applications. No external sense resistor is needed, and no blocking diode is required due to the internal MOSFET architecture. Thermal feedback regulates the charge current to limit the die temperature during high power operation or high ambient temperature. The charge voltage is fixed at 4.2V, and the charge current can be ISET rammed externally with a single resistor. The LP28057 automatically terminates the charge cycle when the charge current drops to 1/10th the ISET rammed value after the final float voltage is reached. When the input supply is removed, the LP28057 automatically enters a low current state, dropping the battery drain current to less than 1µA. The LP28057 can be put into shutdown mode, reducing the supply current to 1µA.

Other features include charge current monitor, under voltage lockout, automatic recharge and a status pin to indicate charge termination and the presence of an input voltage.

Order Information



Features

- Protection of Reverse Connection of Battery
- Programmable Charge Current Up to 1200mA
- No MOSFET, Sense Resistor or Blocking Diode Required
- Constant-Current/Constant-Voltage Operation with Thermal Regulation to Maximize Charge Rate Without Risk of Overheating
- ◆ 4.2V Charge Voltage with ± 1% Accuracy
- Short-circuit protection
- 1μA Supply Current in Shutdown
- ◆ Consumption Available in ESOP-8 Package
- ◆ RoHS Compliant and 100% Lead (Pb)-Free

Applications

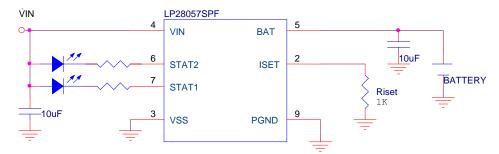
- ♦ Portable Media Players/MP3 players
- Cellular and Smart mobile phone
- ♦ PDA/DSC
- ♦ Bluetooth Applications

Marking Information

Device	Marking	Package	Shipping		
LP28057SPF	LPS	SP:ESOP-8	2.5K/REEL		
	LP28057				
	YWX				
Marking indication:					
Y:Production year W:Production week X:Production batch.					

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Typical Application Circuit



Functional Pin Description

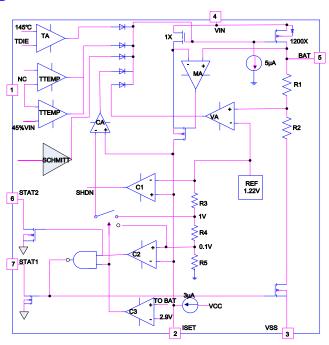
Package Type	Pin Configurations		
ESOP-8	NC 1		

Pin Description

Pin	Name	Description Descri				
1	NC	No Connector.				
2	ISET	Charge Current Program, Charge Current Monitor and Shutdown Pin.				
3	VSS	VSS is the connection to system ground.				
4	VIN	VIN is the input power source. Connect to a wall adapter.				
5	BAT	BAT is the connection to the battery. Typically a 10µF Tantalum capacitor is needed for stability when there				
5	DAT	is no battery attached. When a battery is attached, only a 0.1µF ceramic capacitor is required.				
		Open-Drain Charge Status Output. When the battery is charging, the STAT pin could be pulled High by an				
6	STAT2	external pull high resistor. When the charge cycle is completed, the pin is pulled Low by an internal				
		N-channel MOSFET.				
		Open-Drain Charge Status Output. When the battery is charging, the STAT pin is pulled low by an internal				
7	STAT1	N-channel MOSFET. When the charge cycle is completed, the pin could be pulled High by an external pull				
		high resistor.				
8	NC	No Connector.				
9	PGND	Ground.				

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Function Block Diagram



Absolute Maximum Ratings Note 1

\diamond	Input to GND(VIN)
	BAT to GND
	VIN to BAT 8V
	Other Pin to GND
	BAT Short-circuit Duration Continuous
	Maximum Junction Temperature 150°C
	Storage Temperature
	Operating Ambient Temperature Range (TA)
	Maximum Soldering Temperature (at leads, 10 sec) 260°C

Note 1. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Thermal Information

\diamond	Maximum Power Dissipation (P _D ,T _A =25°C)	 2W

♦ Thermal Resistance (θ_{JA}) ----- 50°C/W

ESD Susceptibility

♦ HBM(Human Body Mode) ------ 2KV

♦ MM(Machine Mode) ------ 200V

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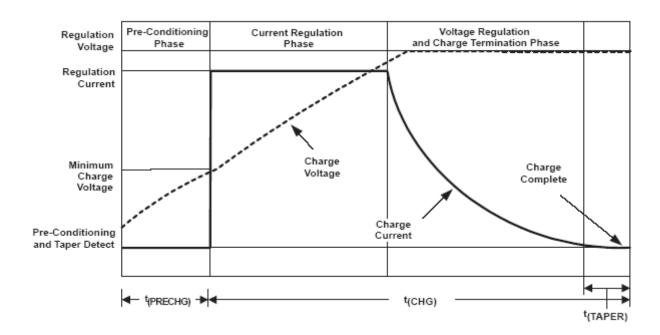
Electrical Characteristics

(T_A=25°C. V_{IN}=5V, unless otherwise noted.)

Symbol	Parameter	Condition	Min	Тур	Max	Units
V _{IN}	Adapter/USB Voltage Range		4.5	5	6.5	V
		Charge Mode, R _{ISET} =10k		300	2000	μΑ
I_{CC}	Input Supply Current	Standby Mode (Charge Terminated)		300	500	μΑ
		Shutdown Mode (R _{ISET} NC)			50	μΑ
V_{FLOAT}	Regulated Output (Float) Voltage	I _{BAT} =40mA	4.158	4.2	4.242	V
		R _{ISET} =1k	850	1000	1150	mA
		R _{ISET} =2k	430	500	570	mA
I_{BAT}	BAT Pin Current	Reverse Current, V _{BAT} =-4.2V		-1		mA
		Standby Mode, V _{BAT} =4.2V	0	0.1	1	μΑ
		Shutdown Mode (R _{ISET} NC)	0	0.1	1	μA
I _{TRIKL}	Trickle Charge Current	V _{BAT} <v<sub>TRIKL</v<sub>		10		%I _{BAT}
V_{TRIKL}	Trickle Charge Threshold Voltage	R _{ISET} =10k, VBAT Rising	2.8	2.9	3.0	V
V _{TR-HYS}	Trickle Charge Hysteresis Voltage	tage R _{ISET} =10k		80	110	mV
V_{UV}	VIN Under voltage Lockout Threshold	From VIN Low to High	3.7	3.8	3.95	V
V _{UV-HYS}	VIN Under voltage Lockout Hysteres <mark>i</mark> s	onComi 2的语子	山首	500		mV
V _{ASD}	VIN -VBAT Lockout Threshold Voltage	VIN from Low to High	70	100	140	mV
V _{ASD-HYS}	V _{ASD} Hysteresis Voltage			20		mV
1	Termination Current Threshold	R _{ISET} =10k		10		%I _{BAT}
I _{TERM}	Termination Current Threshold	R _{ISET} =2k		10		%I _{BAT}
V _{ISET}	ISET Pin Voltage	R _{ISET} =10k, Current Mode		1		V
V _{STAT}	STAT Pin Output Low Voltage	I _{STAT} =5mA		0.35	0.6	V
ISTAT	STAT Pin Weak Pull-Down Current	VSTAT=5V			5	uA
ΔV_{RECHRG}	Recharge Battery Threshold Voltage	V _{FLOAT} -V _{RECHRG}	100	150	200	mV
T _{LIM}	Junction Temperature in Constant Temperature Mode			150		°C
R _{ON}	Power FET "ON" Resistance (Between VIN and BAT)			300		mΩ

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Preliminary Datasheet

LP28057

Application Information

The LP28057 is a single cell lithium-ion battery charger using a constant-current/constant-voltage algorithm. It can deliver up to 1200mA of charge current (using a good thermal PCB layout) with a final float voltage accuracy of ±1%(4.2V). The LP28057 includes an internal P-channel power MOSFET and thermal regulation circuitry. No blocking diode or external current sense resistor is required; thus, the basic charger circuit requires only two external components. Furthermore, the LP28057 is capable of operating from a USB power source.

Normal Charge Cycle

A charge cycle begins when the voltage at the VIN pin rises above the UVLO threshold level and a 1% ISET ram resistor is connected from the ISET pin to ground or when a battery is connected to the charger output. If the BAT pin is less than 2.9V, the charger enters trickle charge mode. In this mode, the LP28057 supplies approximately 1/10 the RISET programmed charge current, When the BAT pin voltage rises above 2.9V, the charger enters constant-current mode, where the RISET rammed charge current is supplied to the battery. When the BAT pin approaches the final float voltage, the LP28057 enters constant-voltage mode and the charge current begins to decrease. The charge cycle ends when the charge current drops to 1/10 of the RISET rammed value.

ISET ramming Charge Current

The charge current is R_{ISET} rammed using a single resistor from the ISET pin to ground. The battery charge current is 500 times the current out of the ISET pin. The ISET ram resistor and the charge current are calculated using the following equations:

R_{ISET}=1000V/I_{BAT} I_{BAT}=1000V/R_{ISET}

The charge current out of the BAT pin can be determined at any time by monitoring the ISET pin voltage using the following equation:

IBAT=VISET×1000/RISET

Charge Termination

A charge cycle is terminated when the charge current falls to 1/10th the ISET rammed value after the final float voltage is reached. This condition is detected by using an internal, filtered comparator to monitor the ISET pin. When the ISET pin voltage falls below 200mV for longer than tTERM(typically 1ms), charging is terminated. The charge current is latched off and the LP28056S enters standby mode, where the input supply current drops to 200µA. In this state, all loads on the BAT pin must be supplied by the battery. (Note: C/10 termination is disabled in trickle charging and thermal limiting modes).

Charge Status Indicator (STAT)

The charge status output has two different states: strong pull-down (~5mA) and high impedance. The strong pull-down state indicates that the LP28057 is in a charge cycle. Once the charge cycle has terminated, the pin state is determined by under voltage lockout conditions. High impedance indicates that the LP28057 is in under voltage lockout mode: either VIN is less than 100mV above the BAT pin voltage or insufficient voltage is applied to the VIN pin.

A microprocessor can be used to distinguish between these two states—this method is discussed in the Applications Information section.

Function	STAT1(pin7)	STAT2(pin6)	
Charging	Low High		
Charge Complete	High	Low	

Thermal Limiting

An internal thermal feedback loop reduces the I_{SET} rammed charge current if the die temperature attempts to rise above a preset value of approximately 150°C. This feature protects the LP28057 from excessive temperature and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the LP28057.

The charge current can be set according to typical (not worst-case) ambient temperature with the assurance that the charger will automatically reduce the current in worst-case conditions.

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Preliminary Datasheet

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Under voltage Lockout (UVLO)

An internal under voltage lockout circuit monitors the input voltage and keeps the charger in shutdown mode until V_{IN} rises above the under voltage lockout threshold. The UVLO circuit has a built-in hysteresis of 200mV. Furthermore, to protect against reverse current in the power MOSFET, the UVLO circuit keeps the charger in shutdown mode if V_{IN} falls to within 80mV of the battery voltage. If the UVLO comparator is tripped, the charger will not come out of shutdown mode until V_{IN} rises to 100mV above the battery voltage.

Power Dissipation

The conditions that cause the LP28057 to reduce charge current through thermal feedback can be approximated by considering the power dissipated in the IC. Nearly all of this power dissipation is generated by the internal MOSFET this is calculated to be approximately:

 $P_D = (V_{IN} - V_{BAT}) \times I_{BAT}$

Where P_D is the power dissipated, V_{IN} is the input supply voltage, V_{BAT} is the battery voltage and I_{BAT} is the charge current. The approximate ambient temperature at which the thermal feedback begins to protect the IC is:

 $T_A=150^{\circ}C-P_D\times\theta_{JA}$

 $T_A=150$ °C-($V_{IN}-V_{BAT}$)× I_{BAT} × θ_{JA}

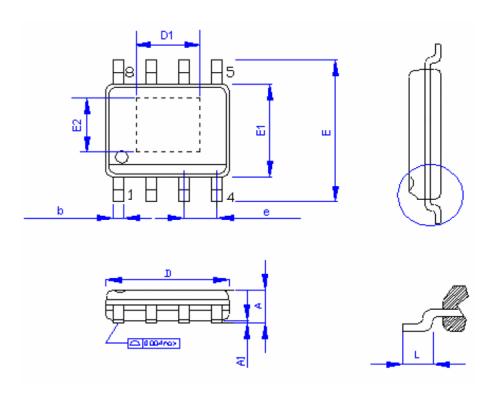


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Packaging Information

ESOP-8



SYMBOLS	MILLIMETERS		INCHES	
SIMBOLS	MIN.	MAX.	MIN.	MAX.
A	1.35	1.75	0.053	0.069
A1	0.05	0.25	0.002	0.010
D	4.90		0.193	
E1	3.90		0.153	
E	5.80	6.20	0.228	0.244
L	0.40	1.27	0.016	0.050
ь	0.33	0.51	0.013	0.020
e	1.27		0.500	
D1	2.06		0.081	
E2	2.06		0.081	

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