1.2-Walts Mono Filter-free Audio Power Amplifier

General Description

The LM4890 is an audio power amplifier designed for portable communication device applications such as mobile phone applications. The LM4890 is capable of delivering 1.2W of continuous average power to an 8Ω load and with less than 1% distortion (THD+N) from a 5.2V power supply, and 350mW to a 8Ω load from a 3V power supply.

The LM4890 provides high quality audio while requiring few external components and minimal power consumption. It features a low-power shutdown mode, which is achieved by driving the SHUTDOWN pin with logic low.

The LM4890 contains circuitry to prevent from "pop and click" noise that would otherwise occur during turn-on and turn-off transitions. For maximum flexibility, the LM4890 provides an externally controlled gain (with resistors), as well as an externally controlled turn-on and turn-off times (with the bypass capacitor).

Features

◆ 2.5-5.5V Operation Voltage
◆ 63dB PSRR at 217Hz, VDD=5V
◆ 0.1μA ultra low current shutdown mode
◆ Improved pop & click circuitry
◆ Unique Modulation Scheme Reduces EMI Emissions
◆ 0.1-μA Shutdown Current
◆ No output coupling capacitors, snubber networks or bootstrap capacitors required
◆ External gain configuration capability
◆ Shutdown Pin has 0.4V Compatible Thresholds
◆ BTL output can drive capacitive loads
◆ RoHS compliant and 100% lead(Pb)-free

Applications

✧ PMP,PSP, Game, Data-Bank
✧ Cellular and Smart mobile phone

## Typical Application Circuit
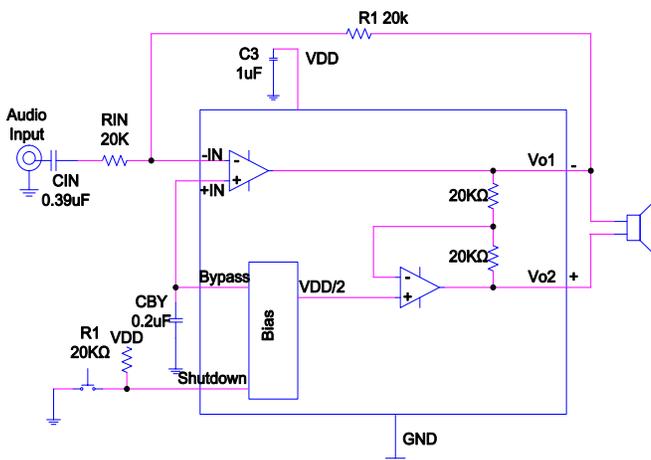
Figure 1. Single Input
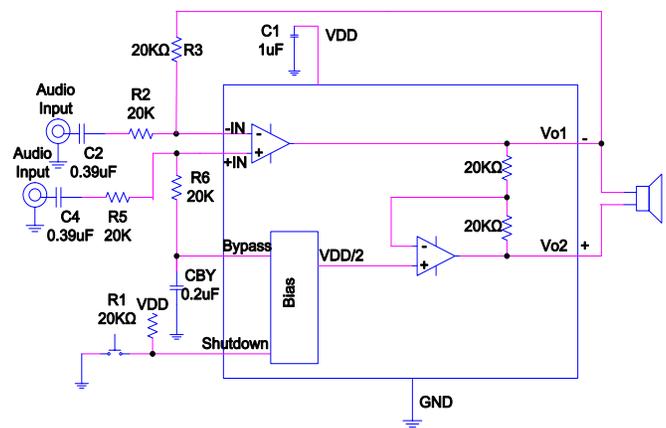
Figure 2. Differential Input

Functional Pin Description

Pin Configurations
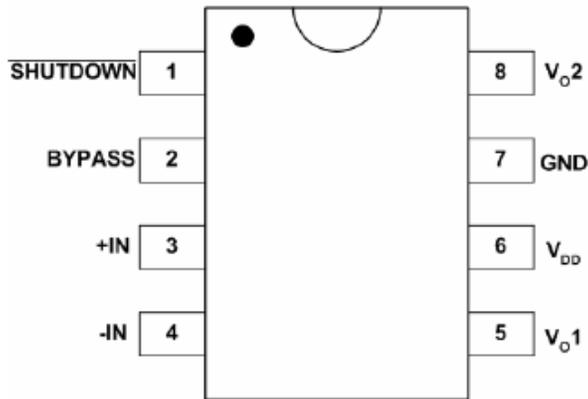
Pin Description

Pin	Name	Description
1	SHDN	The device enters in shutdown mode when a low level is applied on this pin.
2	BYPASS	Bypass capacitor pin which provides the common mode voltage.
3	+IN	Positive input of the first amplifier, receives the common mode voltage.
4	-IN	Negative input of the first amplifier, receives the audio input signal. Connected to the feedback resistor Rf and to the input resistor Rin.
5	VO1	Negative output of the LM4890. Connected to the load and to the feedback resistor Rf.
6	VDD	Analog VDD input supply.
7	GND	Ground connection for circuitry.
8	VO2	Positive output of the LM4890.

Absolute Maximum Ratings

✧ Input Voltage to GND --- -0.3V to 6V

✧ Other pin Voltage to GND --- -0.3 V to 6V

✧ Junction Temperature, T_{JMAX} --- 150°C

✧ Storage Temperature Rang, T_{stg} --- -65°C to 150°C

✧ ESD Susceptibility --- 2kV

✧ Maximum Soldering Temperature (at leads, 10 sec) --- 260°C

✧ Thermal Resistance $θ_{JA}$ --- 165°C/W

✧ Package Dissipation P_D -- 0.9W

Electrical Characteristics

| Symbol	Parameter	Conditions		Min.	Typ.	Max.	Unit
VOS	Output offset voltage (measured differentially)	Vi=0V,Av=2V/V,VDD=2.5V to 5.5V			5	25	mV
IQ	Quiescent current	VDD=5.5V, no load			2.5		mA
		VDD=3.6V, no load			1.40		
		VDD=2.5V, no load			1.06		
ISHDN	Shutdown Current	VSHDN=0.35V, VDD=2.5V to 5.5V		0.1	2.0		µA
RDS(ON)	Static drain-source on-state resistance	VDD=5.5V, no load			400		mΩ
		VDD=3.6V, no load			500		
		VDD=2.5V, no load			700		
Po	Output power	VDD=5.2V, RL=8Ω, THD=1%, f=1KHz			1.2		W
V_{SHDN}	Start up voltage threshold			1.4			V
	Shutdown voltage threshold					0.4	
THD+N	Total harmonic distortion plus noise	VDD=5V,Po=1W,RL=8Ω,f=1KHz			0.123		%
		VDD=3.6V,Po=0.5W,RL=8Ω,f=1KHz			0.130		
		VDD=2.5V,Po=0.2W,RL=8Ω,f=1KHz			0.163		
PSRR	Supply ripple rejection ratio	VDD=3.6V,Inputs ac-grounded with Ci=2uF	F=217Hz, V(ripple)=200mV		-63		dB
CMRR	Common mode rejection ratio	VDD=3.6V, Vic=1Vpp	F=217Hz		-62		dB
Zt	Start-up time from shutdown	VDD=3.6V			45		mS

# Typical Operating Characteristics
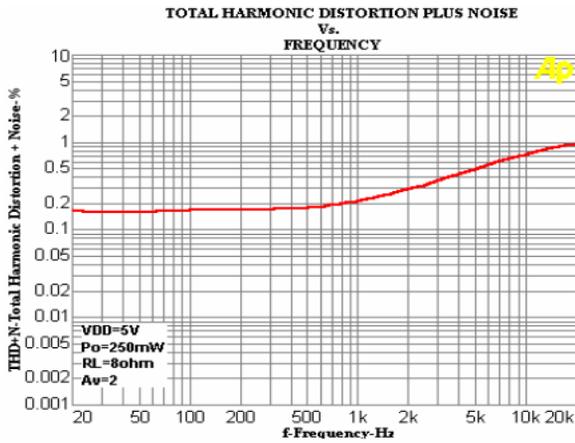

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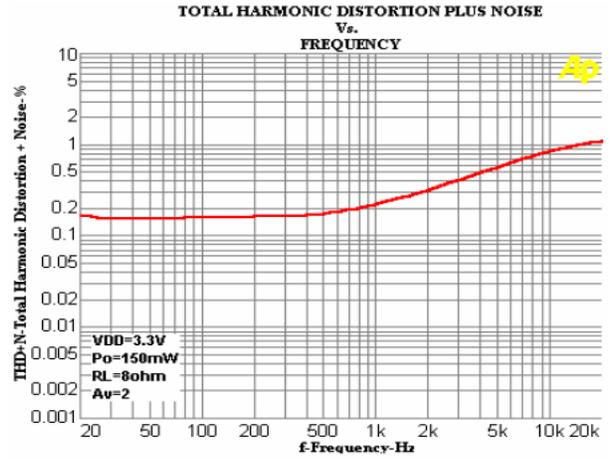
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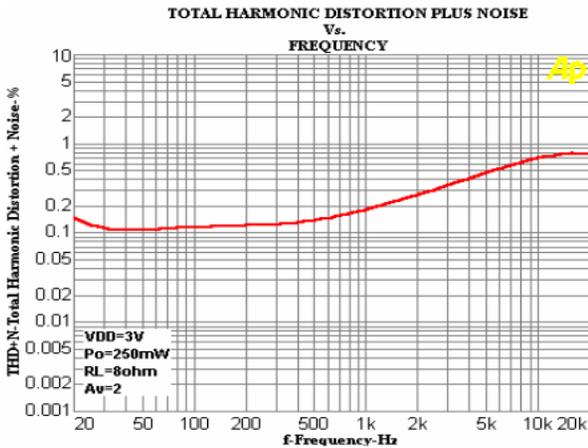
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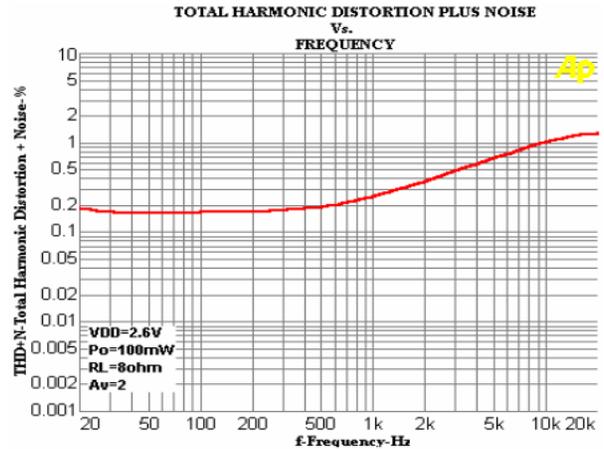
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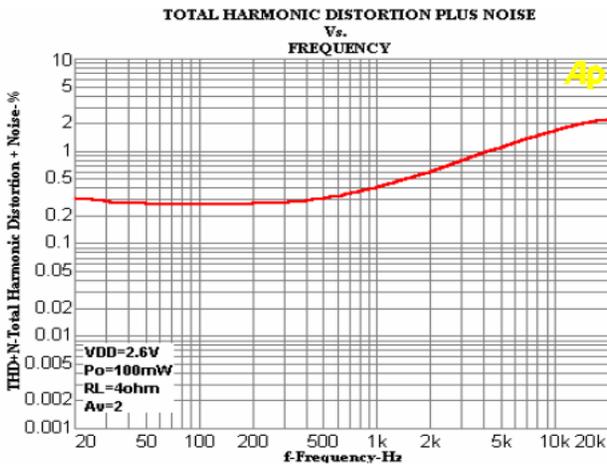
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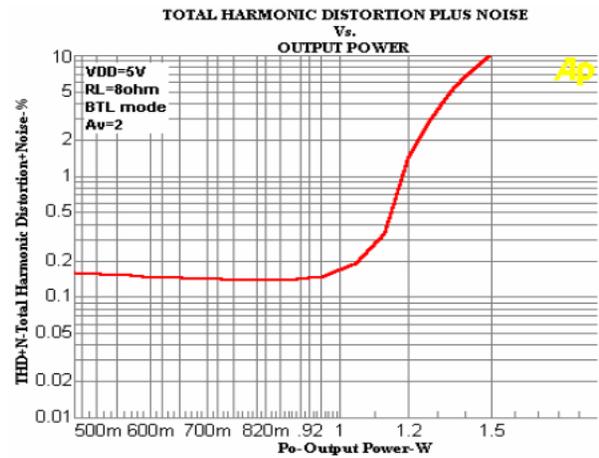
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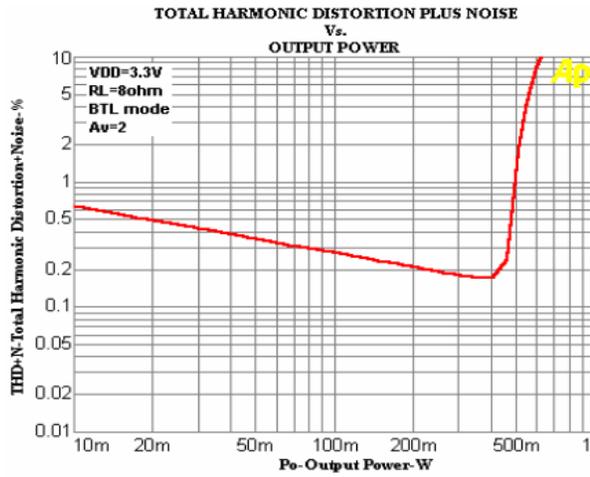
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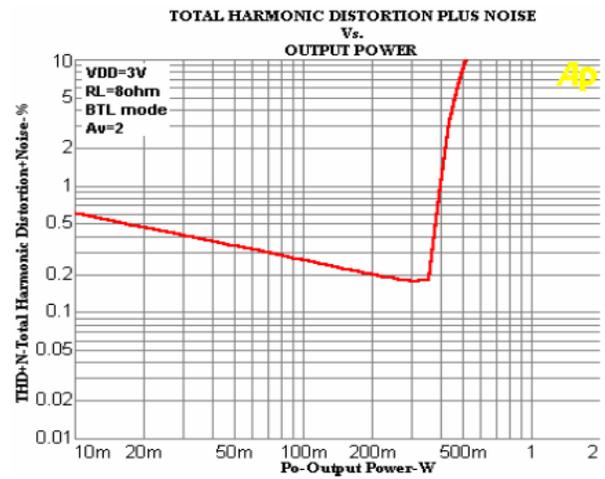
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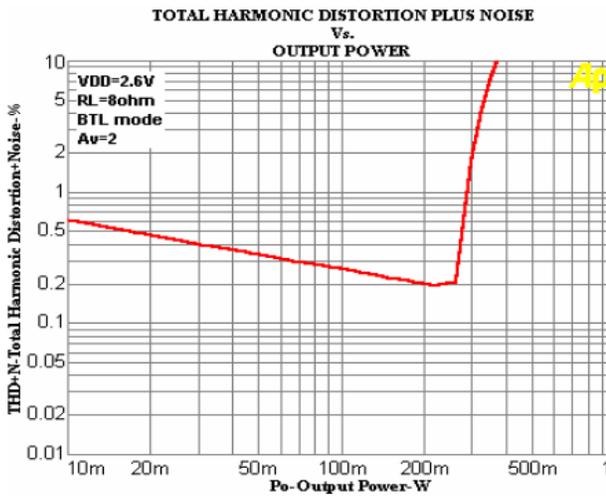
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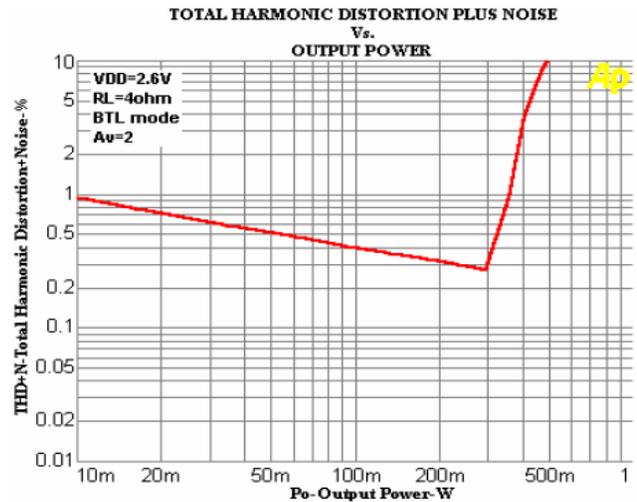
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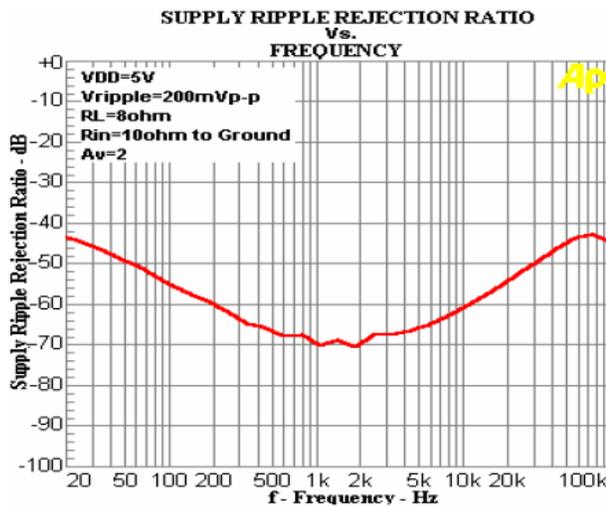
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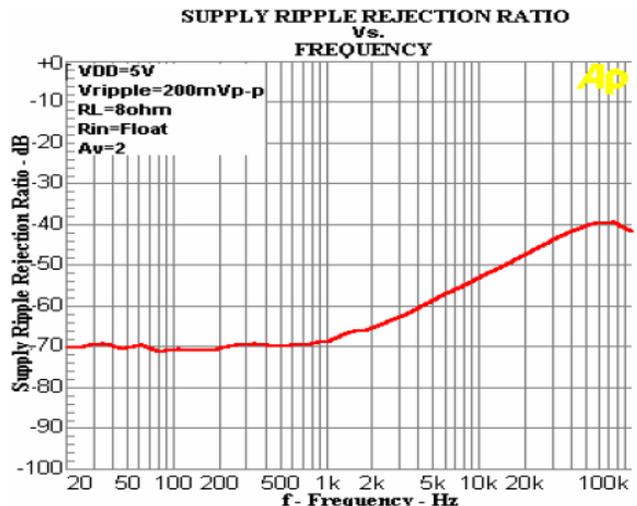


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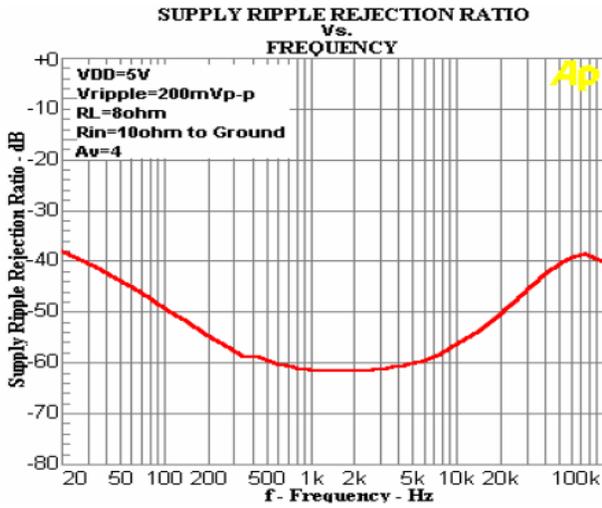
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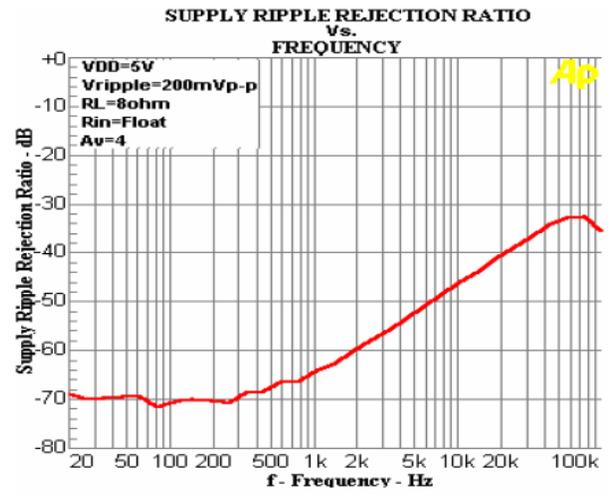
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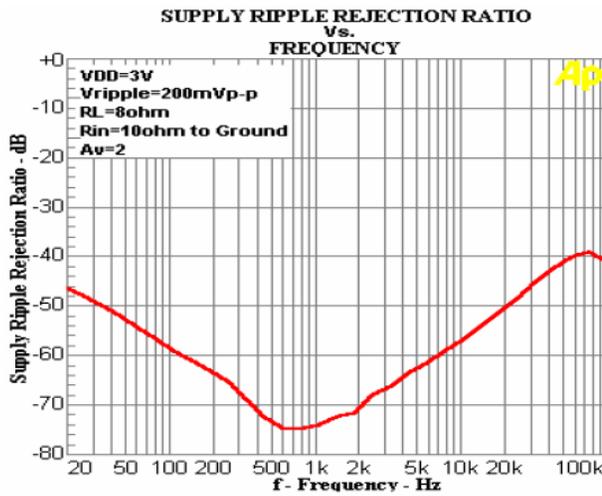

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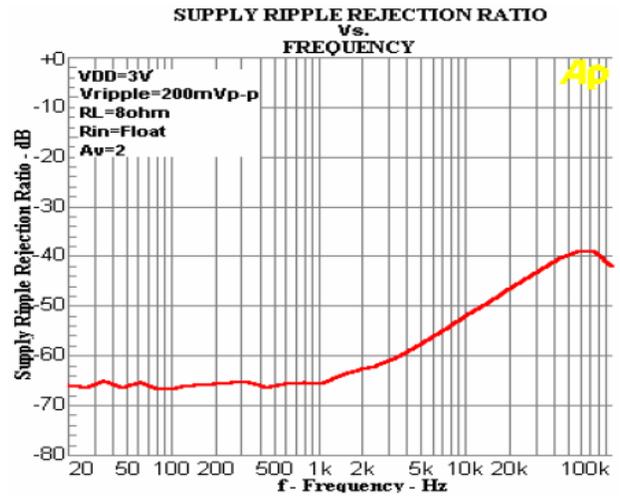

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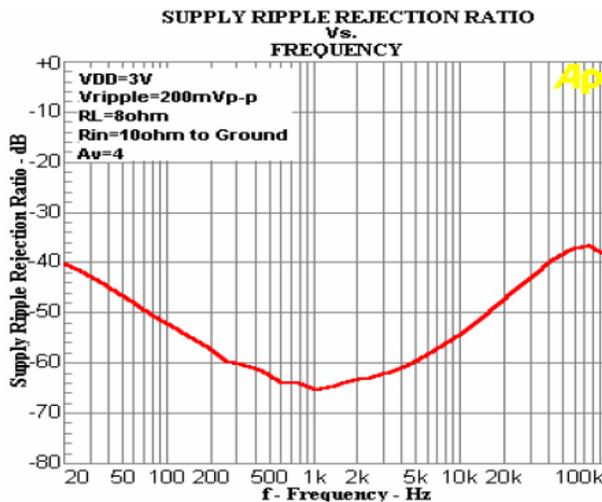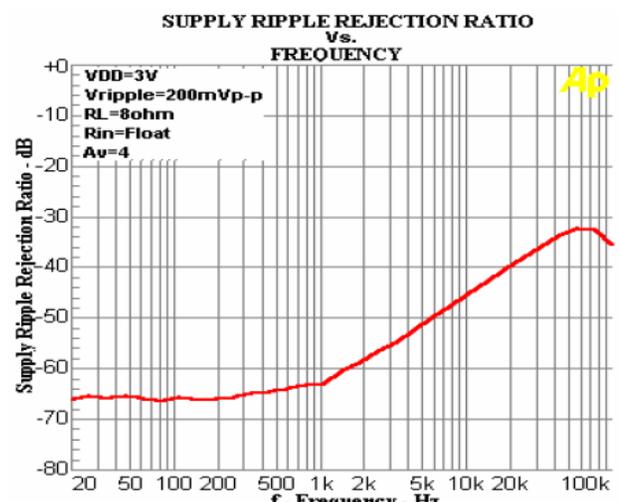
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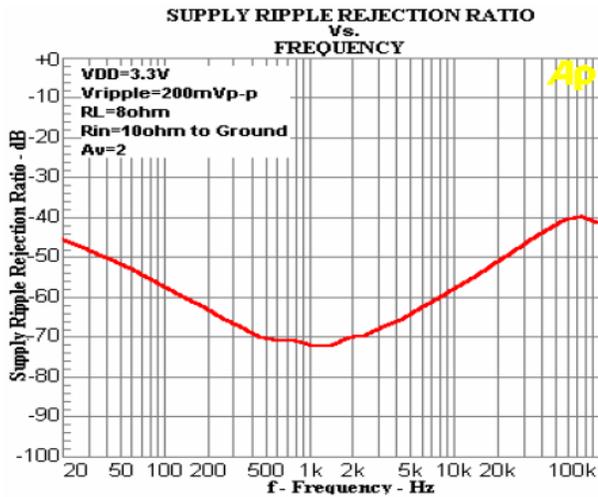
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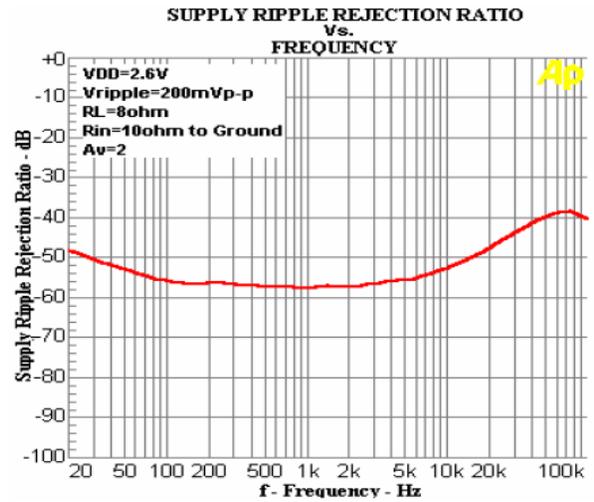

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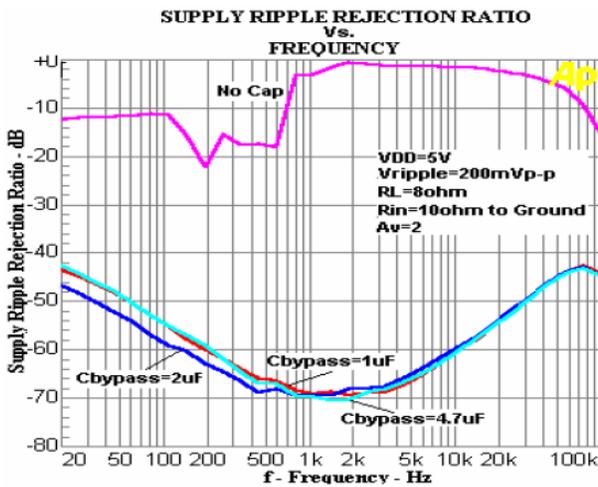
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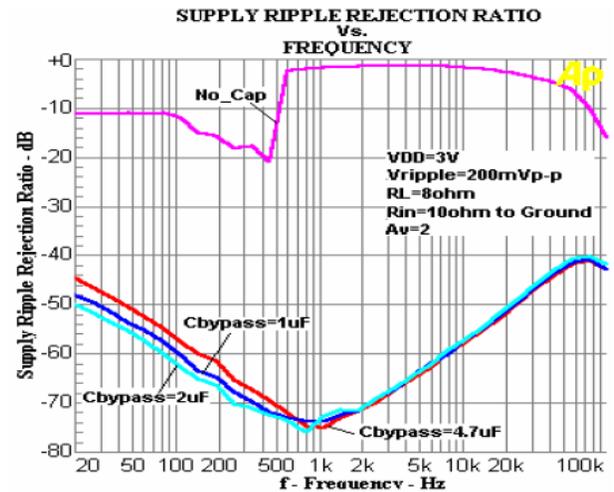
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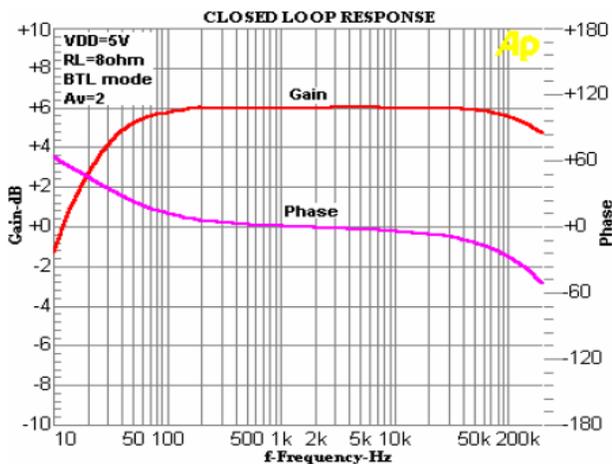
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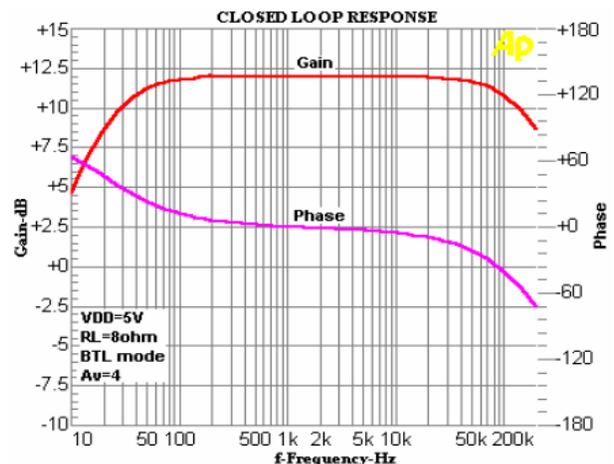
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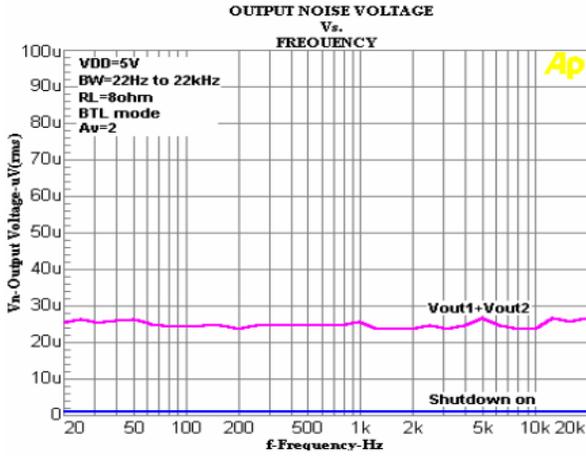
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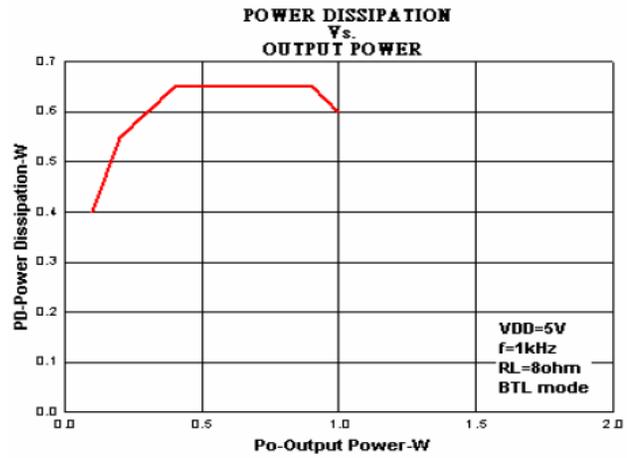
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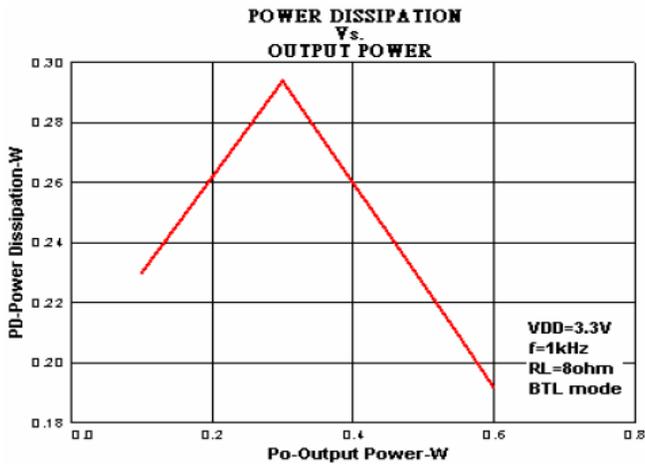


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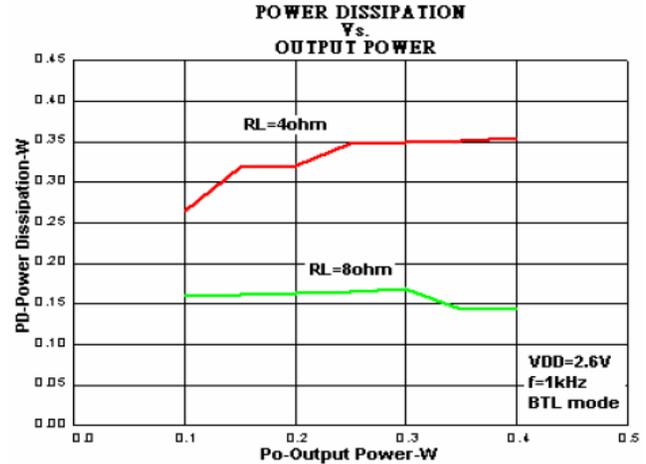
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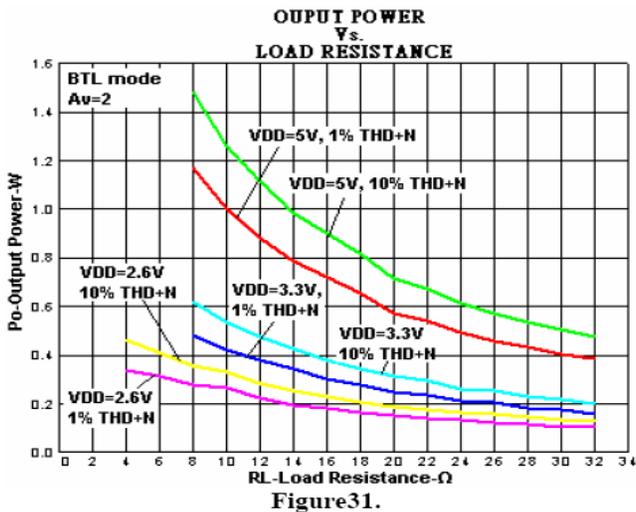
Figure31.

Layout Considerations

As output power increases, interconnect resistance (PCB traces and wires) between the amplifier, load and power supply create a voltage drop. The voltage loss on the traces between the LM4890 and the load results is lower output power and decreased efficiency. Higher trace resistance between the supply and the LM4890 has the same effect as a poorly regulated supply, increase ripple on the supply line also reducing the peak output power. The effects of residual trace resistance increases as output current increases due to higher output power, decreased load impedance or both. To maintain the highest output voltage swing and corresponding peak output power, the PCB traces that connect the output pins to the load and the supply pins to the power supply should be as wide as possible to minimize trace resistance.

The use of power and ground planes will give the best THD+N performance. While reducing trace resistance, the use of power planes also creates parasite capacitors that help to filter the power supply line.

The inductive nature of the transducer load can also result in overshoot on one or both edges, clamped by the parasitic diodes to GND and V_{DD} in each case. From an EMI stand-point, this is an aggressive waveform that can radiate or conduct to other components in the system and cause interference. It is essential to keep the power and output traces short and well shielded if possible. Use of ground planes, beads, and micro-strip layout techniques are all useful in preventing unwanted interference.

As the distance from the LM4890 and the speaker increase, the amount of EMI radiation will increase since the output wires or traces acting as antenna become more efficient with length. What is acceptable EMI is highly application specific.

Ferrite chip inductors placed close to the LM4890 may be needed to reduce EMI radiation. The value of the ferrite chip is very application specific.