



40V P-CHANNEL ENHANCEMENT MODE MOSFET POWERDI[®]

Product Summary

| V _{(BR)DSS} | R _{DS(ON)} Max | Ι _D T _A = +25°C |
|----------------------|------------------------------|--|
| -40V | $11m\Omega @ V_{GS} = -10V$ | -17.0A |
| | $15m\Omega @ V_{GS} = -4.5V$ | -14.5A |

Description

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Analog Switch

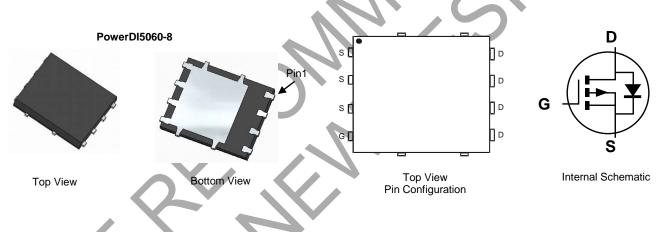
Notes:

Features and Benefits

- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low On-Resistance
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: PowerDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—100% Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (€3)
- Weight: 0.097 grams (Approximate)



Ordering Information (Note 5)

| Part Number | C | ompliance | Case | Packaging |
|----------------|---|-----------|---------------|-------------------|
| DMP4015SPSQ-13 | A | utomotive | PowerDI5060-8 | 2,500/Tape & Reel |
| | | | | |

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free

2. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.



Marking Information



>`` = Manufacturer's Marking P4015SP = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 18 = 2018) WW = Week (01 - 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Units | | |
|--|-----------------|--|------------------|----------------|----|
| Drain-Source Voltage | | | VDSS | -40 | V |
| Gate-Source Voltage | | | V _{GSS} | ±25 | V |
| | Steady State | T _A = +25°C T _A = +70°C | lo | -8.5 -6.8 | А |
| Continuous Drain Current (Note 6) V _{GS} = -10V | t<10s | T _A = +25°C T _A = +70°C | ID | -13.0 -10.5 | А |
| Continuous Daris Current (Nate 7))/ 401/ | Steady State | T _A = +25°C T _A = +70°C | ID | -11.0 -8.7 | А |
| Continuous Drain Current (Note 7) V _{GS} = -10V | t<10s | T _A = +25°C T _A = +70°C | lD | -17.0 -13.5 | А |
| Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) | | | IDM | -100 | A |
| Maximum Body Diode Continuous Current (Note 7) | | | ls | -3.5 | A |
| Avalanche Current (Note 8) | | | I _{AS} | -22 | A |
| Avalanche Energy (Note 8) | | | E _{AS} | 242 | mJ |

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | | Symbol | Value | Units |
|--|------------------------|----------------------------------|-------------|-------|
| Total Dower Discipation (Note 6) | $T_A = +25^{\circ}C$ | Р | 1.3 | W |
| Total Power Dissipation (Note 6) | $T_A = +70^{\circ}C$ | PD | 0.8 | |
| Thermal Resistance, Junction to Ambient (Note 6) | Steady State | R _{ÐJA} | 96.4 | °C/W |
| Thermal Resistance, Sunction to Ambient (Note 0) | t<10s | Reja | 40.6 | °C/W |
| Total Power Dissipation (Note 7) | $T_A = +25^{\circ}C$ | D - | 2.1 | W |
| Total Power Dissipation (Note 7) | T _A = +70°C | PD | 1.4 | |
| Thermal Resistance, Junction to Ambient (Note 7) | Steady State | Devi | 55.0 | °C/W |
| Thermal Resistance, Junction to Ambient (Note 7) | t<10s | R _{ÐJA} | 24.0 | °C/W |
| Thermal Resistance, Junction to Case (Note 7) | | R _{eJC} | 4.15 | °C/W |
| Operating and Storage Temperature Range | | T _{J,} T _{STG} | -55 to +150 | °C |

Notes: 6. Device mounted on FR-4 PCB, with minimum recommended pad layout, single sided.

7. Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1inch square copper plate. 8. UIS in production with L = 0.1mH, TJ = +25°C.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

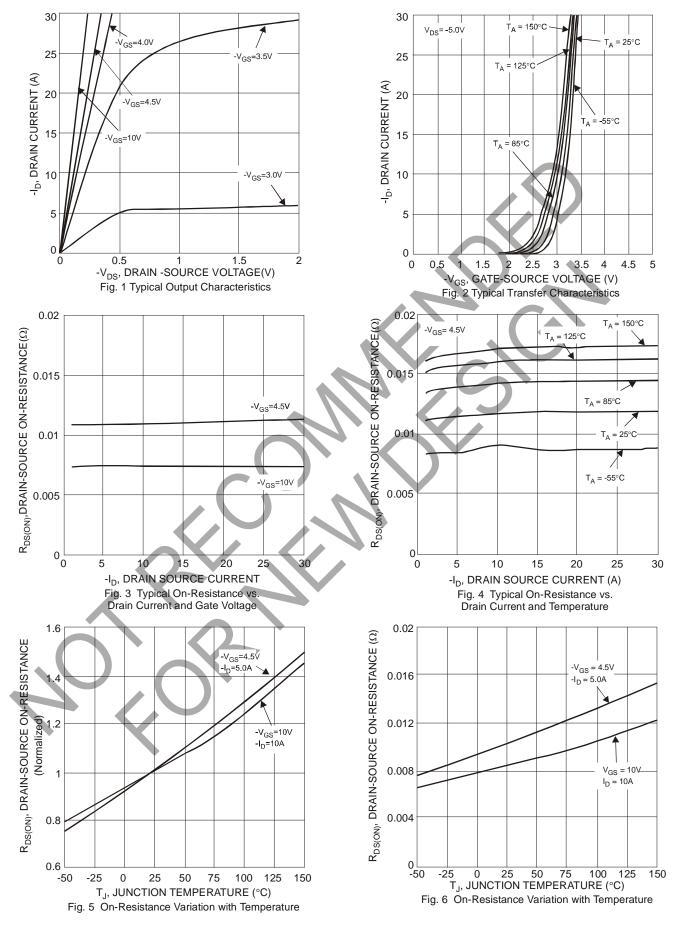
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|-----------------------------------|---------------------|------|-------|------------|-------|--|
| OFF CHARACTERISTICS (Note 9) | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | -40 | — | _ | V | $V_{GS} = 0V, I_D = -250\mu A$ |
| Zero Gate Voltage Drain Current | IDSS | — | — | -1 | μA | $V_{DS} = -40V, V_{GS} = 0V$ |
| Gate-Source Leakage | I _{GSS} | _ | — | ±100 | nA | $V_{GS} = \pm 25 V$, $V_{DS} = 0 V$ |
| ON CHARACTERISTICS (Note 9) | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | -1.5 | -2 | -2.5 | V | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$ |
| Static Drain-Source On-Resistance | Б | — | 7 | 11 | mΩ | V _{GS} = -10V, I _D = -9.8A |
| | R _{DS(ON)} | _ | 9 | 15 | 11152 | $V_{GS} = -4.5V, I_{D} = -9.8A$ |
| Forward Transfer Admittance | Y _{fs} | _ | 26 | — | S | $V_{DS} = -20V, I_{D} = -9.8A$ |
| Diode Forward Voltage | V _{SD} | _ | -0.7 | -1 | V | $V_{GS} = 0V, I_{S} = -1A$ |
| DYNAMIC CHARACTERISTICS (Note 10) | | | | | | |
| Input Capacitance | C _{iss} | — | 4,234 | — | | |
| Output Capacitance | Coss | _ | 1,036 | — | pF | $V_{DS} = -20V$, $V_{GS} = 0V$ f = 1MHz |
| Reverse Transfer Capacitance | C _{rss} | _ | 526 | — | | |
| Gate Resistance | R _G | _ | 7.77 | — | Ω | $V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$ |
| Total Gate Charge | Qg | _ | 47.5 | | | |
| Gate-Source Charge | Q _{gs} | _ | 14.2 | | nC | $V_{DS} = -20V, V_{GS} = -5V$ |
| Gate-Drain Charge | Q _{gd} | _ | 13.5 | | | I _D = -9.8A |
| Turn-On Delay Time | t _{D(on)} | _ | 13.2 | — | | |
| Turn-On Rise Time | tr | _ | 10 | - | | $V_{GS} = -10V, V_{DD} = -20V, R_G = 6\Omega,$ |
| Turn-Off Delay Time | t _{D(off)} | - | 302.7 | V - | ns | $I_{D} = -1A, R_{L} = 20\Omega$ |
| Turn-Off Fall Time | t _f | | 137.9 | _ | | |

Notes:

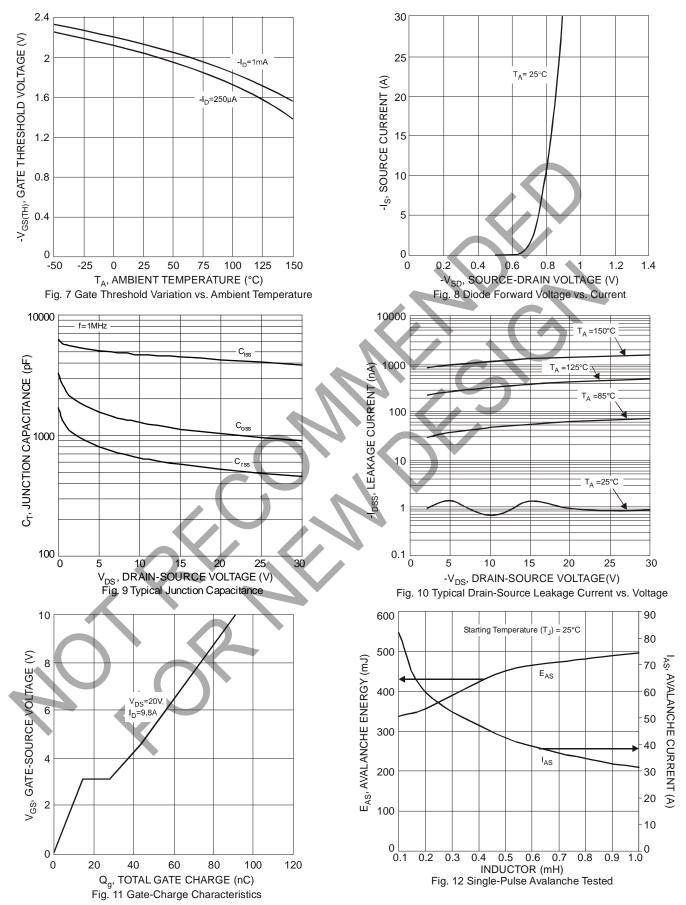
Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.



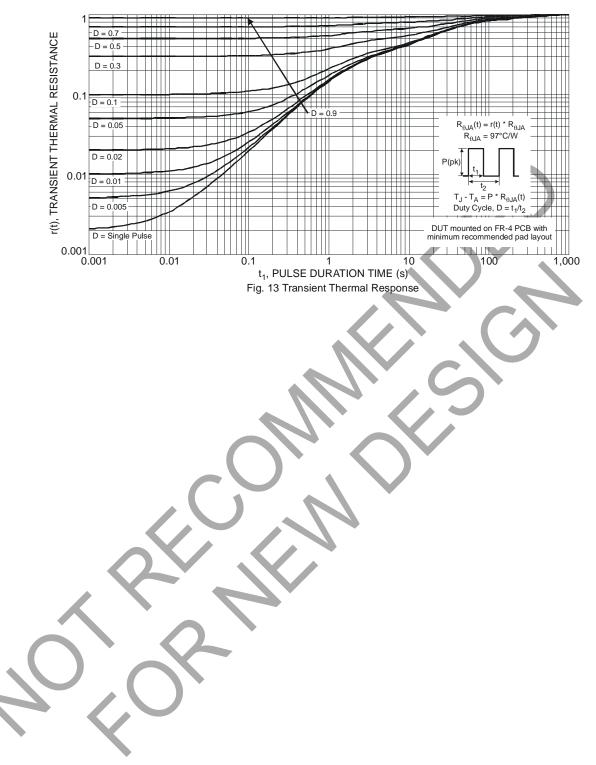
DMP4015SPSQ







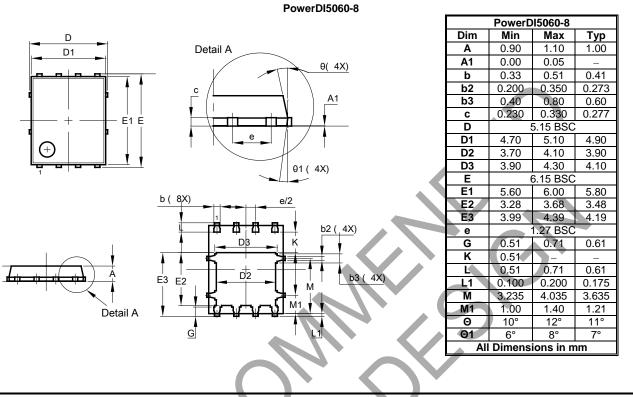






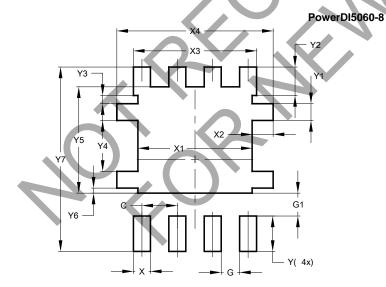
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



| Dimensions | Value (in mm) |
|------------|---------------|
| С | 1.270 |
| G | 0.660 |
| G1 | 0.820 |
| Х | 0.610 |
| X1 | 4.100 |
| X2 | 0.755 |
| X3 | 4.420 |
| X4 | 5.610 |
| Y | 1.270 |
| Y1 | 0.600 |
| Y2 | 1.020 |
| Y3 | 0.295 |
| Y4 | 1.825 |
| Y5 | 3.810 |
| Y6 | 0.180 |
| Y7 | 6.610 |



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