



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON) max}	I _{D MAX} T _A = +25°C
	$48m\Omega$ @ $V_{GS} = -4.5V$	-3.8A
-12V	$59mΩ @ V_{GS} = -2.5V$	-3.4A
	$80m\Omega$ @ $V_{GS} = -1.8V$	-2.9A

Description

This new generation MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.





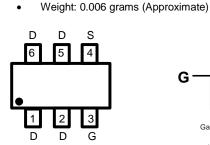
Top View

Features

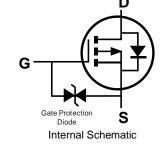
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 <a>@3
- Terminal Connections: See Diagram



Top View Pin out



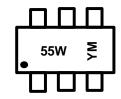
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP1055USW-7	SOT363	3,000/Tape & Reel
DMP1055USW-13	SOT363	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



55W = Product Type Marking Code YM = Date Code Marking Y or Y = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date Code Key

Year	2016		2017	2018		2019	2020		2021	2022		2023
Code	D		Е	F		G	Н		I	J		K
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V_{DSS}	-12	V
Gate-Source Voltage		V _{GSS}	±8	V
Continuous Drain Current (Note 6) V _{GS} = -4.5V	I _D	-3.8 -3.0	Α	
Maximum Continuous Body Diode Forward Curre	ent (Note 6)	Is	-1.7	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1	%)	I_{DM}	-20	А

Thermal Characteristics

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	P_{D}	0.66	W	
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	192	°C/W	
Total Power Dissipation (Note 6)		P _D	1.03	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	123	°C/W
Thermal Resistance, Junction to Case		Rejc	39	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

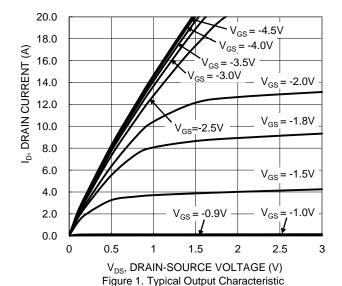
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	Symbol	IVIIII	тур	IVIAX	Onit	rest condition
Drain-Source Breakdown Voltage	BV _{DSS}	-12	-	-	V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	-	-	-1.0	μA	V _{DS} = -12V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±10	μA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)				I .		
Gate Threshold Voltage	V _{GS(TH)}	-0.4	-	-1	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
		-	41	48		$V_{GS} = -4.5V, I_{D} = -3.0A$
Static Drain-Source On-Resistance		-	49	59	mΩ	$V_{GS} = -2.5V, I_D = -1.0A$
Static Drain-Source On-Resistance	R _{DS(ON)}	-	69	80	11122	$V_{GS} = -1.8V, I_{D} = -1.0A$
		-	110	150		$V_{GS} = -1.5V, I_D = -0.5A$
Diode Forward Voltage	V _{SD}	-	-0.7	-1.2	V	$V_{GS} = 0V, I_S = -3.7A$
DYNAMIC CHARACTERISTICS (Note 8)	•					
Input Capacitance	C _{iss}	-	1,028	-	рF	.,
Output Capacitance	Coss	ı	285	-	рF	$V_{DS} = -6V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	-	254	-	pF	1 = 1.000112
Gate Resistance	R_{g}	-	19.6	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = -4.5V)	0	-	13	-	nC	
Total Gate Charge (V _{GS} = -8V)	Qg	-	20.8	-	nC	10)/ 1 4.74
Gate-Source Charge	Qgs	-	1.8	-	nC	$V_{DS} = -10V, I_{D} = -4.7A$
Gate-Drain Charge	Q_{gd}	-	4.5	-	nC	
Turn-On Delay Time	t _{D(ON)}	-	5.6	-	ns	
Turn-On Rise Time	t _R	-	12.8	-	ns	$V_{DD} = -6V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t _{D(OFF)}	-	30.7	-	ns	$R_L = 1.6\Omega$, $R_G = 1\Omega$
Turn-Off Fall Time	t _F	-	25.4	-	ns	
Body Diode Reverse Recovery Time	t _{RR}	-	31.6	-	ns	I _S = -3.6A, dI/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q_{RR}	-	7.8	-	nC	I _S = -3.6A, dI/dt = 100A/μs

Notes:

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.





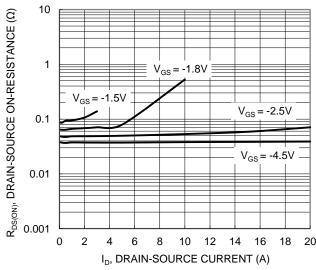


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

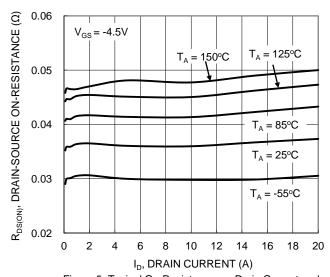
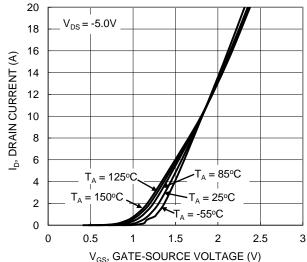


Figure 5. Typical On-Resistance vs. Drain Current and Temperature



V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

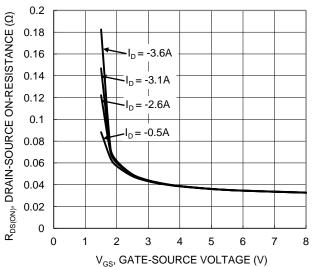


Figure 4. Typical Transfer Characteristic

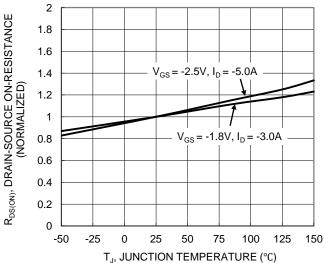


Figure 6. On-Resistance Variation with Temperature



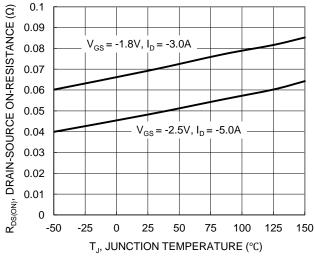
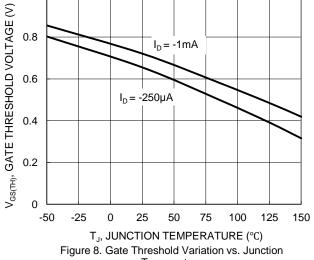


Figure 7. On-Resistance Variation with Temperature



1

Temperature

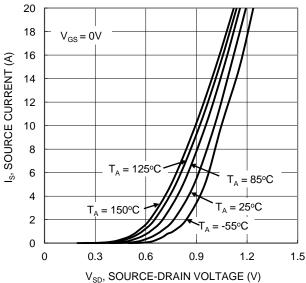
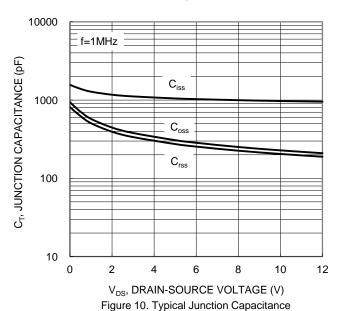


Figure 9. Diode Forward Voltage vs. Current



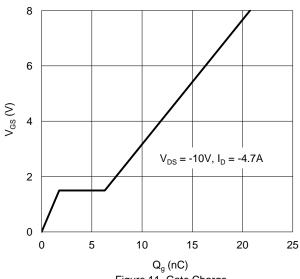


Figure 11. Gate Charge

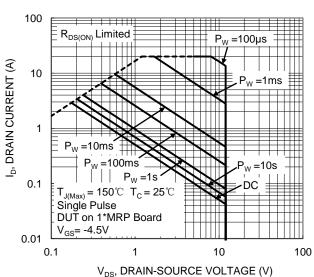


Figure 12. SOA, Safe Operation Area



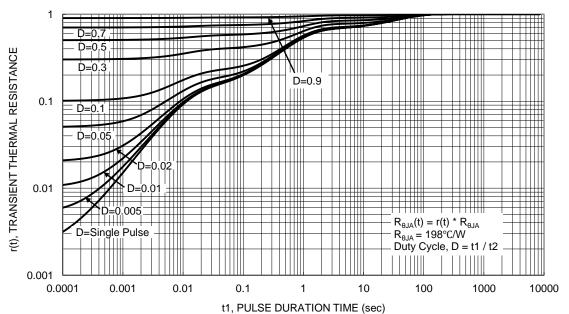


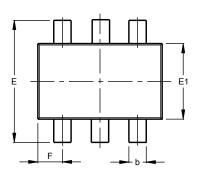
Figure 13. Transient Thermal Resistance

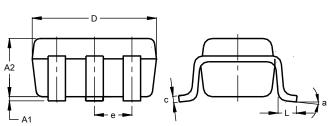


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



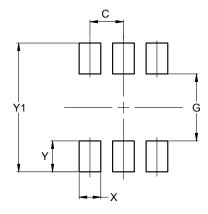


	SOT363						
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	1.00				
b	0.10	0.30	0.25				
С	0.10	0.22	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	0.650 BSC						
F	0.40	0.45	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



Dimensions	Value (in mm)				
С	0.650				
G	1.300				
Х	0.420				
Y	0.600				
Y1	2.500				



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