



N-CHANNEL ENHANCEMENT MODE FIELD MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)}	Ι _D T _A = +25°C	
	3.0Ω @ V _{GS} = 10V	400mA	
60V	4.0Ω @ V _{GS} = 5V	330mA	

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, which makes it ideal for high-efficiency power-management applications.

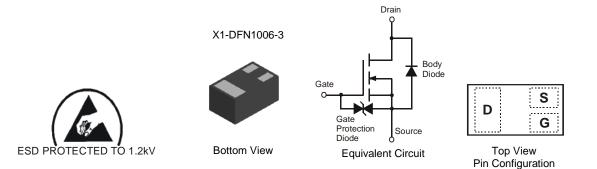
- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

Features and Benefits

- N-Channel MOSFET
- Low On-Resistance
- Low Gate-Threshold Voltage
- Low-Input Capacitance
- Fast Switching Speed
- Small-Surface Mount Package
- ESD Protected Gate, 1.2kV HBM
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish—NiPdAu over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.001 grams (Approximate)



Ordering Information (Note 4)

Notes:

Part Number	Case	Packaging
DMN65D8LFB-7	X1-DFN1006-3	3000/Tape & Reel
DMN65D8LFB-7B	X1-DFN1006-3	10,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

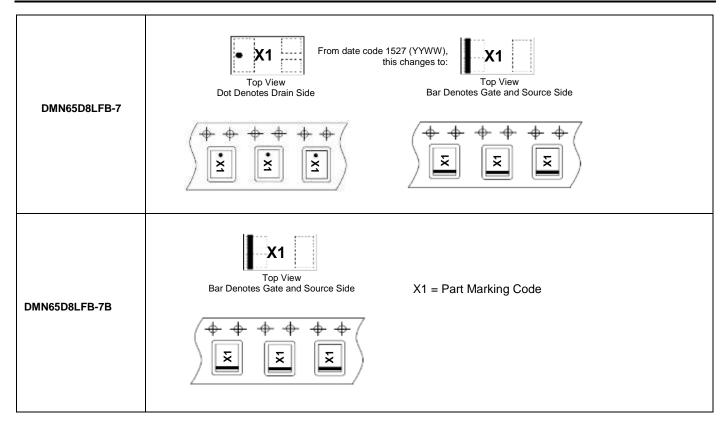
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, refer to our website at http://www.diodes.com.



Marking Information





Maximum Ratings

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 4) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	ID	260 210	mA
Continuous Drain Current (Note 5) V_{GS} = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	400 310	mA

Thermal Characteristics

Characteristic	Symbol	Value	Units
Power Dissipation, @ $T_A = +25^{\circ}C$ (Note 4)	PD	430	mW
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 4)	R _{ÐJA}	290	°C/W
Power Dissipation, @ $T_A = +25^{\circ}C$ (Note 5)	PD	840	mW
Thermal Resistance, Junction to Ambient @ $T_A = +25^{\circ}C$ (Note 5)	R _{ØJSA}	147	°C/W
Operating and Storage Temperature Range	TJ, T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

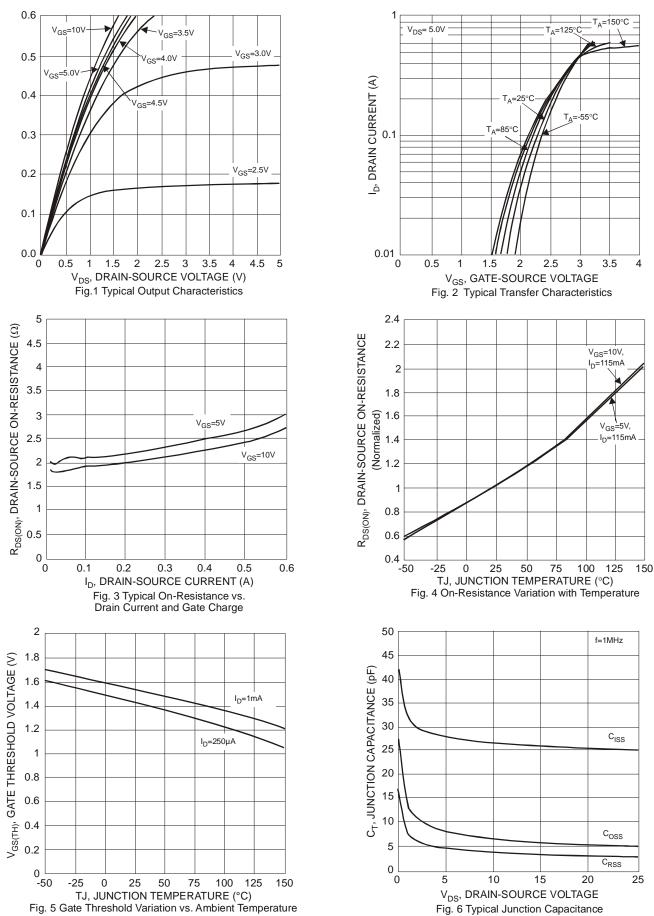
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	—	0.1	μA	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Body Leakage	I _{GSS}	—	—	±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)			-				
Gate Threshold Voltage	V _{GS(th)}	1.2	—	2.0	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS (ON)}	_	_	3.0 4.0	Ω	$V_{GS} = 10V, I_D = 0.115A$ $V_{GS} = 5V, I_D = 0.115A$	
Forward Transfer Admittance	Y _{fs}	80	320	_	mS	V _{DS} = 10V, I _D = 0.115A	
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	V _{GS} = 0V, I _S = 0.115A	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C _{iss}	_	25	_	pF		
Output Capacitance	Coss	—	4.7	_	pF	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	Crss	—	2.5	—	pF	1	
Turn-On Delay Time	t _{D(on)}	_	3.27	—	ns		
Turn-On Rise Time	tr	_	3.15	_	ns	$V_{DD} = 30V, V_{GEN} = 10V,$	
Turn-Off Delay Time	t _{D(off)}	—	12.025	_	ns	$R_{GEN} = 25\Omega, I_D = 0.115A$	
Turn-Off Fall Time	tf	_	6.29	_	ns	1	

Notes:

Device mounted on FR-4 PCB with minimum recommended pad layout, single-sided.
Device mounted on 2" x 2" FR-4 PCB with high coverage 2oz. Copper, single-sided.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing.

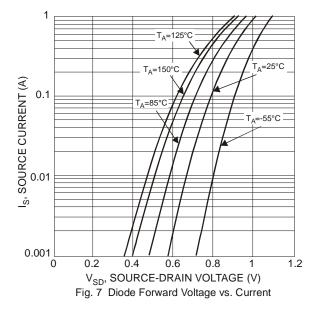


DMN65D8LFB



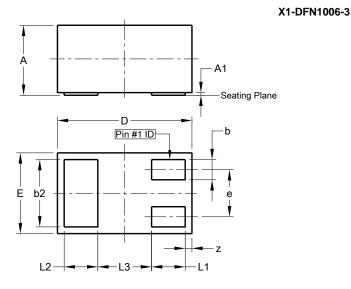


DMN65D8LFB



Package Outline Dimensions

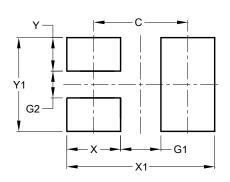
Please see http://www.diodes.com/package-outlines.html for the latest version.



X1-DFN1006-3 Dim Min Max Typ Α 0.47 0.53 0.50 A1 0.00 0.05 0.03 0.10 0.20 0.15 b b2 0.45 0.55 0.50 D 0.95 1.075 1.00 Ε 0.55 0.675 0.60 е 0.35 -L1 0.20 0.30 0.25 L2 0.20 0.30 0.25 0.40 L3 z 0.02 0.08 0.05 All Dimensions in mm

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



X1-DFN1006-3

Dimensions	Value (in mm)
С	0.70
G1	0.30
G2	0.20
Х	0.40
X1	1.10
Y	0.25
Y1	0.70

DMN65D8LFB Document number: DS35545 Rev. 4 - 2



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