



DMN601DWK

#### **DUAL N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR**

### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C		
60V	$3\Omega$ @ $V_{GS} = 5V$	0.3A		

### **Description**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

#### **Applications**

- Motor Control
- Power Management Functions

#### **Features**

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected Up To 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- · Qualified to AEC-Q101 Standards for High Reliability

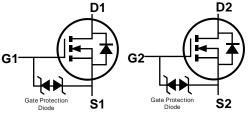
#### **Mechanical Data**

- Case: SOT363
- Case Material: Molded Plastic. "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead-Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)

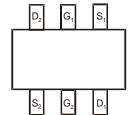




Top View



Equivalent Circuit



Top View Internal Schematic

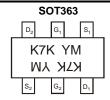
### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMN601DWK-7	SOT363	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**



K7K = Product Type Marking CodeYM = Date Code Marking $Y or <math>\overline{Y} = Year (ex: C = 2015)$  M = Month (ex: 9 = September)

Date Code Key

Year	2005	2006		2015	2016	2017 2	2018	2019	2020	2021	2022	2023	2024
Code	S	Т		С	D	E	F	G	Н		J	K	L
Month	Jan	Feb	Mar	Apr	May	Jun		Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6		7	8	9	0	N	D



### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain Source Voltage		$V_{DSS}$	60	V
Gate-Source Voltage		$V_{GSS}$	±20	V
Drain Current (Note 5)	Continuous	I-	305	m A
Diam Current (Note 5)	Pulsed (Note 6)	ID	800	mA

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	200	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	625	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

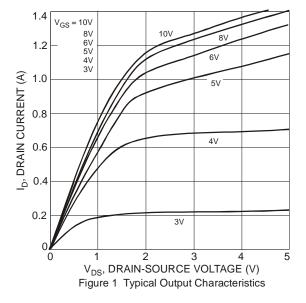
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

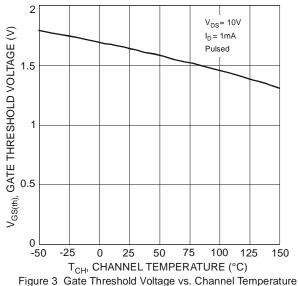
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	eyze.		. , , ,	max	0	Tool Collamon
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_		V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0	1.6	2.5	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA
Static Drain-Source On-Resistance	Paggara		_	2.0	Ω	$V_{GS} = 10V, I_D = 0.5A$
Static Dialif-Source Off-Nesistance	R <sub>DS(ON)</sub>		—	3.0	\$2	$V_{GS} = 5V, I_D = 0.05A$
Forward Transfer Admittance	Y <sub>fs</sub>	80	_		ms	$V_{DS} = 10V, I_D = 0.2A$
Diode Forward Voltage (Note 8)	$V_{SD}$	0.5	_	1.4	V	$V_{GS} = 0V, I_S = 115mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>		30	50	pF	
Output Capacitance	Coss	_	4.2	25	pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	_	2.9	5.0	pF	1 - 1.00012
Gate Resistance	$R_g$	_	133	_	Ω	$f = 1MHz$ , $V_{GS} = 0V$ , $V_{DS} = 0V$
Total Gate Charge	$Q_g$		304	_	рC	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
Gate-Source Charge	Q <sub>gs</sub>	_	203	_	рC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_{D} = 250 \text{mA}$
Gate-Drain Charge	Q <sub>gd</sub>	_	84	_	рС	1D = 23011A
Turn-On Delay Time	t <sub>D(on)</sub>		3.9	_	nS	
Turn-On Rise Time	t <sub>r</sub>	_	3.4	_	nS	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	t <sub>D(off)</sub>	_	15.7	_	nS	$R_G = 25\Omega$ , $I_D = 200mA$
Turn-Off Fall Time	t <sub>f</sub>	_	9.9	_	nS	

Notes:

- 5. Device mounted on FR-4 PCB.
- 6. Pulse width ≤10µS, duty cycle ≤1%.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.







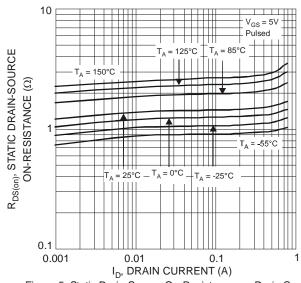
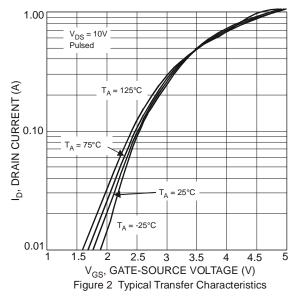


Figure 5 Static Drain-Source On-Resistance vs. Drain Current



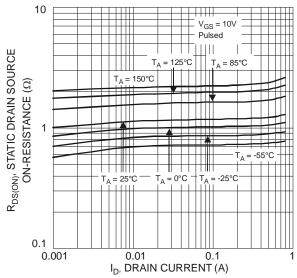


Figure 4 Static Drain-Source On-Resistance vs. Drain Current

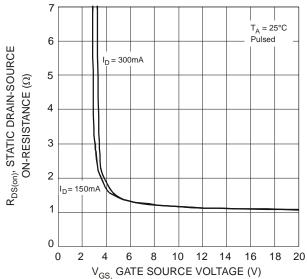


Figure 6 Static Drain-Source On-Resistance vs. Gate-Source Voltage



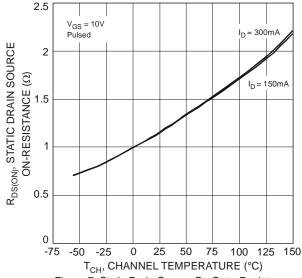


Figure 7 Static Drain-Source On-State Resistance vs. Channel Temperature

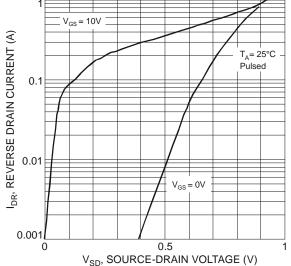
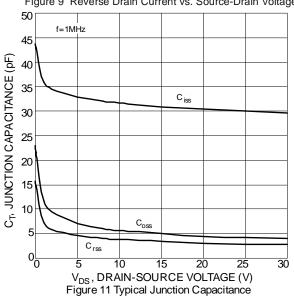


Figure 9 Reverse Drain Current vs. Source-Drain Voltage



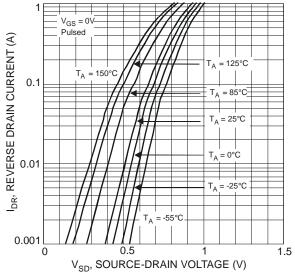


Figure 8 Reverse Drain Current vs. Source-Drain Voltage

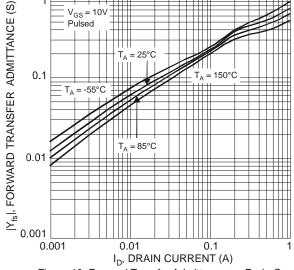
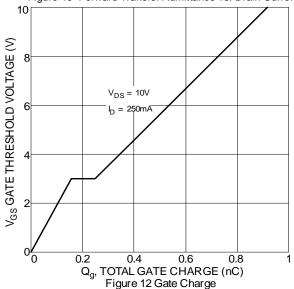
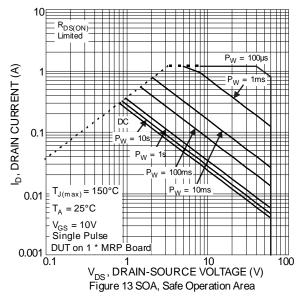


Figure 10 Forward Transfer Admittance vs. Drain Current







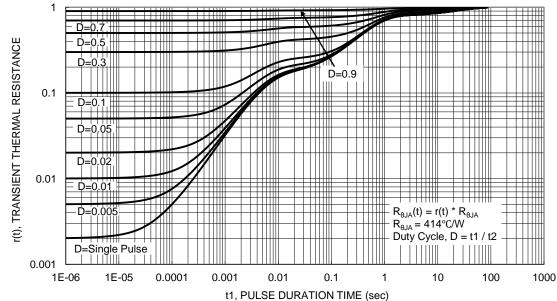
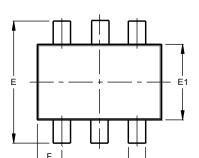


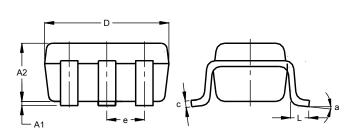
Figure 14 Transient Thermal Resistance



# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.





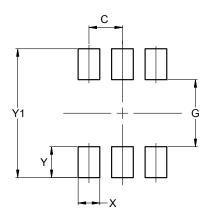
SOT363						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	1.00			
b	0.10	0.30	0.25			
C	0.10	0.22	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	0.650 BSC					
F	0.40	0.45	0.425			
L	0.25	0.40	0.30			
а	8°					
All Dimensions in mm						

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

#### SOT363

**SOT363** 



Dimensions	Value (in mm)				
С	0.650				
G	1.300				
Х	0.420				
Y	0.600				
Y1	2.500				



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