

pF

°C/W

°C

°C

R2MF 2.0AMPS . GLASS PASSIVATED FAST RECOVERY RECTIFIERS **FEATURE** SMF . Fast switching . High current capability .108(2.75) 096(2.45) .059(1.5) .051(1.3) . Low forward voltage drop . Low power loss, high efficiency . High temperature soldering guaranteed: 260° C/10 seconds at terminals. .173(4.40) . For surface mounted application .189(4.80) .132(3.35) . Easy pick and place 144(3.65) 006(0.15) $\left|\frac{.043(1.1)}{.059(1.5)}\right|$.010(0.25) **MECHANICAL DATA** . Case: Molded plastic . Epoxy: UL94V-0 rate flame retardant 4 .016(0.40) . Lead: MIL-STD- 202E, Method 208 guaranteed .028(0.70) .028(0.70) . Polarity: Marking band denotes cathode end .047(1.20). Packaging:12mm tape per EIA STD RS-481 Dimensions in inches and (millimeters) . Mounting position: Any MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS Ratings at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20% **SYM** Type Number R2MF units BOL Maximum Recurrent Peak Reverse Voltage 1000 V **V**_{RRM} **V**_{RMS} Maximum RMS Voltage 700 V V VDC 1000 Maximum DC blocking Voltage Maximum Average Forward Rectified Current 2.0А IF(AV) at $T_A = 55^{\circ}C$ Peak Forward Surge Current 8.3ms single half 60.0 sine-wave superimposed on rated load (JEDEC А **I**FSM method) Maximum Forward Voltage at 2.0A DC $V_{\rm F}$ 1.3 V 5.0 Maximum DC Reverse Current $@T_A = 25^{\circ}C$ $I_{\rm R}$ μA at rated DC blocking voltage $@T_A = 125^{\circ}C$ 100.0 Maximum Reverse Recovery Time (Note 1) 500 nS *t*rr

Test Conditions: *I*_F=0.5A, *I*_R=1.0A, *I*_{rr}=0.25A
Measured at 1.0 MHz and applied reverse voltage of 4.0Vdc

Typical Junction Capacitance (Note2)

Typical Thermal Resistance (Note 3)

Operation Junction Temperature

Storage Temperature

Note:

3. Measured on P. C. Board with $0.2 \times 0.2''(5.0 \times 5.0mm)$ Copper Pad Areas.

Cj

 $R_{(JA)}$

TSTG

TJ

30

50

-55 to +150-55 to +150



RATING AND CHARACTERISTIC CURVES (R2MF)

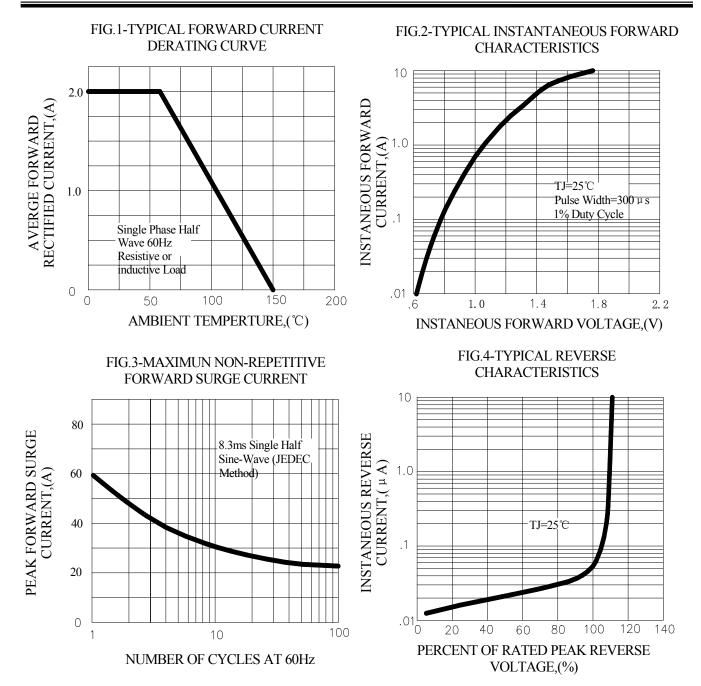


FIG.5-TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERSITIC

