

N-Channel Enhancement Mode Power MOSFET

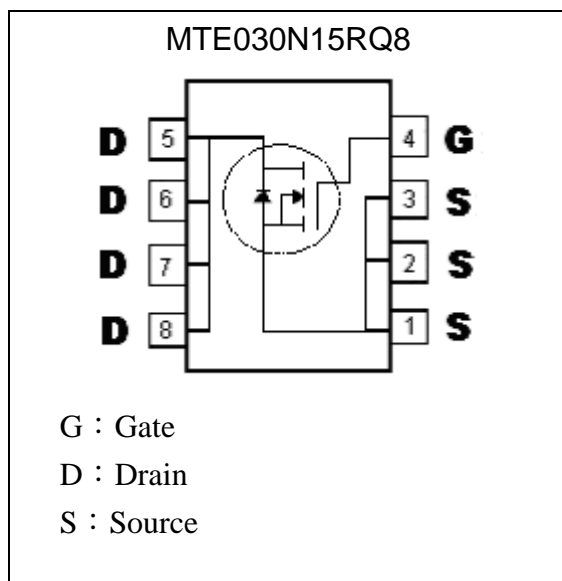
MTE030N15RQ8

BV_{DSS}	150V
I_D @ T_C=25°C, V_{GS}=10V	7.7A
I_D @ T_A=25°C, V_{GS}=10V	5.6A
R_{DS(ON)}@ V_{GS}=10V, I_D=6A	29.5 mΩ (typ)

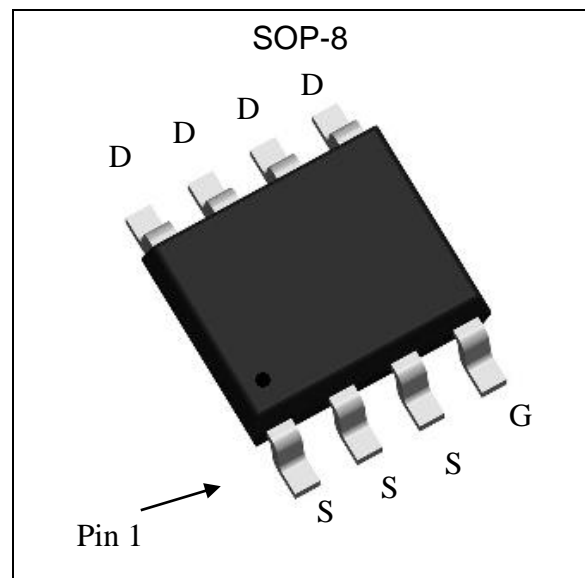
Features

- Single Drive Requirement
- Low On-resistance
- Fast Switching Characteristic
- Pb-free & Halogen-free package

Symbol

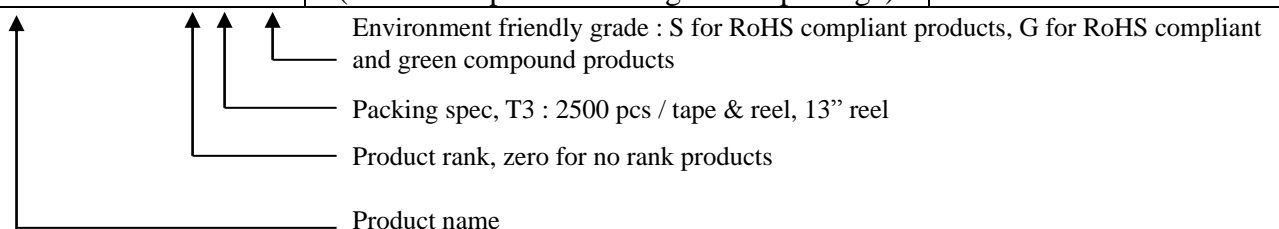


Outline



Ordering Information

Device	Package	Shipping
MTE030N15RQ8-0-T3-G	SOP-8 (RoHS compliant & Halogen-free package)	2500 pcs / Tape & Reel





Absolute Maximum Ratings (Tc=25°C, unless otherwise noted)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V _{DS}	150	V	
Gate-Source Voltage	V _{GS}	±30		
Continuous Drain Current @ T _c =25°C, V _{GS} =10V	I _D	7.7	A	
Continuous Drain Current @ T _c =70°C, V _{GS} =10V		6.1		
Continuous Drain Current @ T _A =25°C, V _{GS} =10V		5.6		
Continuous Drain Current @ T _A =70°C, V _{GS} =10V		4.5		
Pulsed Drain Current	I _{DM}	24 *1		
Avalanche Current @ L=0.1mH	I _{AS}	40		
Avalanche Energy @ L=10mH, I _D =6A, V _{DD} =50V	E _{AS}	180 *3	mJ	
Repetitive Avalanche Energy @ L=0.05mH	E _{AR}	1.6 *2		
Total Power Dissipation	P _D	T _A =25 °C	3.1	W
		T _A =70 °C	2	
Operating Junction and Storage Temperature	T _j , T _{stg}	-55~+150	°C	

Note : *1. Pulse width limited by maximum junction temperature
 *2. Duty cycle ≤ 1%
 *3. 100% tested by conditions of L=0.1mH, I_{AS}=6A, V_{GS}=10V, V_{DD}=25V

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case	R _{θJC}	20	°C/W
Thermal Resistance, Junction-to-ambient (Note)	R _{θJC}	40	

Note : 40°C / W when mounted on a 1 in² pad of 2 oz copper, t≤10s; 125°C/W when mounted on minimum pad.

Characteristics (Tc=25°C, unless otherwise specified)

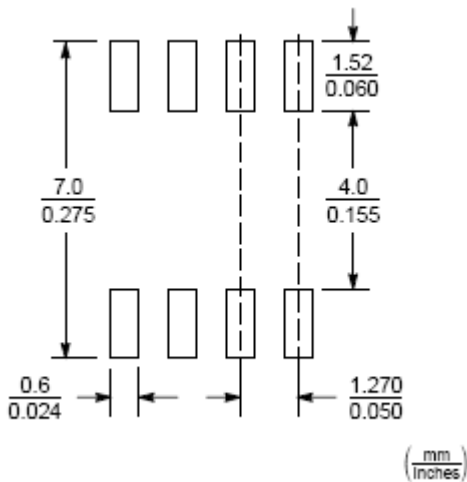
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	150	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	2	-	4		V _{DS} = V _{GS} , I _D =250μA
G _{FS}	-	10.4	-	S	V _{DS} =10V, I _D =5A
I _{GSS}	-	-	±100	nA	V _{GS} =±30V, V _{DS} =0V
I _{DSS}	-	-	1	μA	V _{DS} =120V, V _{GS} =0V
	-	-	25		V _{DS} =120V, V _{GS} =0V, T _j =125°C
*R _{DS(ON)}	-	29.5	38	mΩ	V _{GS} =10V, I _D =6A
Dynamic					
Q _g *1, 2	-	34.3	-	nC	V _{DS} =75V, V _{GS} =10V, I _D =6A
Q _{gs} *1, 2	-	8.6	-		
Q _{gd} *1, 2	-	9.1	-		
C _{iss}	-	1792	-	pF	V _{DS} =75V, V _{GS} =0V, f=1MHz
C _{oss}	-	106	-		
C _{rss}	-	8	-		

Characteristics (Cont. Tc=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Dynamic					
$t_{d(ON)}$ *1, 2	-	19.6	-	ns	$V_{DS}=75V, I_D=6A, V_{GS}=10V, R_{GS}=3\Omega$
t_r *1, 2	-	19.2	-		
$t_{d(OFF)}$ *1, 2	-	38.6	-		
t_f *1, 2	-	9	-		
Source-Drain Diode Ratings and Characteristics					
I_S *1	-	-	5.6	A	
I_{SM} *3	-	-	24		
V_{SD} *1	-	0.78	1.2	V	$I_S=6A, V_{GS}=0V$
t_{rr}	-	54	-	ns	$I_F=6A, dI_F/dt=100A/\mu s$
Q_{rr}	-	114	-	nC	

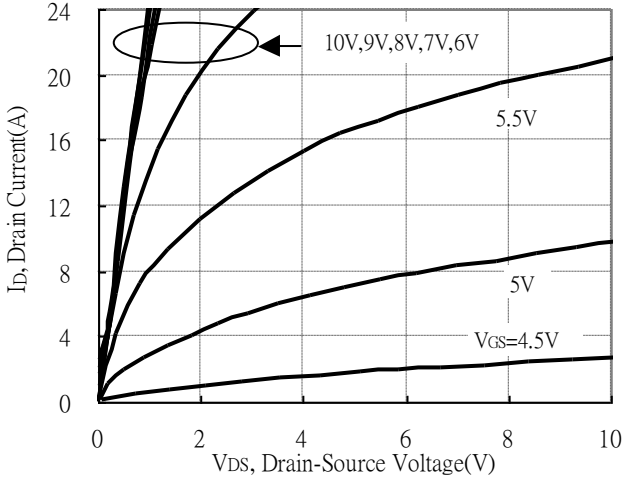
Note : *1.Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$
 *2.Independent of operating temperature
 *3.Pulse width limited by maximum junction temperature.

Recommended Soldering Footprint

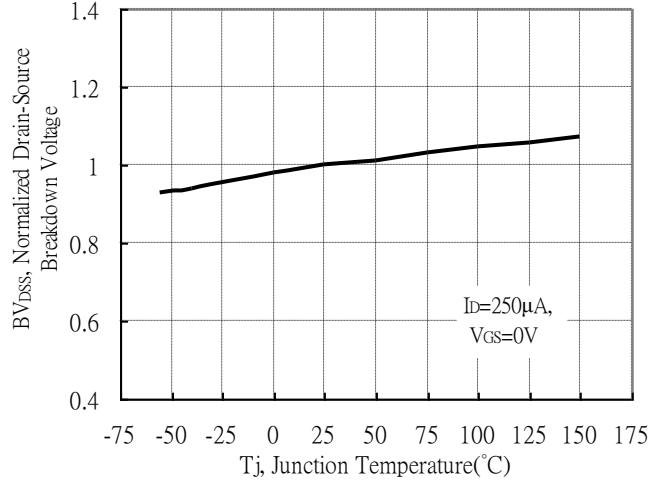


Typical Characteristics

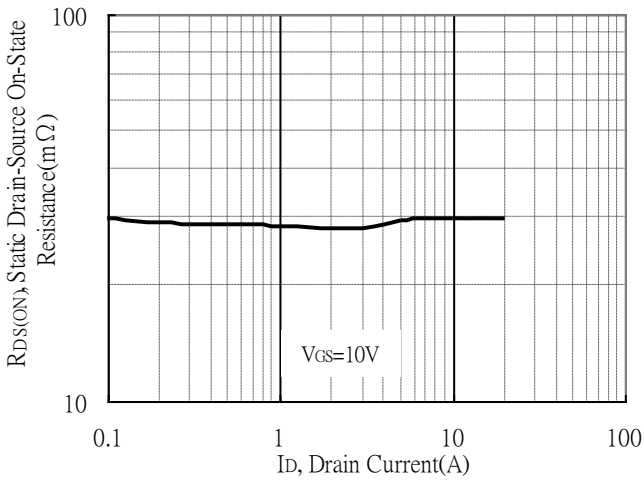
Typical Output Characteristics



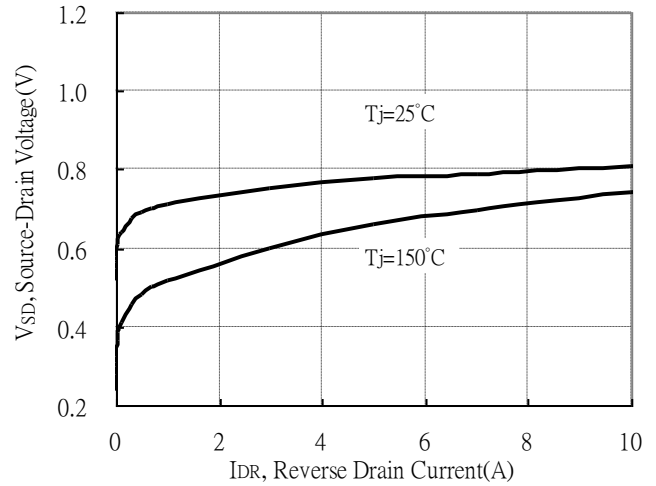
Brekdown Voltage vs Ambient Temperature



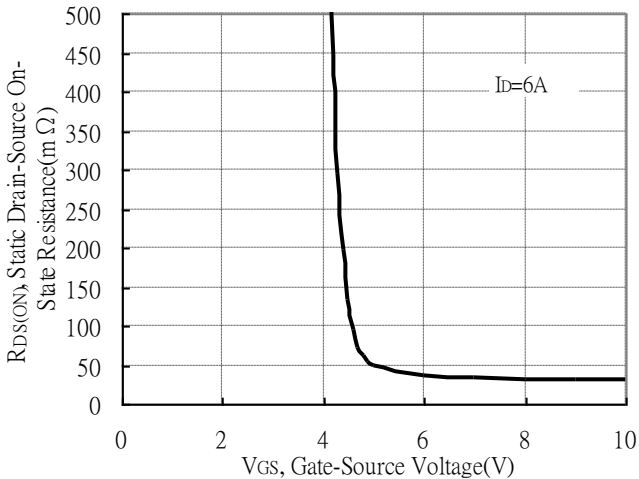
Static Drain-Source On-State resistance vs Drain Current



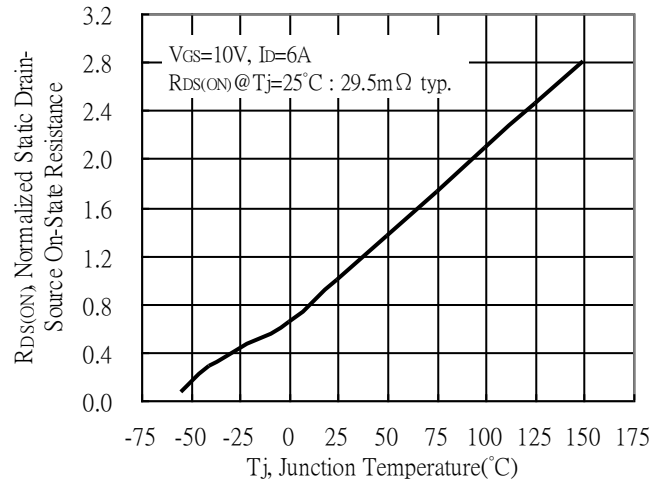
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

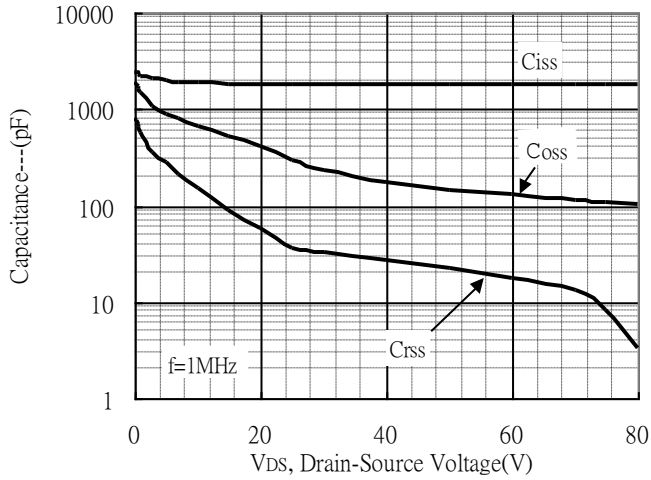


Drain-Source On-State Resistance vs Junction Temperature

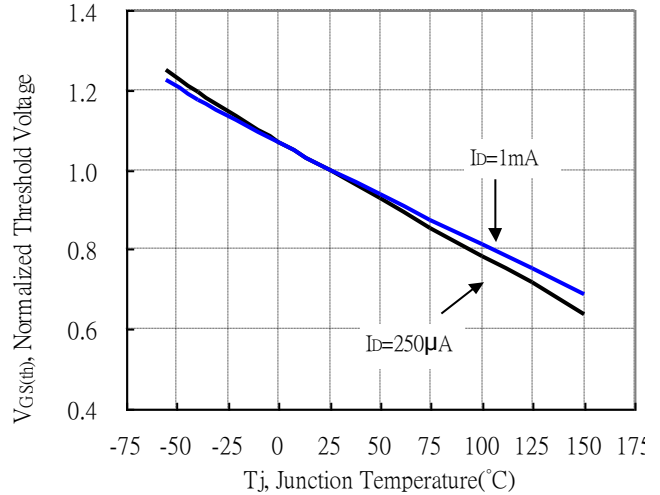


Typical Characteristics(Cont.)

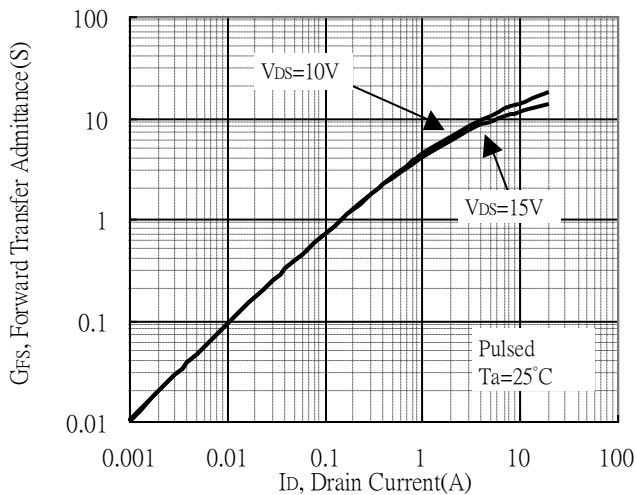
Capacitance vs Drain-to-Source Voltage



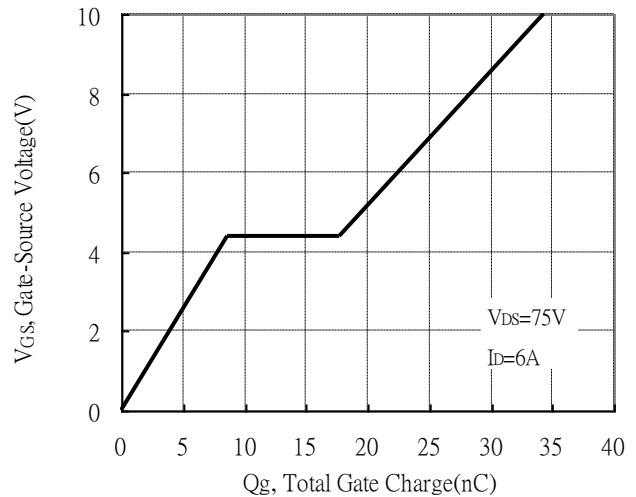
Threshold Voltage vs Junction Temperature



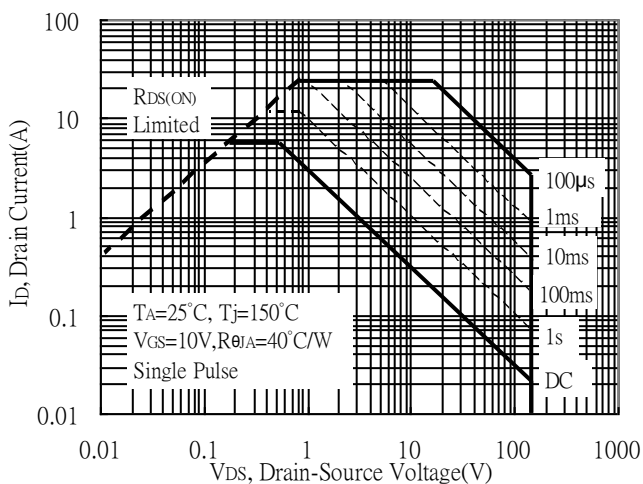
Forward Transfer Admittance vs Drain Current



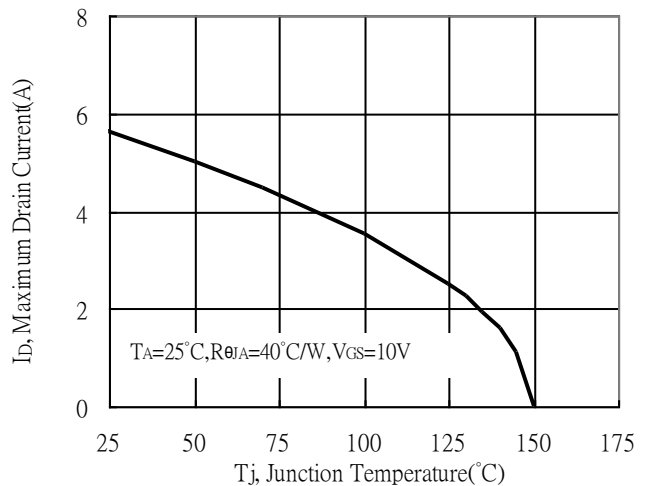
Gate Charge Characteristics



Maximum Safe Operating Area



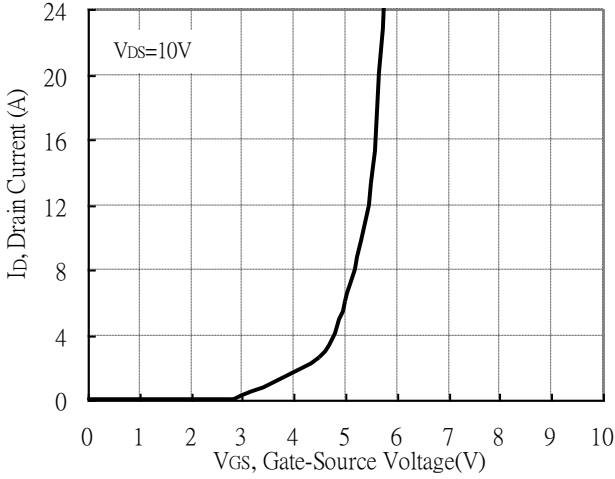
Maximum Drain Current vs Junction Temperature



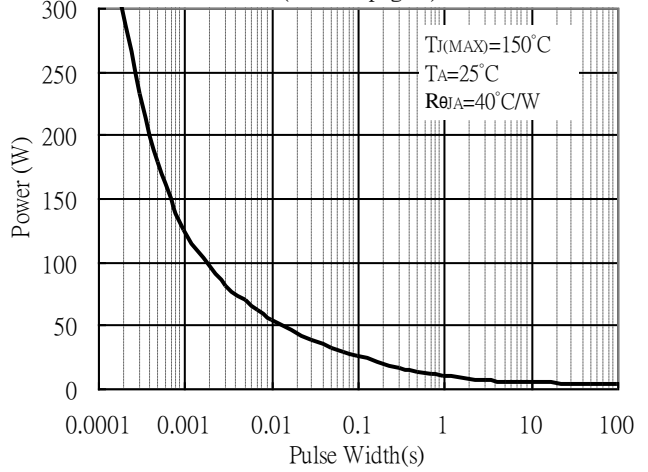


Typical Characteristics(Cont.)

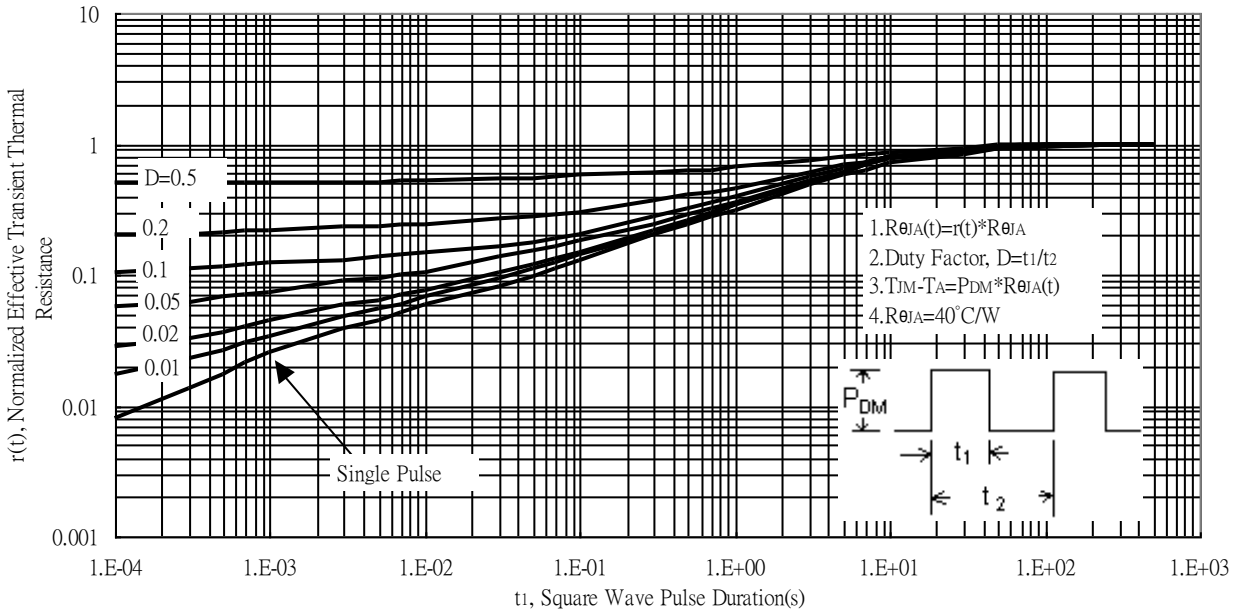
Typical Transfer Characteristics



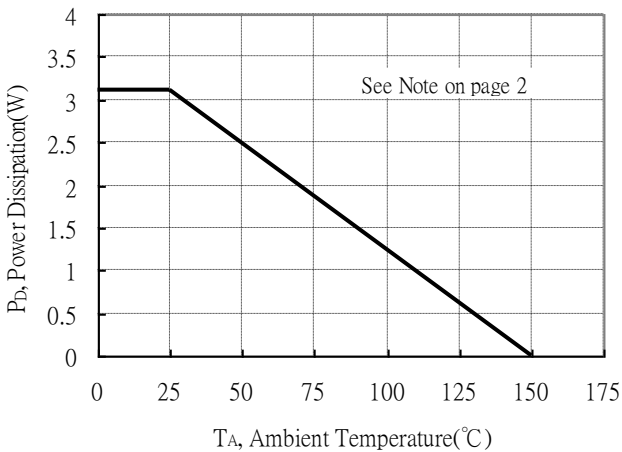
Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)



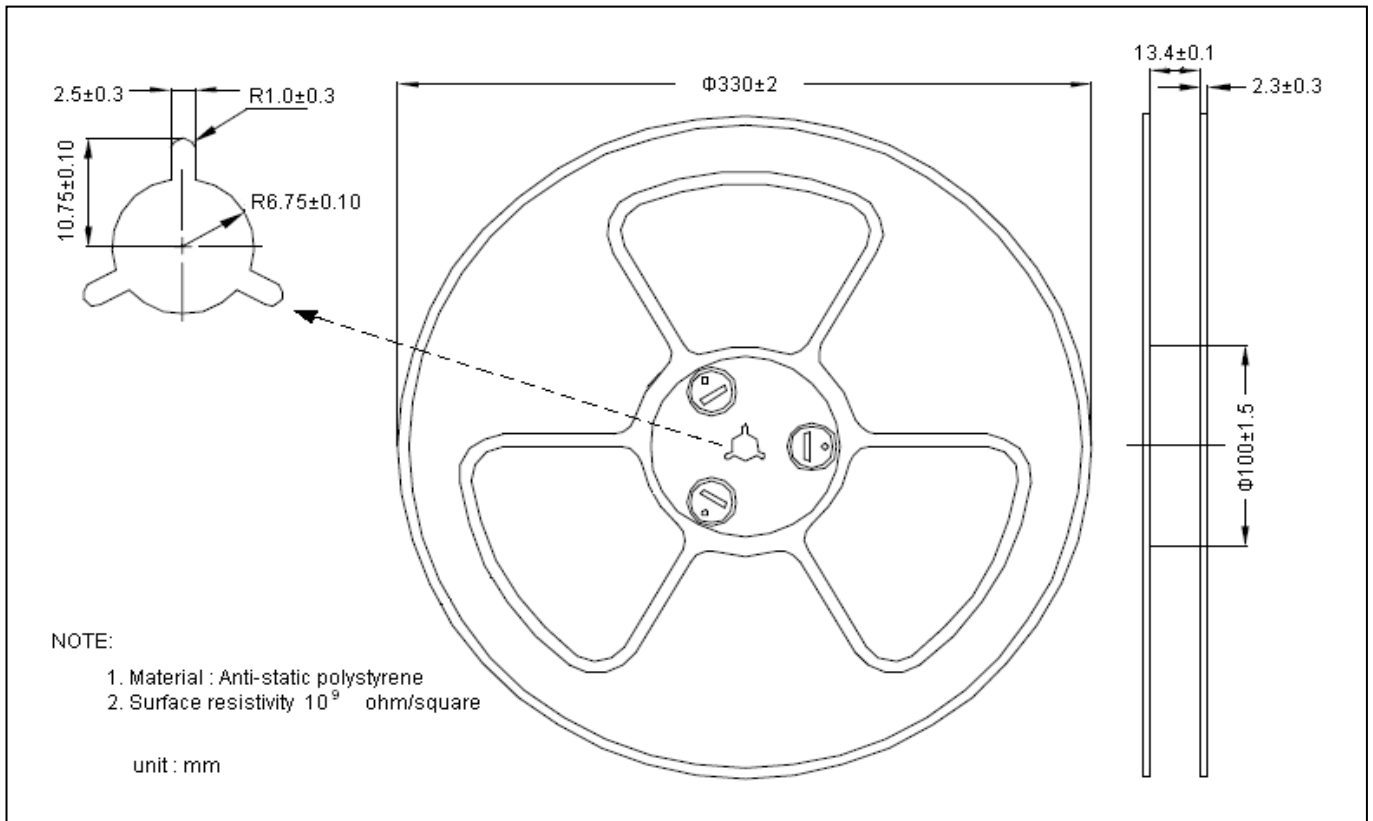
Transient Thermal Response Curves



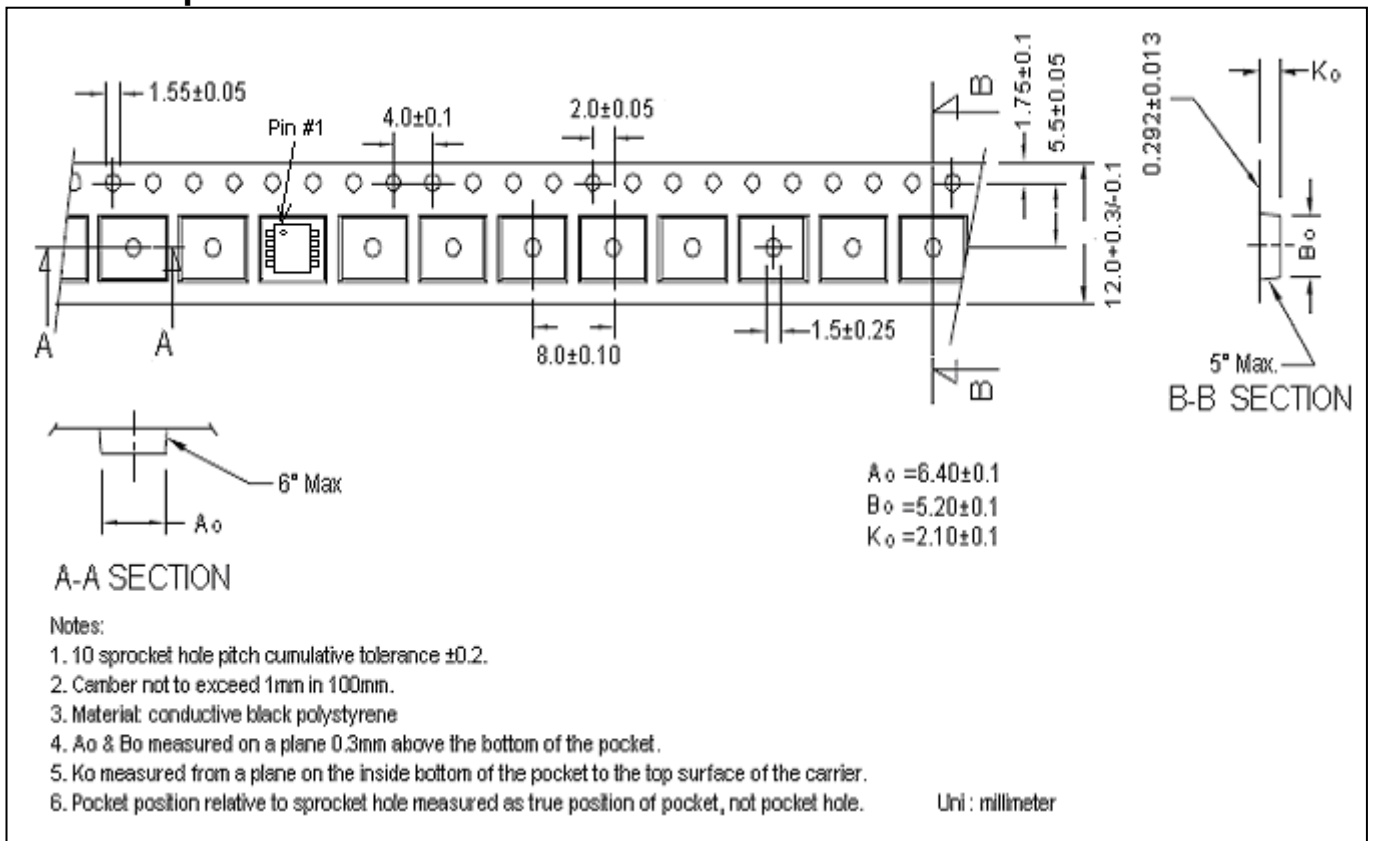
Power Derating Curve



Reel Dimension



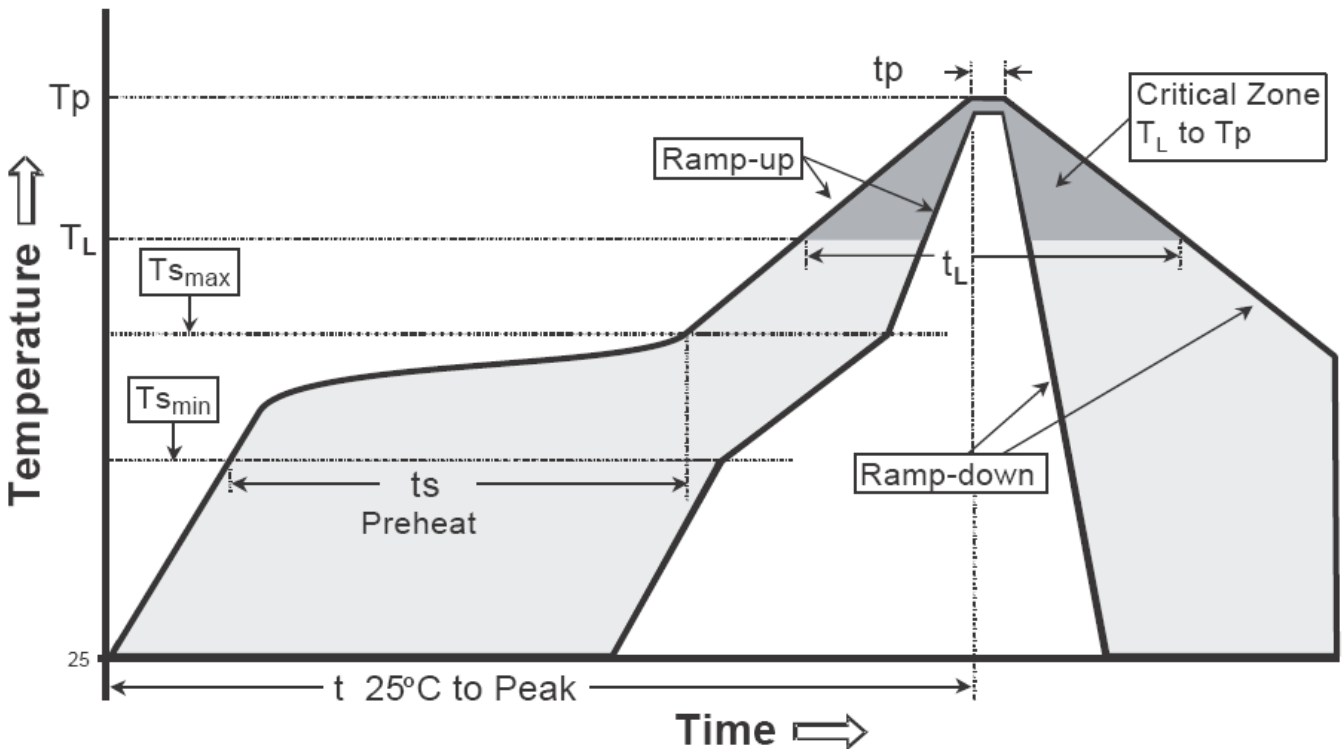
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

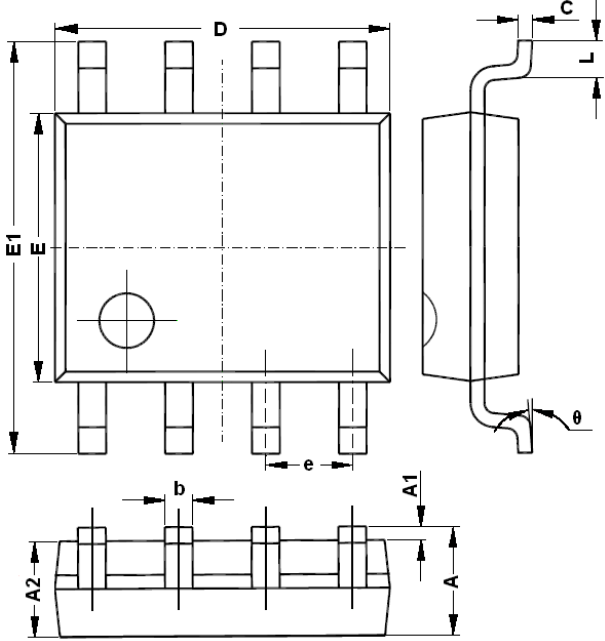
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _p)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(t _p)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

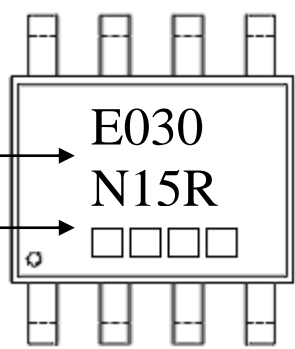
Note : All temperatures refer to topside of the package, measured on the package body surface.

SOP-8 Dimension



The diagram shows three views of an 8-lead SOP-8 package: a top view with dimensions D, E, and E1; a side view with dimensions A, A1, A2, b, c, L, and lead angle θ; and a bottom view with dimensions A1 and A2.

Marking:



The marking diagram shows a rectangular package with eight leads. The top surface is marked with 'E030' and 'N15R'. Below these markings are four small squares representing the date code. An arrow labeled 'Device Name' points to 'E030' and an arrow labeled 'Date Code' points to the four squares.

Date Code(counting from left to right) :

1st code: year code, the last digit of Christian year
 2nd code : month code, Jan→A, Feb→B, Mar→C, Apr→D
 May→E, Jun→F, Jul→G, Aug→H, Sep→J,
 Oct→K, Nov→L, Dec→M
 3rd and 4th codes : production serial number, 01~99

8-Lead SOP-8 Plastic Package
 CYStek Package Code: Q8

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069	E	3.800	4.000	0.150	0.157
A1	0.100	0.250	0.004	0.010	E1	5.800	6.200	0.228	0.244
A2	1.350	1.550	0.053	0.061	e	1.270 (BSC)		0.050 (BSC)	
b	0.330	0.510	0.013	0.020	L	0.400	1.270	0.016	0.050
c	0.170	0.250	0.006	0.010	θ	0	8°	0	8°
D	4.700	5.100	0.185	0.200					

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.