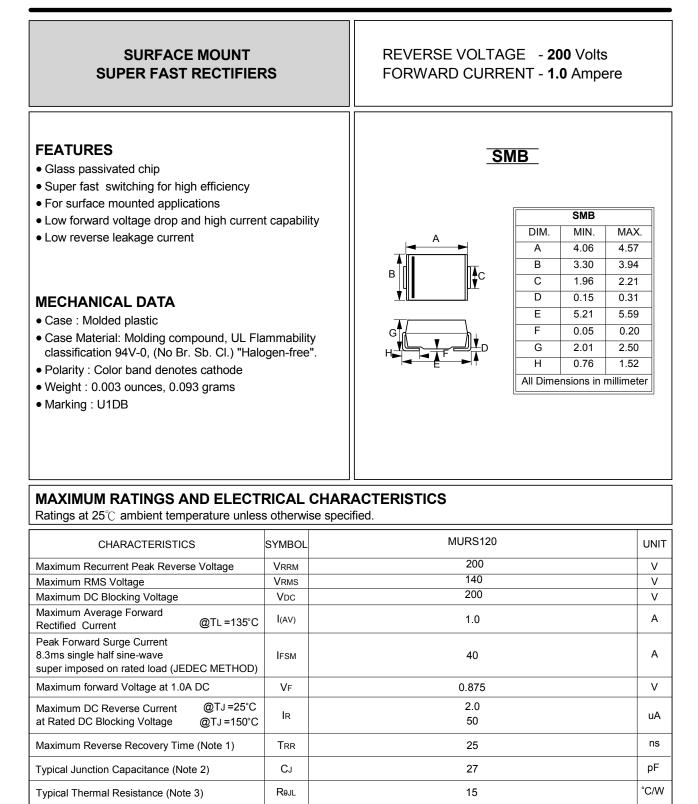
LITE ON SEMICONDUCTOR

MURS120



NOTES : 1.Reverse Recovery Test Conditions :IF=0.5A,IR=1.0A,IRR=0.25A.

REV.3, Aug-2014, KSGB07

-55 to +150

-55 to +175

°C

°C

2.Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

ΤJ

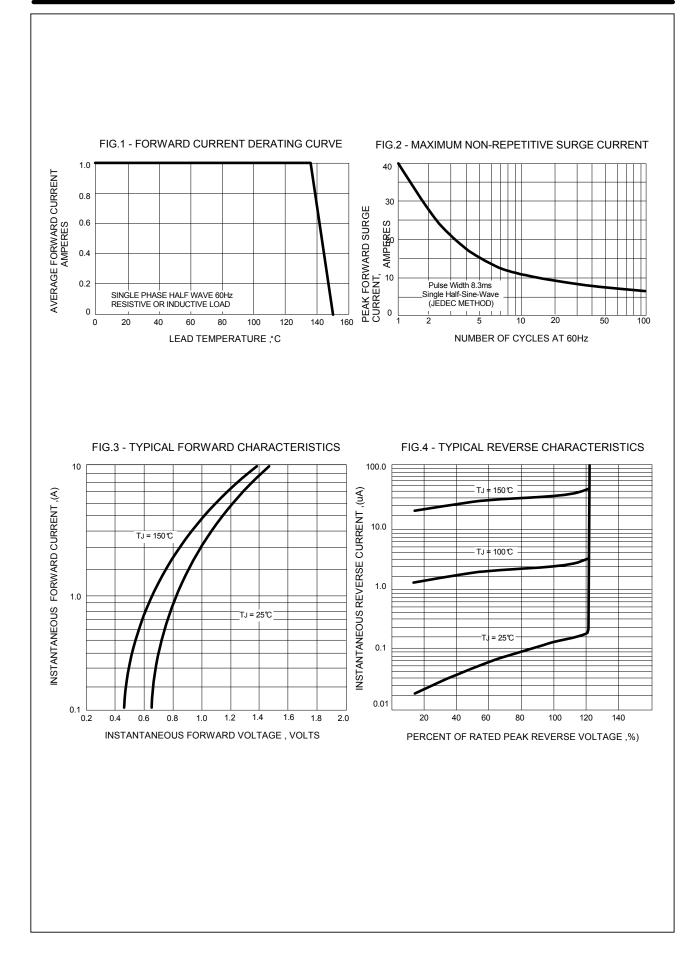
Tstg

3. Thermal Resistance junction to Lead.

Operating Temperature Range

Storage Temperature Range

RATING AND CHARACTERISTIC CURVES MURS120



LITE ON



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