

%This is a RoHS and REACH compliant product whose related documents are available on request.
%Graphic is only for dimensionally application.

### 1. Range of application:

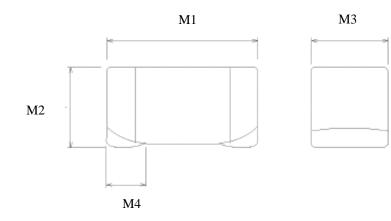
This specifications are applied to SMD Power Inductor, CSMB2512D.

### 2. Ordering code:

Example:  $\frac{CSCB}{(1)}$   $\frac{2512}{(2)}$   $\frac{D-XXX}{(3)}$   $\frac{X-\Box\Box}{(5)}$ 

- (1) Product Type
- (2) External dimensions
- (3) Solder Type
- (4) Inductance
- (5) Inductance tolerance
- (6) ROHS+HF

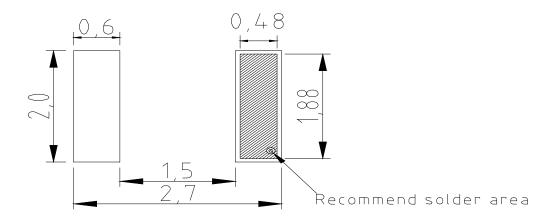
#### 3. Mechanical Dimension:



#### UNIT: mm

	DIM.	TOL.
М1	2.5	±0.2
M2	1.2	MAX
М3	2.0	±0.2
M4	0.5	±0.2

#### 4. Recommended Land-Pattern:



(Unit: mm)



#### 5. Electrical Characteristics:

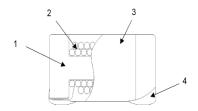
	Nominal .	Inductance Tolerance	D.C. Resistance (Ω)		Typ Current (mA)		Reted Current (mA)		Measuring
Part Number.	Inductance (uH)		<b>T</b>		Тур		Max		Frequency (MHz)
			Typ Max	ldc 1	ldc 2	ldc 1	ldc 2		
CSCB2512D-R24N-LRH	0.24	±30%	0.020	0.026	5200	3900	4750	3500	1
CSCB2512D-R47N-LRH	0.47	±30%	0.035	0.042	4250	2900	3900	2600	1
CSCB2512D-R68N-LRH	0.68	±30%	0.048	0.058	3550	2400	3150	2150	1
CSCB2512D-1R0M-LRH	1.0	±20%	0.060	0.072	2600	2050	2350	1850	1
CSCB2512D-1R5M-LRH	1.5	±20%	0.092	0.106	2250	1700	2050	1500	1
CSCB2512D-2R2M-LRH	2.2	±20%	0.138	0.159	1950	1400	1800	1250	1
CSCB2512D-3R3M-LRH	3.3	±20%	0.225	0.260	1600	1050	1400	970	1
CSCB2512D-4R7M-LRH	4.7	±20%	0.330	0.380	1300	900	1150	800	1

Maximum rated voltage: DC25V

#### \*Caution for Temperature Rise.

Temperature rise of this inductor depends on the installed board condition. It shall be confirmed in the actual end product that. temperature rise of inductor is within operating temperature.

#### 6. Structural Drawing:



1. Ferrite core

2. Coil material

3. Over-coating resin

4. Electrode

Ni-Zn ferrite

Polyurethane-copper wire

Epoxy resin, containing metal magnetic powder

Base material : Ag

Foundation plating : Ni

Surface plating : Sn



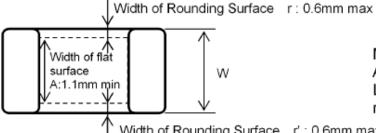
<sup>\*)</sup>The saturation current value (Idc1) is the maximum DC current value having inductance decrease down to 30% (at 20degC)

<sup>\*)</sup>The temperature rise current value (Idc2) is the maximum DC current value having temperature increase by 40degC. (at 20degC)

<sup>\*)</sup>The rated current value is following either Idc1 or Idc2. which is the lower one.



### 7. Appearance Criteria for Chip-off Mode:

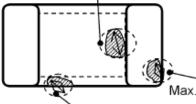


More than 1.1mm width of flat surface A shall be acceptable.

Less than 0.6mm width of rounding surface r & r' shall be acceptable.

Width of Rounding Surface r': 0.6mm max

Max. Trajectory Diameter of Pinhole: 0.5mm max

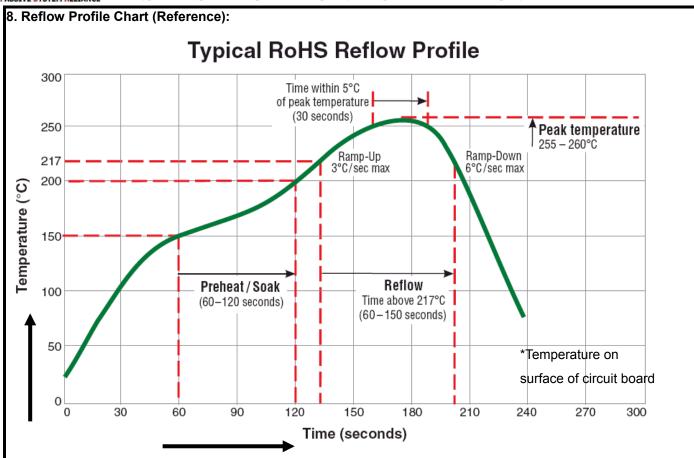


Less than φ0.5mm max. trajectory diameter of pinhole and / or chip-off of whole surface shall be acceptable.

Max. Trajectory Diameter of Chip-off: 0.5mm max

Max. Trajectory Diameter of Chip-off: 0.5mm max





The products may be exposed to reflow soldering process of above profile up to two times.





9. Mec	hanical Performand	e /Environmental Test P	erformance Specifications:
	Test Item	Standard	Test method
	Resistance to Flexure substrate	No damage.	The test samples shall be soldered to the testing board and by reflow soldering conditions as show in Page5 Reflow Profile Chart.  Apply pressure in the direction of the arrow until bent width reaches 2 mm.  Pressure  Rod  Rod  Rod  Rod  Rod  Rod  Rod  Unit: mm
TERISTICS			Substrate size:100*40*1.0 Substrate material:glass epoxy-resin Solder cream thickness :0.1 (Land size refer to recommended Land Pattern Dimensions of "Precaution:)
MECHANICAL CHARACTERISTICS	Adhesion of Terminal Electrode	No abnormality	The test samples shall be soldered to the testing board and by reflow soldering conditions as shown Page5 Reflow Profile Chart.   The test samples shall be soldered to the testing board and by reflow soldering conditions as shown Page5 Reflow Profile Chart.  Applied force:10 N to X and Y directions Duration:5 s.  Solder cream thickness:0.1mm (Land size refer to recommended Land Pattem Defined in "Precaution")
	Body strength	No damage	Applied force :10 N Duration :10 s





Test Item	Standard		Test method				
Resistance to vibration	Inductance change. Within±10%	The test sample table.	The test samples shall be soldered to testing jip as shown in untable.				
	No abnormality	Frequency ra	inge 10	Hz~55Hz			
	observed in	Overall Ampl		5mm(Shall no 'S²)	t exceed a	accelerati	on 196
	appearance.	Sweeping Me	thod 10	to 55 to 10 H	lz for 1 mi	n.	
		Time		nours each in rection.	X,Y, and I	Z	
Resistance to Soldering	Inductance change. Within±10% No abnormality observed in appearance.	3 time of reflow oven at 230 degC min for 40 sec max. With peak temperature at 260+0/-5 degC for 5 sec max. Substrate thickness. 1.0mm Substrate material:glass epoxy-resin					
Solder ability	At least 90% of Terminal electrode is covered by new	The test samples shall be submerged molten solder as show table.  Flux. methanol solution with 25% of rosin or equivalent.  { Pb free solder: Sn-3Ag-0.5Cu}				shown in	
	Solder	Solder Temp	erature	245±de	eg C		
		Time		5±0.5	S.		
		Immersing S	Speed	25 mr	n/s		
Temperature Characteristics	Inductance change. Within±15% No abnormality. Observed in appearance.	Measurement shall be taken in a temperature range of -40 degC +105 degC and the value at +20 degC was used as the standard					
Thermal shock	Inductance change. Within±10% No abnormality observed in appearance.	The test samples shall be soldered to testing jip and by reflow soldering conditions as shown in Page5 Reflow Profile Chart.  The test samples shall be left for the specified time at each of temperature in steps from 1 to 4. as shown in under table in sec The temperature cycles shall be repeated 100 cycled in the Me Conditions for 1 cycle.  Step Temperature Time(min)				Chart. ach of le in seq	
		1 -	40±3 deg	g C	30±3		
			Room Ter	•	Within	3	
		3 85±2 deg (		С	30±3		
		4 F	Room Ter	mp	Within	3	
Low Temperature life Test	Inductance change. Within±10% No abnormality observed in appearance.	The test samples shall be soldering conditions as sl And after that proceed the Temperature  Time		shown in Pag	ge5 Reflow own condi	v Profile (	Chart.



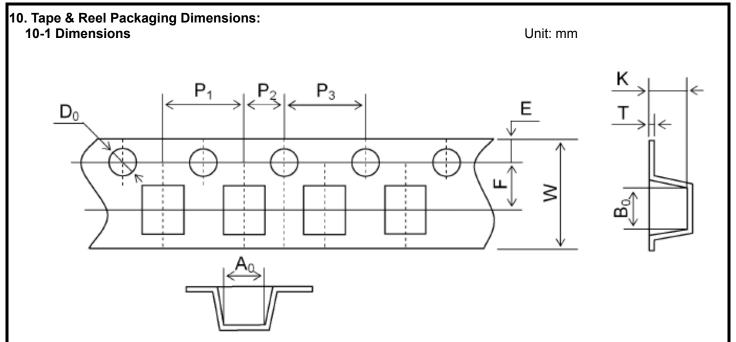


	Т	T	1						
	Test Item	Standard			Test	method			
	Hing Temperature life Test	Inductance change. Within±10% No abnormality observed in	The test samples shall be soldered to testing jip and by reflow soldering conditions as shown in Page5 Reflow Profile Chart.  And after that proceed the test as shown condition under table.						
		appearance.	Tempe	Temperature 85±2 deg C					
		appourance.	Tir	ne	1000-	+24h			
	Damp heat life	Inductance change.	The test sa	mples shal	II be soldere	d to the tes	ting jip and b	v reflow	
တ	test	Within±10%		•			ow Profile Ch	•	
ENVIRONMENT TESTS		No abnormality observed in appearance.	The test samples shall be put in thermostatic oven set at temperature with humidity as shown in under table.						
HE N				Temperature		60±2 deg C			
N N				Humidity		90~95%	RH		
VIR				Т	īme	1000+24	h	1	
E	Loading under Damp heat life test	Inductance change. Within±10% No abnormality observed in appearance.	conditions a	as shown ii mples shal ity , as sho ly applied. Temp	n Page5 Ref Il be put in th	flow Profile nermostatic	Chart. oven set at to ith the rated  Charter  Ch		
					ime	1000+24	h	1	
								1	

Standard	Unless otherwise specified,at least 2 hrs of recovery under the room
measuring	Temperature and normal humidity after the test. Followed by the measurement.
condition	Within-48 hrs

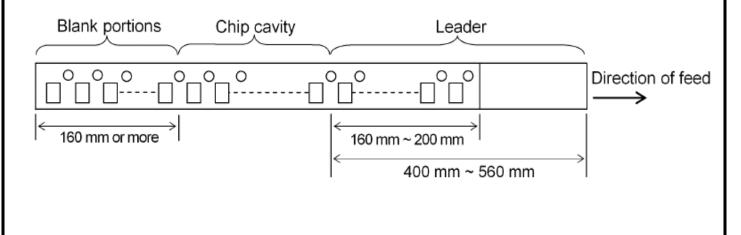






<b>A</b> 0	Bo	W	F	Е	P <sub>1</sub>	P <sub>2</sub>	P <sub>0</sub>	Do	Т	K
2.30	2.80	8.0 ±0.3	3.5 ±0.05	1.75 ±0.1	4.0 ±0.1	2.0 ±0.1	4.0 ±0.1	Ф1.5 +0.1 -0	0.3 ±0.05	1.45 MAX

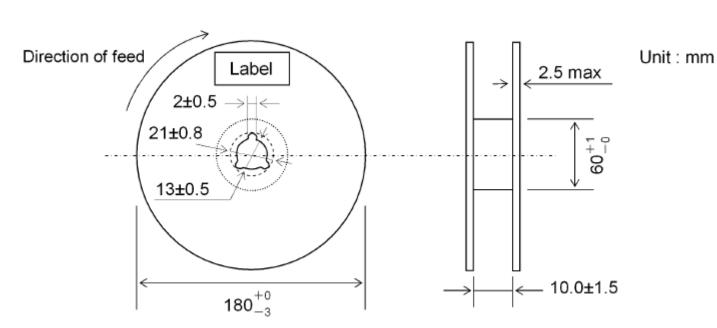
### 10-2 Direction of rolling





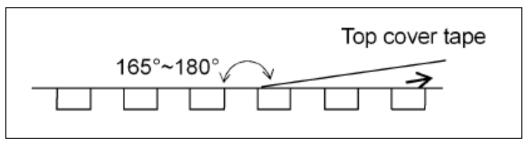


#### 10-3 Reel



Label position: the opposite side of pilot holes

### 10-4 Top tape strength



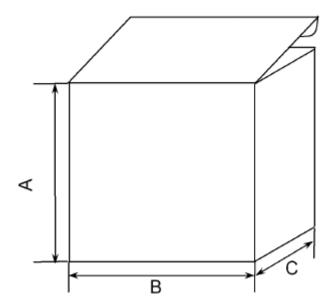
Peel-off strength: 0.2N~0.7N

Peel-off angle:165°~180°

Peel-off speed: 300mm/min



10-5 Dimensions of packing box (for Tape & Reel package)



Code	Α	В	С	Standard Quantity
Size	190	105	75	15, 000 pcs. max
Size		190 185	100	140

[Unit : mm]

