

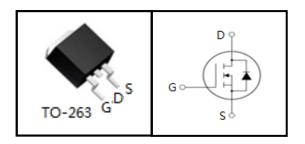
80V N-Channel Trench MOSFET

FEATURES

- High Density Cell Design for Ultra Low Rdson
- Fully Characterized Avalanche Voltage and Current
- Good Stability with High E_{AS}
- Excellent Package for Good Heat Dissipation

APPLICATIONS

- Power Switching Application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply





Device Marking and Package Information			
Device	Package	Marking	
TMB80N08A	TO-263	80N08A	

Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted					
Parameter	Symb	ol Value	Unit		
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	, 80	V		
Continuous Drain Current	I _D	80	А		
Pulsed Drain Current (n	ote1) I _{DM}	320	А		
Gate-Source Voltage	V _{GSS}	±20	V		
Single Pulse Avalanche Energy (n	ote2) E _{AS}	620	mJ		
Avalanche Current (n	ote1) I _{AS}	45	А		
Power Dissipation (T _C = 25°C)	P _D	170	W		
Operating Junction and Storage Temperature Range	T_J,T_s	-55~+175	°C		

Thermal Resistance				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	R _{thJC}	0.88	14004	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62	K/W	



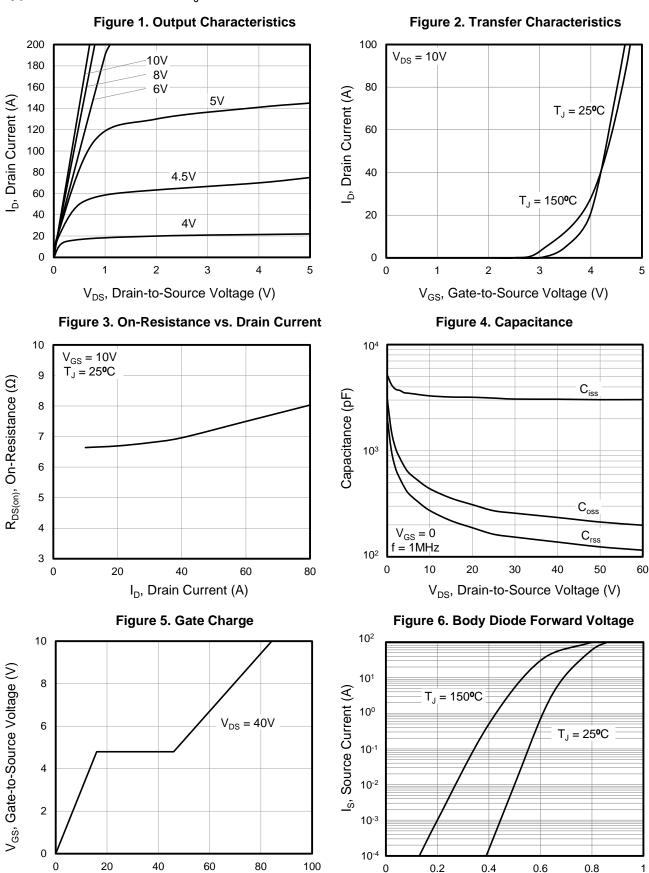
Specifications $T_J = 25^{\circ}$ C, unless otherwise noted						
D	Symbol		Value			
Parameter		Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_{D} = 250\mu A$	80			V
Zoro Coto Voltago Drain Current	I _{DSS}	$V_{DS} = 80V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	- μA
Zero Gate Voltage Drain Current		$V_{DS} = 80V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100	
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 20V$			±100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 10V, I_{D} = 30A$		6.8	8.5	mΩ
Forward Transconductance (Note3)	g _{fs}	$V_{DS} = 5V, I_{D} = 20A$	25			S
Dynamic						
Input Capacitance	C _{iss}	V = 0V		3134		pF
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 25V,$		272		
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		163		
Total Gate Charge	Q_g			84		nC
Gate-Source Charge	Q_{gs}	$V_{DD} = 40V, I_{D} = 20A, V_{GS} = 10V$		16		
Gate-Drain Charge	Q_{gd}	. 63		30		
Turn-on Delay Time	t _{d(on)}			17		
Turn-on Rise Time	t _r	$V_{DD} = 40V, I_{D} = 2A,$		18		
Turn-off Delay Time	t _{d(off)}	$R_G = 2.5\Omega$		25		ns
Turn-off Fall Time	t _f			9.5		
Drain-Source Body Diode Characteri	stics					
Continuous Body Diode Current	I _S	T _C = 25°C			80	^
Pulsed Diode Forward Current	I _{SM}				320	A
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 20A$, $V_{GS} = 0V$			1.2	V
Reverse Recovery Time	t _{rr}	I _F = 20A,		27		ns
Reverse Recovery Charge	Q _{rr}	$di_F/dt = 500A/\mu s$		33		nC

Notes

- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2. I_{AS} = 45A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 1%

V_{SD}, Source-to-Drain Voltage (V)

Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted



Q_q, Total Gate Charge (nC)



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Figure 7. On-Resistance vs. Temperature

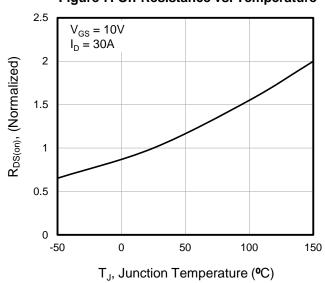


Figure 8. Threshold Voltage vs. Temperature

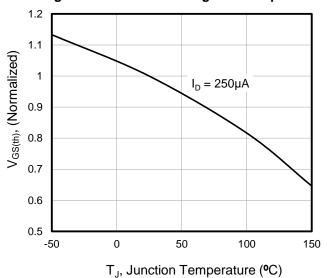


Figure 9. Transient Thermal Impedance

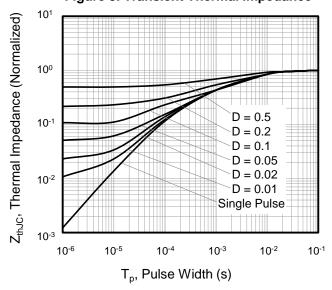




Figure A: Gate Charge Test Circuit and Waveform

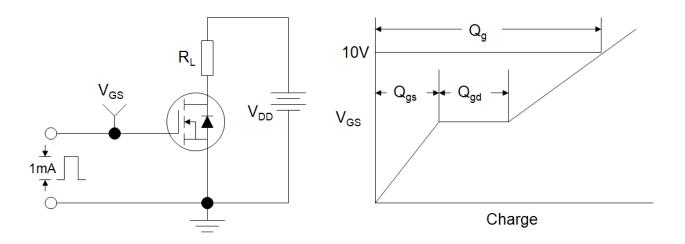


Figure B: Resistive Switching Test Circuit and Waveform

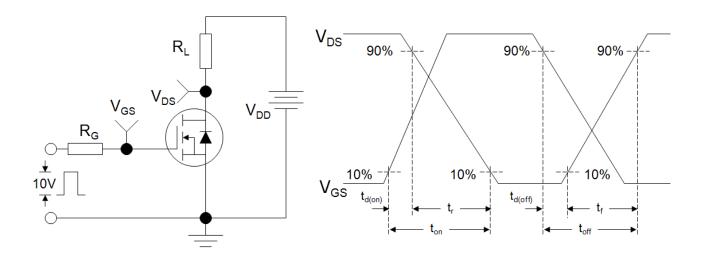
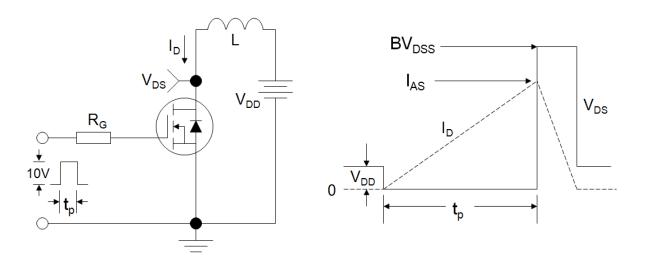
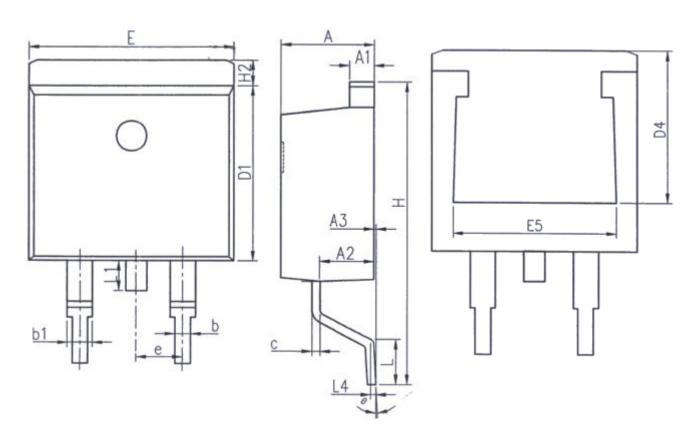


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-263



Unit: mm				
Symbol	Min.	Max.		
Α	4. 37	4. 77		
A 1	1. 22	1. 42		
A2	2. 49	2. 89		
A3	0. 00	0. 25		
b	0. 70	0.96		
b1	1. 17	1. 47		
С	0. 30	0. 53		
D1	8. 50	8. 90		
D4	6. 60	_		

Unit: mm			
Symbol	Min.	Max.	
E	9. 86	10.36	
E 5	7. 06	-	
е	2. 54BSC		
Н	14. 70	15. 50	
H2	1. 07	1. 47	
L	2.00	2. 60	
L1	1. 40	1. 70	
L4	0. 25BSC		
θ	0° 9°		



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