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December 2010

# NC7SVL08 TinyLogic<sup>®</sup> Low-I<sub>CCT</sub> Two-Input AND Gate

#### **Features**

- 0.9V to 3.6V V<sub>CC</sub> Supply Operation
- 3.6V Over-Voltage Tolerant I/Os at V<sub>CC</sub> from 0.9V to 3.6V
- Power-Off High-Impedance Inputs and Outputs
- Proprietary Quiet Series<sup>™</sup> Noise / EMI Reduction Circuitry
- Ultra-Small MicroPak™ Packages
- Ultra-Low Dynamic Power

## **Description**

The NC7SVL08 is a single two-input AND gate with a low-l<sub>CCT</sub> input design from Fairchild's Ultra-Low Power (ULP-A) series of TinyLogic  $^{\$}$ . The NC7SVL08 features very low quiescent current, even when the input voltage is lower than the V<sub>CC</sub> supply. This feature services mobile handset applications very well, allowing for direct interface with baseband processor general-purpose I/Os. Since mobile devices rely on a battery supply, the NC7SVL08 facilitates lower power consumption in mixed-voltage rail environments.

This product is designed on an advanced CMOS technology for a wide low-voltage operating range (0.9V to 3.6V  $V_{\rm CC}$ ), high drive needs (up to 24mA), and speed (maximum propagation delay of 3.5ns,  $V_{\rm CC}$ =3.3V). It achieves this performance while maintaining low CMOS power dissipation.

## **Ordering Information**

Part Number	Top Mark	Package	Packing Method
NC7SVL08P5X	L08	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3000 Units on Tape & Reel
NC7SVL08L6X	CE	6-Lead MicroPak™, 1.00mm Wide	5000 Units on Tape & Reel
NC7SVL08FHX	CE	6-Lead, MicroPak2™, 1x1mm Body, .35mm Pitch	5000 Units on Tape & Reel

## **Connection Diagrams**



Figure 1. Logic Symbol

## **Pin Configurations**

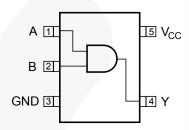


Figure 2. SC70 (Top View)

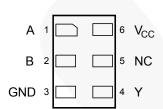


Figure 3. MicroPak™ (Top Through View)

### **Pin Definitions**

Pin # SC70	Pin # MicroPak™	Name	Description
1	1	A	Input
2	2	В	Input
3	3	GND	Ground
4	4	4 Y Output	
	5	NC	No Connect
5	6	Vcc	Supply Voltage

## **Function Table**

#### Y = AB

Inp	Output			
Α	В	Y		
L	L	L		
L	Н	L		
Н	L	L		
Н	Н	Н		

L = Low Logic Level

H = High Logic Level

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Para	meter	Min.	Max.	Unit
V <sub>CC</sub>	Supply Voltage		-0.5	4.6	V
V <sub>IN</sub>	DC Input Voltage		-0.5	4.6	V
V	DC Output Voltage	HIGH or LOW State <sup>(1)</sup>	-0.5	V <sub>CC</sub> to +0.5	V
V <sub>OUT</sub>	DC Output Voltage	V <sub>CC</sub> =0V	-0.5	4.6	V
I <sub>IK</sub>	DC Input Diode Current	V <sub>IN</sub> < 0V		-50	mA
	DC Output Diede Current	V <sub>OUT</sub> < 0V		-50	A
lok	DC Output Diode Current	V <sub>OUT</sub> > V <sub>CC</sub>		+50	mA
I <sub>OH</sub> / I <sub>OL</sub>	DC Output Source/Sink Current			±50	mA
I <sub>CC</sub> or I <sub>GND</sub>	DC V <sub>CC</sub> or Ground Current per S	Supply Pin		±50	mA
T <sub>STG</sub>	Storage Temperature Range		-65	+150	°C
TJ	Junction Temperature Under Bi	as		+150	Ĵ
TL	Junction Lead Temperature (So	ldering, 10 Seconds)		+260	°C
		SC70-5		150	
$P_D$	Power Dissipation at +85°C	MicroPak™-6		130	mW
		MicroPak2™-6		120	
ESD	Human Body Model	JEDEC: JESD22-A114		4000	V
ESD	Charged Device Model	JEDEC: JESD22-C101		2000	٧

#### Note:

1. The I<sub>O</sub> maximum rating must be observed.

## **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Conditions	Min.	Max.	Unit	
V <sub>CC</sub>	Supply Voltage		0.9	3.6	V	
V <sub>IN</sub>	Input Voltage <sup>(2)</sup>		0	3.6	V	
V	Output Voltage	HIGH or LOW State	0	Vcc	V	
V <sub>OUT</sub>	Output Voltage	V <sub>CC</sub> =0V	0	3.6	]	
		V <sub>CC</sub> =3.0V to 3.6V		±24.0		
		V <sub>CC</sub> =2.3V to 2.7V		±18.0	mA	
1 /1	Output Current in L / L	V <sub>CC</sub> =1.65V to 1.95V		±6.0		
I <sub>OH</sub> / I <sub>OL</sub>	Output Current in I <sub>OH</sub> / I <sub>OL</sub>	V <sub>CC</sub> =1.40V to 1.60V		±4.0		
		V <sub>CC</sub> =1.10V to 1.30V		±2.0		
		V <sub>CC</sub> =0.9V		±0.1	μA	
T <sub>A</sub>	Free Air Operating Temperature		-40	+85	°C	
Δt / ΔV	Minimum Input Edge Rate	V <sub>IN</sub> =0.8V to 2.0V, V <sub>CC</sub> =3.0V		10	ns/V	
		SC70-5		425	°C/W	
$\theta_{JA}$	Thermal Resistance	MicroPak™-6		500		
		MicroPak2™-6		560	1	

#### Note

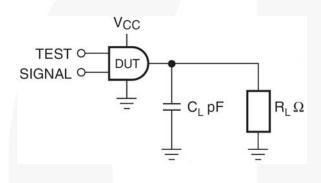
2. Unused inputs must be held HIGH or LOW. They may not float.

## **DC Electrical Characteristics**

Symbol	Parameter	V	Conditions	T <sub>A</sub> =25°C		T <sub>A</sub> =-40 to 85°C		Units
Symbol	Parameter	V <sub>CC</sub>	Conditions	Min.	Max.	Min.	Max.	Units
		0.90		0.65 x V <sub>CC</sub>		0.65 x V <sub>CC</sub>		
		1.10 ≤ V <sub>CC</sub> ≤ 1.30		0.65 x V <sub>CC</sub>		0.65 x V <sub>CC</sub>		
	HIGH Level Input	1.40 ≤ V <sub>CC</sub> ≤ 1.60		0.65 x V <sub>CC</sub>		0.65 x V <sub>CC</sub>		V
$V_{IH}$	Voltage	1.65 ≤ V <sub>CC</sub> ≤ 1.95		0.9		0.9		
		2.30 ≤ V <sub>CC</sub> ≤ 2.70		1.5		1.5		
		2.70 ≤ V <sub>CC</sub> ≤ 3.60		1.5		1.5		
		0.90			0.25 x V <sub>CC</sub>		0.25 x V <sub>CC</sub>	
		1.10 ≤ V <sub>CC</sub> ≤ 1.30			0.25 x V <sub>CC</sub>		0.25 x V <sub>CC</sub>	
.,	LOW Level Input	1.40 ≤ V <sub>CC</sub> ≤ 1.60			0.25 x V <sub>CC</sub>		0.25 x V <sub>CC</sub>	.,
$V_{IL}$	Voltage	1.65 ≤ V <sub>CC</sub> ≤ 1.95			0.25 x V <sub>CC</sub>		0.25 x V <sub>CC</sub>	V
		2.30 ≤ V <sub>CC</sub> ≤ 2.70			0.7		0.7	
		2.70 ≤ V <sub>CC</sub> ≤ 3.60			0.8		0.8	
	/	0.90		V <sub>CC</sub> - 0.1		V <sub>CC</sub> - 0.1		
		1.10 ≤ V <sub>CC</sub> ≤ 1.30		V <sub>CC</sub> - 0.1		V <sub>CC</sub> - 0.1		
		1.40 ≤ V <sub>CC</sub> ≤ 1.60	-	V <sub>CC</sub> - 0.2		V <sub>CC</sub> - 0.2		
		1.65 ≤ V <sub>CC</sub> ≤ 1.95	I <sub>OH</sub> =-100μA	V <sub>CC</sub> - 0.2		V <sub>CC</sub> - 0.2		
	9	2.30 ≤ V <sub>CC</sub> ≤ 2.70	-	V <sub>CC</sub> - 0.2		V <sub>CC</sub> - 0.2		
		$2.70 \le V_{CC} \le 3.60$	1	V <sub>CC</sub> - 0.2		V <sub>CC</sub> - 0.2		
		$1.10 \le V_{CC} \le 1.30$	I <sub>OH</sub> =-2mA	0.75 x V <sub>CC</sub>		0.75 x V <sub>CC</sub>		V
VoH	HIGH Level Output	$1.40 \le V_{CC} \le 1.60$	I <sub>OH</sub> =-4mA	0.75 x V <sub>CC</sub>		0.75 x V <sub>CC</sub>		
VOH	Voltage	$1.65 \le V_{CC} \le 1.95$	-I <sub>OH</sub> =-6mA	1.25		1.25		
		$2.30 \le V_{CC} \le 2.70$		2.0		2.0		
		$2.30 \le V_{CC} \le 2.70$		1.8		1.8		
		$2.70 \le V_{CC} \le 2.70$ $I_{OH}$ =-12mA	I <sub>OH</sub> =-12mA	2.2		2.2		
		$2.30 \le V_{CC} \le 2.70$		1.7		1.7		
		$2.70 \le V_{CC} \le 3.60$	I <sub>OH</sub> =-18mA	2.4		2.4		
		$2.70 \le V_{CC} \le 3.60$ $2.70 \le V_{CC} \le 3.60$	I <sub>OH</sub> =-24mA	2.4		2.4		
		0.90	10H24IIIA	2.2	0.10	2.2	0.10	
		1.10 ≤ V <sub>CC</sub> ≤ 1.30	-		0.10		0.10	
		$1.40 \le V_{CC} \le 1.50$ $1.40 \le V_{CC} \le 1.60$			0.10		0.10	
		$1.65 \le V_{CC} \le 1.00$	I <sub>OL</sub> =100μA		0.20		0.20	
		$2.30 \le V_{CC} \le 1.93$	1		0.20		0.20	
		$2.70 \le V_{CC} \le 2.70$ $2.70 \le V_{CC} \le 3.60$	1		0.20		0.20	
	LOW Love Contract	$1.10 \le V_{CC} \le 3.00$	I=2mΛ		0.25 x V <sub>CC</sub>		0.25 x V <sub>CC</sub>	
$V_{OL}$	LOW Level Output Voltage	$1.40 \le V_{CC} \le 1.60$	I <sub>OL</sub> =2mA I <sub>OL</sub> =4mA		0.25 x V <sub>CC</sub>		0.25 x V <sub>CC</sub>	V
	Voltago	$1.65 \le V_{CC} \le 1.00$	I <sub>OL</sub> =6mA		0.30		0.30	
	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	$2.30 \le V_{CC} \le 1.95$	IOL-OITIA		0.40		0.30	
		$2.70 \le V_{CC} \le 2.70$ $2.70 \le V_{CC} \le 3.60$	I <sub>OL</sub> =12mA		0.40		0.40	
		$2.70 \le V_{CC} \le 3.60$ $2.30 \le V_{CC} \le 2.70$			0.40		0.40	
		$2.70 \le V_{CC} \le 2.70$ $2.70 \le V_{CC} \le 3.60$	I <sub>OL</sub> =18mA		0.60		0.60	
			L =24m A					
I <sub>IN</sub>	Input Leakage	2.70 ≤ V <sub>CC</sub> ≤ 3.60 0.90 to 3.60	$I_{OL}$ =24mA $0 \le V_{IN} \le 3.6V$		0.55 ±0.1		0.55 ±0.5	μA
	Current Power Off Leakage		$0 \le V_{\text{IN}} = 3.0V$ $0 \le (V_{\text{IN}}, V_{\text{O}}) \le$					•
I <sub>OFF</sub>	Current	0	3.6V		0.5		0.5	μA
Icc	Quiescent Supply	0.90 to 3.60	V <sub>IN</sub> =V <sub>CC</sub> or GND		0.9		0.9	μA
	Current		$V_{CC} \le V_{IN} \le 3.6V$		_		±0.9	•
$I_{CCT}$	Increase in I <sub>CC</sub> per	1.95	V <sub>IN</sub> =0.9V		6		8	μΑ
551	Input	3.6	V <sub>IN</sub> =1.5V		6		8	•

## **AC Electrical Characteristics**

Cymhal	Parameter	V	Conditions		T <sub>A</sub> =25°	С	T <sub>A</sub> =-40	to 85°C	Units	Ciaura	
Symbol	Parameter V <sub>CC</sub> Conditions Min. Typ.	Тур.	Max.	Min.	Max.	Ullits	Figure				
		0.90	$C_L$ =15pF, $R_L$ =1M $\Omega$		45.0						
		$1.10 \le V_{CC} \le 1.30$	C <sub>L</sub> =15pF,	3.5	8.2	17.5	3.0	30.5			
t <sub>PHL</sub> , t <sub>PLH</sub>	Propagation Delay	$1.40 \le V_{CC} \le 1.60$	R <sub>L</sub> =2kΩ $C_L=30pF,$ R <sub>I</sub> =500Ω	1.5	4.0	7.0	1.5	7.5	ns	Figure 4, Figure 5	
		$1.65 \le V_{CC} \le 1.95$			1.1	3.0	5.5	1.0	6.0		rigure 5
		$2.30 \le V_{CC} \le 2.70$		0.6	2.2	4.0	0.6	4.5			
		$2.70 \le V_{CC} \le 3.60$	112 00022	0.5	1.6	3.5	0.5	4.0			
C <sub>IN</sub>	Input Capacitance	0			3				pF		
$C_{PD}$	Power Dissipation Capacitance	0.90 to 3.60	V <sub>IN</sub> =0V or V <sub>CC</sub> , f=10MHz		5				pF		



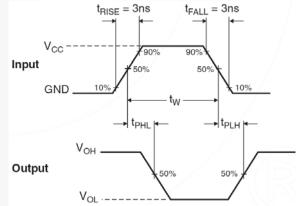


Figure 4. AC Test Circuit

Figure 5. AC Waveforms

Symbol	V <sub>cc</sub>					
	3.3V ± 0.3V	2.5V ± 0.2V	1.8V ± 0.15V	1.5V ± 0.1V	1.2V ± 0.1V	0.9V
V <sub>mi</sub>	1.5V	V <sub>CC</sub> / 2				
$V_{mo}$	1.5V	V <sub>CC</sub> / 2				

## **Physical Dimensions**

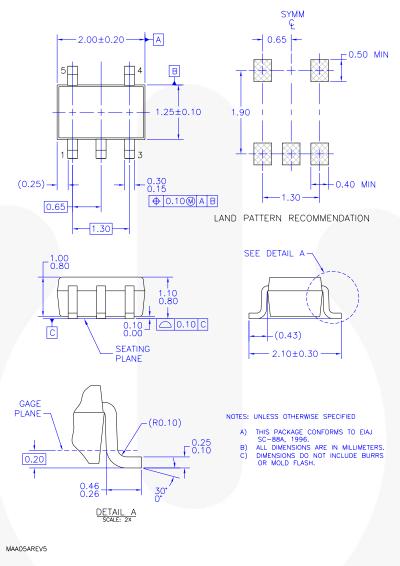


Figure 6. 5-Lead, SC70, EIAJ SC-88a, 1.25mm Wide

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

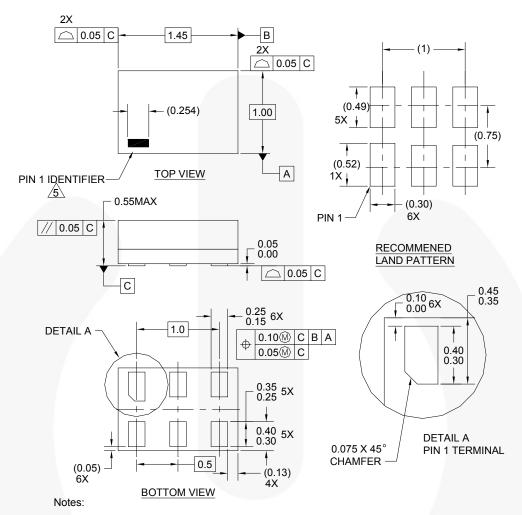
Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/.

#### **Tape and Reel Specifications**

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: http://www.fairchildsemi.com/products/analog/pdf/sc70-5\_tr.pdf.

Package Designator	Tape Section	Tape Section Cavity Number		Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
P5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

## **Physical Dimensions**



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994
- FILENAME AND REVISION: MAC06AREV4
- 5. PIN ONE IDENTIFIER IS 2X LENGTH OF ANY

OTHER LINE IN THE MARK CODE LAYOUT.

Figure 7. 6-Lead, MicroPak™, 1.0mm Wide

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

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#### **Tape and Reel Specifications**

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications:

Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
L6X	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

## **Physical Dimensions**

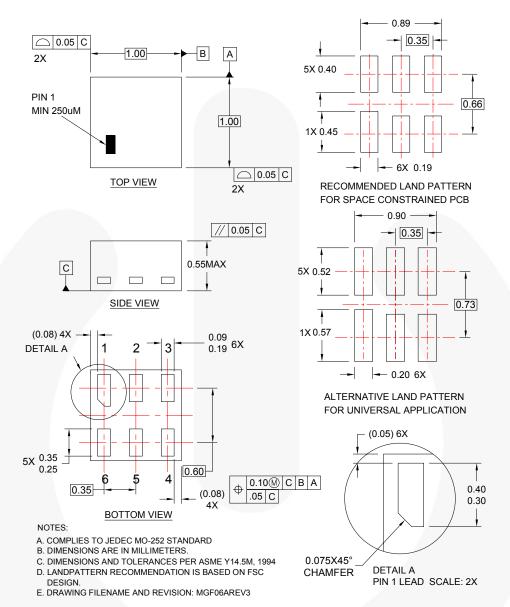


Figure 8. 6-Lead, MicroPak™2, 1x1mm Body, .35mm Pitch

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#### **Tape and Reel Specifications**

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: <a href="http://www.fairchildsemi.com/packaging/MicroPAK2">http://www.fairchildsemi.com/packaging/MicroPAK2</a> 6L tr.pdf.

Package Designator	Tape Section	Tape Section Cavity Number		Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
FHX	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed





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Fairchild® Fairchild Semiconductor® FACT Quiet Series™ FACT®

FastvCore™ FETBench™ FlashWriter®\* **FPSTM** 

F-PFS™ FRFET

Global Power Resourcesin Green FPS™

Green FPS™e-Series™ GmaxIII

GTOM IntelliMAX™ ISOPLANAR™

MegaBuck™ MICROCOUPLER™ MicroFET™ MicroPak™ MicroPak2™ MillerDrive™

MotionMa×™ Motion-SPM™ mWSaver™ OptoHiT™ OPTOLOGIC® OPTOPLANAR®

PDP SPM"

Power-SPM™ PowerTrench® PowerXS™

Programmable Active Droop™

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Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

#### PRODUCT STATUS DEFINITIONS

#### Definition of Terms

Datasheet Identification	Product Status	Definition		
Advance Information Formative / In Desi		Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.		

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