

OX Type

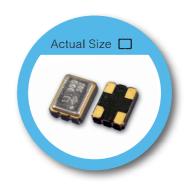
3.2 x 2.5 mm SMD Crystal Oscillator

FEATURE

- Typical 3.2 x 2.5 x 0.95 mm ceramic SMD package.
- Tight symmetry (45 to 55%) available.
- Operation voltage: 1.8V, 2.5V, 3.3V
- Tri-state enable/disable

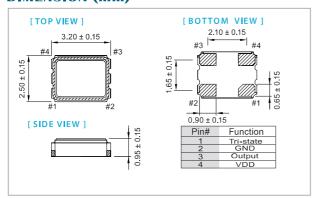
TYPICAL APPLICATION

- WLAN/WiMAX
- Mobile Phone
- DSC, Set-top Box, HDTV



RoHS Compliant

DIMENSION (mm)



SOLDER PAD LAYOUT (mm)



To ensure optimal oscillator performance, place a by-pass capacitor of 0.1µF as close to the part as possible between Vdd and GND pads.

ELECTRICAL SPECIFICATION

Parameter	3.3 V		2.5 V		1.8 V		unit
Farameter	Min.	Max.	Min.	Max.	Min.	Max.	unit
Supply Voltage Variation (VDD)	VDD-10%	VDD+10%	VDD-10%	VDD+10%	VDD-10%	VDD+10%	V
Frequency Range	1.25	125	1.25	125	1.25	125	MHz
Standard Frequency	24, 26, 32, 38.4, 40				IVIIIZ		
Supply Current 1.25 MHz ≤ Fo < 100 MHz	_	15	_	10	_	7	mA
100 MHz ≦ Fo ≦ 125 MHz	_	25	_	20	_	12	MA
Duty Cycle	45	55	45	55	45	55	%
Output Level (CMOS) Output High (Logic "1")	2.97	-	2.25	_	1.62	_	V
Output Low (Logic "0")	_	0.33	_	0.25	_	0.18	V
Transition Time:Rise/Fall Time ⁺							
1.25 MHz ≦ Fo < 20 MHz	_	4		4	_	5	
20 MHz ≦ Fo < 80 MHz	_	3	_	3	_	4	nSec
80 MHz ≦ Fo ≦ 125 MHz	-	3	-	3	_	4	
Start Time	_	2	_	2	_	2	mSec
Tri-State(Input to Pin 1) Enable (High voltage or floating)	2.31	_	1.75	_	1.26	_	V
Disable (Low voltage or GND)	_	0.99	_	0.75	_	0.54	V
Period Jitter(Pk-Pk)	_	40	_	40	_	40	pSec
RMS Phase Jitter (Integrated 12 kHz ~ 20 MHz)	_	1	_	1	_	1	pSec
Standby Current	-	10	-	10	_	10	μA
Aging (@ 25°C 1st year)	_	±3	_	±3	_	±3	ppm
Storage Temp. Range	-55	125	- 55	125	-55	125	°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C)	±20	±25	±50
-10 ~ +60	0	0	0
- 20 ~ +70	Δ	0	0
-40 ~ +85	×	0	0
-40 ~ +125	×	×	0

^{*} \bigcirc : Available \triangle :Conditional X: Not available

Note: not all combination of options are available. Other specifications may be available upon request.

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⁺ Transition times are measured between 10% and 90% of VDD, with an output load of 15pF.

^{*} Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration