



# LOW OHMIC CHIP RESISTORS

RL series 5%, 2%, 1% sizes 0402/0603/0805/1206/ 1210/1218/2010/2512

RoHS compliant & Halogen Free







Chip Resistor Surface Mount RL SERIES 0402 to 2512

<u>SCOPE</u>

This specification describes RL0402 to RL2512 low ohmic chip resistors with lead-free terminations made by thick film process.

#### APPLICATIONS

- Converters
- Printer equipment
- Server board
- Telecom
- Consumer

#### FEATURES

- Halogen Free Epoxy
- RoHS compliant
- Hazardous wastes
- High component and equipment reliability
- Saving of PCB space
- Non-forbidden materials used in products/production
- Low resistances applied to current sensing
- MSL Class: MSL I

#### ORDERING INFORMATION - GLOBAL PART NUMBER & 12NC

Both part numbers are identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

#### YAGEO BRAND ordering code

#### **GLOBAL PART NUMBER (PREFERRED)**

### $\mathsf{RL} \ \underline{\mathsf{XXXX}} \ \underline{\mathsf{X}} \ \underline{\mathsf{X}} \ \underline{\mathsf{X}} \ \underline{\mathsf{X}} \ \underline{\mathsf{X}} \ \underline{\mathsf{X}} \ \underline{\mathsf{XXX}} \ \underline{\mathsf{L}} \ \underline{\mathsf{L}}$

|--|

#### (I) SIZE

0402 / 0603 / 0805 / 1206 / 1210 / 1218 / 2010 / 2512

#### (2) TOLERANCE

 $F = \pm 1\%$  $G = \pm 2\%$ 

 $J = \pm 5\%$ 

### (3) PACKAGING TYPE R = Paper taping reel

K = Embossed taping reel

#### (4) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Based on spec

#### (5) TAPING REEL

07 = 7 inch dia. Reel

13 = 13 inch dia. Reel

#### (6) RESISTANCE VALUE

There are  $2\sim4$  digits indicated the resistor value. Letter R/K/M is decimal point, no need to mention the last zero after R/K/M, e.g. I K2, not I K20.

10 = 10 inch dia. Reel

Detailed coding rules of resistance are shown in the table of "Resistance rule of global part number".

#### (7) DEFAULT CODE

Letter L is system default code for order only <sup>(Note)</sup>

Letter H is Halogen / Lead free (special code on request)

### Resistance rule of global part number Resistance code rule Example

ORXXX	$ORT = 0.1 \Omega$
010000	$0R12 = 0.12 \Omega$
(1 to 976 mΩ)	$0R105 = 0.105 \Omega$

#### **ORDERING EXAMPLE**

The ordering code of a RL0603 chip resistor, value  $0.56\Omega$  with  $\pm 1\%$  tolerance, supplied in 7-inch tape reel is: RL0603FR-070R56L.

#### NOTE

- All our R-Chip products meet RoHS compliant and Halogen Free. "LFP" of the internal 2D reel label mentions "Lead Free Process"
- 2. On customized label, "LFP" or specific symbol can be printed



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#### PHYCOMP BRAND ordering codes

Both GLOBAL PART NUMBER (preferred) and I2NC (traditional) codes are acceptable to order Phycomp brand products.

#### **GLOBAL PART NUMBER (PREFERRED)**

For detailed information of GLOBAL PART NUMBER and ordering example, please refer to page 2.

#### 12NC CODE

2350	) / <b>2390</b> / (I)	2322		<b>X XXXXX L</b> 2) (3) (4)				Last o Resistanc
SIZE	TYPE	START IN <sup>(I)</sup>	TOL. (%)	RESISTANCE RANGE	EMBOSSED <sup>(2)</sup> TAPE ON REEL 4,000	PAPER/PE <sup>(2</sup> . TAPE ON F 5,000		0.01 to 0 0.1 to 0.1
0402	LRC31	2350	+5%	0.05 to   Ω	-	-	5 3 20xxx	l to 9.76
	LRC32	2350		0.05 to   Ω	-	-	513 22xxx	10 to 97
0603	LRC21	2350		0.01 to 1 Ω	-	512 10xxx	-	100 to 9
	LRC22	2350	±1%	0.01 to 1 Ω	-	51212xxx	-	l to 9.76 10 to 97
0805	LRCII	2350	±5%	0.01 to 1 Ω	-	51110xxx	-	10 to 97
	LRC12	2350	±1%	0.01 to 1 Ω	-	51112xxx	-	l to 9.76
1206	LRC01	2350	±5%	0.0  to   Ω	-	51010xxx	-	10 to 97
	LRC02	2350	±1%	0.0  to   Ω	-	51012xxx	-	10 to 77
1210	LPRC101	2390	±5%	0.01 to 0.0976 Ω	-	735 90xxx	-	Example
	LPRC101	2390	±5%	0.  to   Ω	-	735 60xxx	-	
	LPRC102	2390	±1%	0.0  to   Ω	-	735 3xxxx	-	
1218	LPRC201	2322	±5%	0.0  to   Ω	735 64xxx	-	-	
	LPRC201	2322	±1%	0.0  to   Ω	735 7xxxx	-	-	
2010	LPRCIII	2322	±5%	0.01 to 0.0976 Ω	760 90xxx	-	-	Orderi
	LPRCIII	2322	±5%	0.1 to 1 Ω	760 60xxx	-	-	The orde
	LPRCIII	2322	±1%	0.01 to 0.0976 Ω	761 90xxx	-	-	resistor,
	LPRCIII	2322	±1%	0.1 to 1 Ω	761 6xxxx	-	-	tolerance
2512	LPRC221	2322	±5%	0.01 to 0.0976 Ω	762 90xxx	-	-	units per RL0603F
	LPRC221	2322	±5%	0.1 to 1 Ω	762 60xxx	-	-	KLUGU3F
	LPRC221	2322	±1%	0.01 to 0.0976 Ω	763 90xxx	-	-	NOTE
	LPRC221	2322	±1%	0.1 to I Ω	763 6xxxx	-		I . All our I complia

(1) The resistors have a 12-digit ordering code starting with 2350/2390/2322.

- (2) The subsequent 4 or 5 digits indicate the resistor tolerance and packaging. (In 12NC code, only 07" tape reel code is supplied. Supply of 10"/13" tape reel is requested in Global part number ordering code.)
- (3) The remaining 4 or 3 digits represent the resistance value with the last digit indicating the multiplier as shown in the table of "Last digit of I2NC".
- (4) Letter L is system default code for order only <sup>(Note)</sup>.
   Letter H is Halogen / Lead free (special code on request).

Last digit of I2NC						
Resistance	)	Last digit				
0.01 to 0.0	<b>976</b> Ω		0			
0.1 to 0.97	<b>6</b> Ω		7			
I to 9.76Ω	2		8			
10 to 97.69	Ω		9			
100 to 976	Ω		I			
l to 9.76 k		2				
10 to 97.6	kΩ		3			
100 to 976	kΩ		4			
l to 9.76 N	1Ω		5			
10 to 97.6	MΩ		6			
Example:	<b>0.02</b> Ω	=	0200 or 200			
	<b>0.3</b> Ω	=	3007 or 307			
	IΩ	=	1008 or 108			
	<b>33 k</b> Ω	=	3303 or 333			
	<b>Ι0 Μ</b> Ω	=	1006 or 106			

#### **ORDERING EXAMPLE**

The ordering code of a RL0603 chip

resistor, value 0.56  $\Omega$  with ±1% tolerance, supplied in tape of 5,000 units per reel is: 235051212567L or RL0603FR-070R56L.

- All our R-Chip products meet RoHS compliant and Halogen Free. "LFP" of the internal 2D reel label mentions "Lead Free Process"
- 2. On customized label, "LFP" or specific symbol can be printed

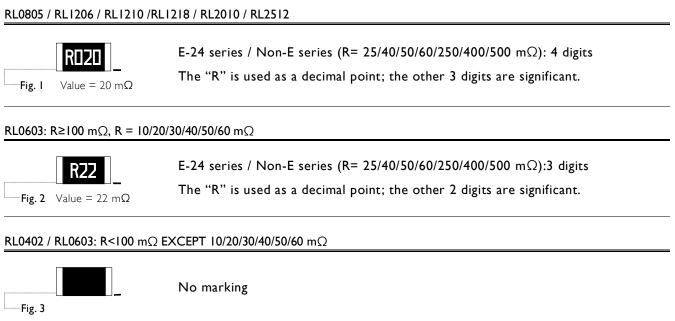


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#### MARKING



For further marking information, please see special data sheet "Chip resistors marking".

#### **CONSTRUCTION**

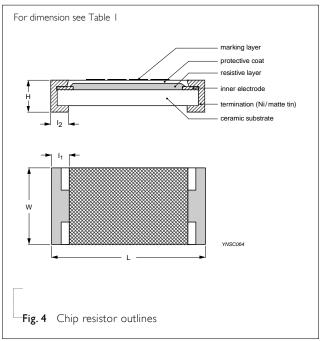
The resistors are constructed out of a high-grade ceramic body. Internal metal electrodes are added at each end and connected by a resistive paste. The composition of the paste is adjusted to give the approximate required resistance and laser cutting of this resistive layer that achieves tolerance trims the value. The resistive layer is covered with a protective coat and printed with the resistance value. Finally, the two external terminations (matte tin) are added. See fig. 4.

#### **DIMENSIONS**

Table I	For outlines	see fig. 4
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ТҮРЕ	L (mm)	W (mm)	H (mm)	l⊨(mm)	l2 (mm)
RL0402	1.00 ±0.10	0.50 ±0.05	0.35 ±0.05	0.20 ±0.10	0.25 ±0.10
RL0603	1.60 ±0.10	0.80 ±0.10	0.45 ±0.10	0.25 ±0.15	0.25 ±0.15
RL0805	2.00 ±0.10	1.25 ±0.10	0.50 ±0.10	0.35 ±0.20	0.35 ±0.20
RL1206	3.10 ±0.10	1.60 ±0.10	0.55 ±0.10	0.45 ±0.20	0.40 ±0.20
RL1210	3.10 ±0.10	2.60 ±0.15	0.55 ±0.10	0.50 ±0.20	0.50 ±0.20
RL1218	3.05 ±0.15	4.60 ±0.20	0.55 ±0.10	0.45 ±0.25	0.50 ±0.25
RL2010	5.00 ±0.10	2.50 ±0.15	0.55 ±0.10	0.60 ±0.20	0.50 ±0.20
RL2512	6.35 ±0.10	3.20 ±0.15	0.55 ±0.10	0.60 ±0.20	0.50 ±0.20

#### OUTLINES





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#### ELECTRICAL CHARACTERISTICS

Table 2

TYPE /	RESISTANCE RANGE	TEMPERATURE COEFFICIENT OF RESISTANCE									
RL0402		50m <u>Ω</u> ≤R	<100mΩ			I00mΩ ≤	R<500mΩ	500mΩ :	≤R <iω< th=""></iω<>		
KL040Z	50mΩ≤R <iω< th=""><th>±1000 p</th><th>opm/°C</th><th></th><th></th><th>±800 j</th><th>opm/°C</th><th>±300 p</th><th>pm/°C</th></iω<>	±1000 p	opm/°C			±800 j	opm/°C	±300 p	pm/°C		
RL0603		l0mΩ ≤R≤36r	mΩ	36m	ıΩ <r≤< th=""><th>≦<b>9l</b>mΩ</th><th>91m<u>Ω</u> &lt;</th><th>R≤500m<u>Ω</u></th><th>500mΩ <r<iω< th=""></r<iω<></th></r≤<>	≦ <b>9l</b> mΩ	91m <u>Ω</u> <	R≤500m <u>Ω</u>	500mΩ <r<iω< th=""></r<iω<>		
KL0003	l0mΩ≤R <lω< th=""><td colspan="2">±1,500 ppm/°C</td><td colspan="2">±1,200 ppm/°C</td><td>±800</td><td>ppm/°C</td><td>±300 ppm/°C</td></lω<>	±1,500 ppm/°C		±1,200 ppm/°C		±800	ppm/°C	±300 ppm/°C			
		10m <u>Ω</u> ≤R≤18m <u>Ω</u>	I8m <u>Ω</u> <r< th=""><th>.≤47mΩ</th><th>47m</th><th><u>Ω</u> <r≤91mω< th=""><th>91mΩ <r≤360mω< th=""><th>360m<u>Ω</u> <r≤500m<u>Ω</r≤500m<u></th><th>500m<u>Ω</u> <r<i <u="">Ω</r<i></th></r≤360mω<></th></r≤91mω<></th></r<>	.≤47mΩ	47m	<u>Ω</u> <r≤91mω< th=""><th>91mΩ <r≤360mω< th=""><th>360m<u>Ω</u> <r≤500m<u>Ω</r≤500m<u></th><th>500m<u>Ω</u> <r<i <u="">Ω</r<i></th></r≤360mω<></th></r≤91mω<>	91mΩ <r≤360mω< th=""><th>360m<u>Ω</u> <r≤500m<u>Ω</r≤500m<u></th><th>500m<u>Ω</u> <r<i <u="">Ω</r<i></th></r≤360mω<>	360m <u>Ω</u> <r≤500m<u>Ω</r≤500m<u>	500m <u>Ω</u> <r<i <u="">Ω</r<i>		
RL0805	_	±1,500 ppm/°C	±1,200 p	opm/°C	±1,(	000 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C		
RL1206		±1,500 ppm/°C	±1,200 ppm/°C		±1,(	000 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C		
RL1210	-10mΩ≤R<1Ω	±1,500 ppm/°C	±1,000 p	±1,000 ppm/°C		00 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C		
RL2010	_	±1,500 ppm/°C	±1,200 p	±1,200 ppm/°C		±1,200 ppm/°C		000 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C
RL2512		±1,500 ppm/°C	±1,200 ppm/°C		±800 ppm/°C		±600 ppm/°C ±300 ppm/°C		±200 ppm/°C		
RL1218 10mO <r<< th=""><th></th><th>l0m<u>Ω</u> ≤R≤30m<u>Ω</u></th><th>30m(</th><th>) <r≤56m< th=""><th>nΩ</th><th>56m<u>Ω</u> <r≤i< th=""><th>80m<u>Ω</u></th><th> 80mΩ <r<iω< th=""><th>2</th></r<iω<></th></r≤i<></th></r≤56m<></th></r<<>		l0m <u>Ω</u> ≤R≤30m <u>Ω</u>	30m(	) <r≤56m< th=""><th>nΩ</th><th>56m<u>Ω</u> <r≤i< th=""><th>80m<u>Ω</u></th><th> 80mΩ <r<iω< th=""><th>2</th></r<iω<></th></r≤i<></th></r≤56m<>	nΩ	56m <u>Ω</u> <r≤i< th=""><th>80m<u>Ω</u></th><th> 80mΩ <r<iω< th=""><th>2</th></r<iω<></th></r≤i<>	80m <u>Ω</u>	80mΩ <r<iω< th=""><th>2</th></r<iω<>	2		
	l0mΩ≤R <lω< th=""><td>±2,000 ppm/°C</td><td>±I,C</td><td>)00 ppm/°</td><td>С</td><td>±700 ppn</td><td>n/°C</td><td>±250 ppm/°C</td><td></td></lω<>	±2,000 ppm/°C	±I,C	)00 ppm/°	С	±700 ppn	n/°C	±250 ppm/°C			

#### FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please see the special data sheet "Chip resistors mounting".

#### PACKING STYLE AND PACKAGING QUANTITY

Table 3         Packing style a	Table 3         Packing style and packaging quantity								
PACKING STYLE	REEL DIMENSION	RL0402	RL0603	RL0805	RL1206	RL1210	RL1218	RL2010	RL2512
Paper taping reel (R)	7" (178 mm)	10,000	5,000	5,000	5,000	5,000			
	10" (254 mm)	20,000	10,000	10,000	10,000	10,000			
	13" (330 mm)	50,000	20,000	20,000	20,000	20,000			
Embossed taping reel (K)	7" (178 mm)						4,000	4,000	4,000

#### NOTE

I. For paper/embossed tape and reel specification/dimensions, please see the special data sheet "Chip resistors packing".



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#### FUNCTIONAL DESCRIPTION

#### **OPERATINGTEMPERATURE RANGE**

Range: -55 °C to +155 °C

#### **POWER RATING**

Each type rated power at 70 °C: RL0402=1/16 W; RL0603=1/10 W; RL0805=1/8 W; RL1206=1/4 W; RL1210=1/2 W; RL1218=1 W; RL2010=3/4 W; RL2512=1 W.

#### **R**ATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

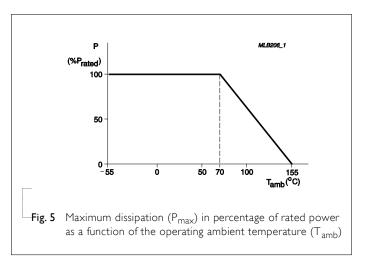
$$V = \sqrt{(P \times R)}$$

Where

V = Continuous rated DC or AC (rms) working voltage (V)

P = Rated power (W)

 $R = Resistance value (\Omega)$ 



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## TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
ife/IEC 60115-1 4.25.1I ,000 hours at 70±2°C applied RCWVindurance1.5 hours on, 0.5 hour off, still air required		±(2% +0.5m <b>Ω</b> )	
High Temperature Exposure/ Endurance at upper	IEC 60068-2-2	I,000 hours at maximum operating temperature depending on specification, unpowered	±(1% +0.5m <b>Ω</b> )
category temperature		No direct impingement of forced air to the parts	
		Tolerances: 155±5 °C	
Moisture Resistance	MIL-STD-202 Method 106	Each temperature / humidity cycle is defined at 8 hours, 3 cycles / 24 hours for 10d with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered	±(2% +0.5m <b>Ω</b> )
		Parts mounted on test-boards, without condensation on parts	
Thermal Shock	MIL-STD-202 Method 107	-55/+125 °C	±(1% +0.5m <b>Ω</b> )
		Number of cycles required is 300.	
		Maximum transfer time is 20 seconds. Dwell time is 15 minutes.	
Short time overload	IEC60115-14.13	2.5 times RCWV or maximum overload voltage whichever is less for 5 sec at room temperature	±(2% +0.5m <b>Ω</b> ) No visible damage
Board Flex/ Bending	IEC 60068-2-21	Device mounted on PCB test board as described, only 1 board bending required	±(1% +0.5m <b>Ω</b> ) No visible damage
		3 mm bending	
		Bending time: 60±5 seconds	
		Ohmic value checked during bending	



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TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Solderability		Flastical Test act way in d	Wall tipped (295% covered)
- Wetting	IPC/JEDECJ-STD-002B test B	Electrical Test not required	Well tinned (≥95% covered) No visible damage
		Magnification 50X	
		SMD conditions:	
		I <sup>st</sup> step: method B, aging 4 hours at 155 °C dry heat	
		$2^{nd}$ step: leadfree solder bath at 245±3 °C	
		Dipping time: 3±0.5 seconds	
- Leaching	IPC/JEDECJ-STD-002B test D	Leadfree solder, 260 °C, 30 seconds immersion time	No visible damage
- Resistance to	IEC 60068-2-58	Condition B, no pre-heat of samples.	±(1% +0.5m <b>Ω</b> )
Soldering Heat		Leadfree solder, 260 °C, 10 seconds immersion time	No visible damage
		Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	

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#### **REVISION HISTORY**

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 6	Nov. 18, 2013	-	- Operating temperature extension
Version 5	Mar 22, 2010	-	- The statement of "Halogen Free" on the cover added
			- Test methods updated
Version 4	Dec 11, 2008	-	- Halogen Free Epoxy
			- Update global part number definition
Version 3	Aug 07, 2008	-	- Change to dual brand datasheet that describe RL0402 to RL2512 with
			RoHS compliant
			- Define global part number
Version 2	Jul 15, 2005	-	- Ordering example revised
Version I	Apr 15, 2005	-	- Size 1218 extended
			- Test method and procedure updated
			- PE tape added (paper tape will be replaced by PE tape)
Version 0	Nov. 10, 2003	-	- First issue of this specification

"Yageo reserves all the rights for revising the content of this datasheet without further notification, as long as the products itself are unchanged. Any product change will be announced by PCN."

