

INA146

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High-Voltage, Programmable Gain DIFFERENCE AMPLIFIER

FEATURES

- ◆ HIGH COMMON-MODE VOLTAGE: +40V at V_S = +5V ±100V at V_S = ±15V
- DIFFERENTIAL GAIN = 0.1V/V TO 100V/V: Set with External Resistors
- LOW QUIESCENT CURRENT: 570µA
- WIDE SUPPLY RANGE: Single Supply: 4.5V to 36V Dual Supplies: ±2.25V to ±18V
- LOW GAIN ERROR: 0.025%
- HIGH CMR: 80dBSO-8 PACKAGE

APPLICATIONS

- CURRENT SHUNT MEASUREMENTS
- SENSOR AMPLIFIER
- SYNCHRONOUS DEMODULATOR
- CURRENT AND DIFFERENTIAL LINE RECEIVER
- VOLTAGE-CONTROLLED CURRENT SOURCE
- BATTERY POWERED SYSTEMS
- LOW COST AUTOMOTIVE INSTRUMENTATION

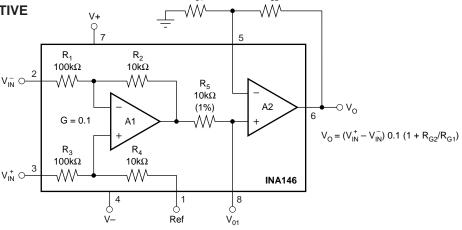
DESCRIPTION

The INA146 is a precision difference amplifier that can be used to accurately attenuate high differential voltages and reject high common-mode voltages for compatibility with common signal processing voltage levels. High-voltage capability also affords inherent input protection. The input common-mode range extends beyond both supply rails, making the INA146 well-suited for both single and dual supply applications.

On-chip precision resistors are laser-trimmed to achieve accurate gain and high common-mode rejection. Excellent TCR tracking of these resistors assures continued high precision over temperature.

A 10:1 difference amplifier provides 0.1 V/V gain when the output amplifier is used as a unity-gain buffer. In this configuration, input voltages up to $\pm 100 \text{V}$ can be measured. Gains greater than 0.1 V/V can be set with an external resistor pair without affecting the common-mode input range.

The INA146 is available in the SO-8 surface-mount package specified for the extended industrial temperature range, -40°C to +85°C.



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Twx: 910-952-1111 • Internet: http://www.burr-brown.com/ • Cable: BBRCORP • Telex: 066-6491 • FAX: (520) 889-1510 • Immediate Product Info: (800) 548-6132

SPECIFICATIONS: $V_S = \pm 2.25V$ to $\pm 18V$ Dual Supplies

At $T_A = +25^{\circ}C$, G = 0.1, $R_L = 10k\Omega$ connected to ground and ref pin connected to ground unless otherwise noted. **Boldface** limits apply over the specified temperature range, $T_A = -40^{\circ}C$ to $+85^{\circ}C$.

				INA146UA		
PARAMETER		CONDITION	MIN	TYP	MAX	UNITS
OFFSET VOLTAGE, V _O Input Offset Voltage vs Temperature vs Power Supply vs Time Offset Voltage, V _{O1}	V _{OS} Δ V_{OS}/ Δ T PSRR	RTI(1, 2) $V_S = \pm 15$, $V_{CM} = V_O = 0V$ $V_S = \pm 1.35V$ to $\pm 18V$ RTI(1, 2)	So	±1 ee Typical Cur ±100 ±3 ±1	±5 ve ±600	mV μV/V μV/mo mV
INPUT VOLTAGE RANGE Common-Mode Voltage Range Common-Mode Rejection Over Temperature	V _{CM} CMRR	$(V_{IN}+) - (V_{IN}-) = 0V, V_{O} = 0V$ $V_{CM} = 11 (V-) \text{ to } 11 (V+) = 11, R_{S} = 0\Omega$	70 64	80 74	±100 ⁽³⁾	V dB dB
INPUT BIAS CURRENT ⁽²⁾ Bias Current Offset Current	I _B	$V_{CM} = V_S/2$		±50 ±5		nA nA
INPUT IMPEDANCE Differential (non-inverting input) Differential (inverting input) Common-Mode				110 91.7 55		kΩ kΩ kΩ
NOISE Voltage Noise, f = 0.1Hz to 10Hz Voltage Noise Density, f = 1kHz	e _n	RTI ^(1, 4)		10 550		μVp-p nV/√Hz
GAIN Gain Equation Initial(1) Gain Error vs Temperature vs Temperature Nonlinearity		$R_{L} = 100k\Omega, \ V_{O} = (V-)+0.15 \ to \ (V+)-1, \ G = 1$ $R_{L} = 100k\Omega, \ V_{O} = (V-)+0.25 \ to \ (V+)-1, \ G = 1$ $R_{L} = 10k\Omega, \ V_{O} = (V-)+0.3 \ to \ (V+)-1.25, \ G = 1$ $R_{L} = 10k\Omega, \ V_{O} = (V-)+0.5 \ to \ (V+)-1.25, \ G = 1$ $V_{O} = (V-)+0.3 \ to \ (V+)-1.25, \ G = 1$	G =	$ \begin{array}{c c} G = 0.1 \text{ to } 100 \\ 0.1 \bullet (1 + R_{G2}) \\ 0.1 \\ \pm 0.025 \\ \pm 1 \\ \pm 0.025 \\ \pm 1 \\ \pm 0.001 \end{array} $	/R _{G1}) ±0.1 ±10 ±0.1 ±10 ±0.1 ±10 ±0.01	V/V V/V % ppm/°C % ppm/°C % of FS
FREQUENCY RESPONSE Small Signal Bandwidth Slew Rate Settling Time, 0.1% 0.01% Overload Recovery		G = 0.1 G = 1 G = 1, 10V Step G = 1, 10V Step 50% Input Overload		550 50 0.45 40 80 40		kHz kHz V/μs μs μs μs
OUTPUT, V _O Voltage Output Over Temperature Over Temperature Short-Circuit Current Capacitive Load		$R_L = 100k\Omega$, $G = 1$ $R_L = 100k\Omega$, $G = 1$ $R_L = 10k\Omega$, $G = 1$ $R_L = 10k\Omega$, $G = 1$ Continuous to Common Stable Operation	(V-) + 0.15 (V-) + 0.25 (V-) + 0.3 (V-) + 0.5	±15 1000	(V+) - 1 (V+) - 1 (V+) - 1.25 (V+) - 1.25	V V V mA pF
POWER SUPPLY Specified Voltage Range, Dual Sup Operating Voltage Range Quiescent Current Over Temperature	pplies	V _{IN} = 0, I _O = 0	±2.25 ±1.35	±570	±18 ±18 ±700 ± 750	V V μΑ μΑ
TEMPERATURE RANGE Specified Range Operating Range Storage Range Thermal Resistance	$ heta_{\sf JA}$		-40 -55 -55	150	+85 +125 +125	°C °C °C

NOTES: (1) Overall difference amplifier configuration. Referred to input pins (V_{IN}^+) , gain = 0.1V/V. Specified with 10k Ω in feedback of A2. (2) Input offset voltage specification includes effects of amplifier's input bias and offset currents. (3) Common-mode voltage range is 11 (V–) to 11 [(V+) – 1] with a maximum of \pm 100V. (4) Includes effects of input current noise and thermal noise contribution of resistor network.

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SPECIFICATIONS: $V_S = +5V$ Single Supply

At T_A = +25°C, G = 1, R_L = 10k Ω connected to $V_S/2$ and Ref pin connected to $V_S/2$ unless otherwise noted. **Boldface** limits apply over the specified temperature range, T_A = -40°C to +85°C.

PARAMETER	CONDITION	MIN	TYP	MAX	UNITS	
OFFSET VOLTAGE, V₀ Input Offset Voltage V₀s vs Temperature ΔV₀s/ΔT vs Power Supply Rejection Ratio PSRR vs Time	$RTI^{(1, 2)}$ $V_{CM} = V_{O} = 0V$ $V_{S} = \pm 1.35V \text{ to } \pm 18V$	Se	±3 ±10 See Typical Curve ±100 ±600 ±3			
INPUT VOLTAGE RANGE Common-Mode Voltage Range Common-Mode Rejection Ratio Over Temperature V _{CM} CMRR	V_{IN} + - V_{IN} - = 0V, V_{O} = 0V V_{CM} = -25V to +19V, R_{S} = 0 Ω	-25 70 64	80 74	19	V dB dB	
			±50 ±5		nA nA	
INPUT IMPEDANCE Differential (non-inverting input) Differential (inverting input) Common-Mode			110 91.7 55		kΩ kΩ kΩ	
$\begin{tabular}{ll} \textbf{NOISE} \\ \textbf{Voltage Noise, f} = 0.1 \text{Hz to 10Hz} \\ \textbf{Voltage Noise Density, f} = 1 \text{kHz} \\ \end{tabular} \\ e_n \\ \end{tabular}$	RTI ^(1, 3, 4)		10 550		μVp <u>-p</u> nV/√Hz	
GAIN Gain Equation Initial(1) Gain Error vs Temperature Vs Temperature Nonlinearity	$\begin{aligned} R_L &= 100k\Omega, \ V_O = 0.15V \ to \ 4V, \ G = 1 \\ R_L &= 100k\Omega, \ V_O = 0.25V \ to \ 4V, \ G = 1 \\ R_L &= 10k\Omega, \ V_O = 0.3V \ to \ 3.75V, \ G = 1 \\ R_L &= 10k\Omega, \ V_O = 0.5V \ to \ 3.75V, \ G = 1 \\ V_O &= +0.3 \ to \ +3.75, \ G = 1 \end{aligned}$	G =	$ \begin{aligned} G &= 0.1 \text{ to } 100 \\ 0.1 & \bullet (1 + R_{G2} / \\ 0.1 \\ \pm 0.025 \\ \pm 1 \\ \pm 0.025 \\ \pm 1 \\ \pm 0.001 \end{aligned} $	R _{G1}) ±0.1 ±10 ±0.1 ±10 ±0.01	V/V V/V V/V % ppm/°C % ppm/°C % of FS	
FREQUENCY RESPONSE Small Signal Bandwidth Slew Rate Settling Time, 0.1% 0.01% Overload Recovery	G = 0.1 G = 1 G = 1, 10V Step G = 1, 10V Step 50% Input Overload		550 50 0.45 40 80 40		kHz kHz V/μs μs μs μs	
OUTPUT, V _O Voltage Output Over Temperature Over Temperature Short-Circuit Current Capacitive Load	$\begin{aligned} R_L &= 100k\Omega, \ G = 1 \\ R_L &= 100k\Omega, \ G = 1 \\ R_L &= 10k\Omega, \ G = 1 \\ R_L &= 10k\Omega, \ G = 1 \\ \text{Continuous to Common} \\ \text{Stable Operation} \end{aligned}$	0.15 0.25 0.3 0.5	±15 1000	4 4 3.75 3.75	V V V W mA	
POWER SUPPLY Voltage Range, Dual Supplies Voltage Range, Single Supply Quiescent Current Over Temperature	V _{IN} = 0, I _O = 0	±2.25 ±4.5	±570	±18 ±36 ±700 ± 750	V V μΑ μΑ	
		-40 -55 -55	150	+85 +125 +125	°C °C °C °C	

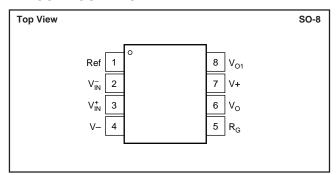
NOTES: (1) Overall difference amplifier configuration. Referred to input pins (V_{IN} + and V_{IN} -), gain = 0.1V/V. Specified with 10k Ω in feedback of A2. (2) Input offset voltage specification includes effects of amplifier's input bias and offset currents. (3) Includes effects of input current noise and thermal noise contribution of resistor network. (4) Common-mode voltage range is 11 (V-) to 11 [(V+) - 1] with a maximum of \pm 100V.

AMPLIFIER A1, A2 PERFORMANCE

At $T_A = +25^{\circ}C$, G = 0.1, $R_L = 10k\Omega$ connected to ground and Ref pin, unless otherwise noted. **Boldface** limits apply over the specified temperature range, $T_A = -40^{\circ}C$ to +85°C.

			INA146UA			
PARAMETER		CONDITION	MIN	TYP	MAX	UNITS
OFFSET VOLTAGE, V _O Input Offset Voltage vs Temperature	V _{OS} ∆V _{OS} /∆T	RTI ^(1, 2) $V_S = \pm 15V$, $V_{CM} = V_O = 0V$ $T_A = -40^{\circ}C$ to +85°C		±0.5 ±1		mV μV/°C
INPUT VOLTAGE RANGE Common-Mode Voltage Range Common-Mode Rejection Ratio	V _{CM} CMRR	V_{IN} + - V_{IN} - = 0V, V_{O} = 0V V_{CM} = (V-) to (V+) -1		(V–) to (V+) –1 90		V dB
OPEN-LOOP GAIN Open Loop Gain	A _{OL}			110		dB
INPUT BIAS CURRENT ⁽²⁾ Bias Current Offset Current	I _B I _{OS}			±50 ±5		nA nA
RESISTOR AT A1 OUTPUT, V ₀₁ Initial Error Temperature Drift Coefficient				10 ±1 ±100		kΩ % ppm/°C

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATINGS(1)

Supply Voltage, V+ to V	36V
Signal Input Terminals, Voltage	±100V
Current	±1mA
Output Short Circuit (to ground)	Continuous
Operating Temperature	55°C to +125°C
Storage Temperature	55°C to +150°C
Junction Temperature	+150°C
Lead Temperature (soldering, 10s)	+240°C

NOTE: (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability.

ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Burr-Brown recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

PACKAGE/ORDERING INFORMATION

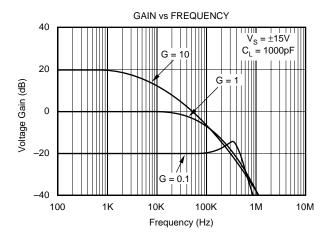
PRODUCT	PACKAGE	PACKAGE DRAWING NUMBER(1)	SPECIFIED TEMPERATURE RANGE	PACKAGE MARKING	ORDERING NUMBER(2)	TRANSPORT MEDIA	
INA146UA	SO-8	182	-40°C to +85°C	INA146UA	INA146UA	Rails	
"	"	"		"	INA146UA/2K5	Tape and Reel	

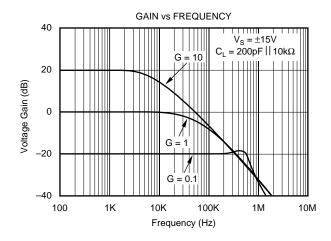
NOTES: (1) For detailed drawing and dimension table, please see end of data sheet, or Appendix C of Burr-Brown IC Data Book, or visit the Burr-Brown web site at www.burr-brown.com. (2) Models with a slash (/) are available only in Tape and Reel in the quantities indicated (e.g., /2K5 indicates 2500 devices per reel). Ordering 2500 pieces of "INA146UA/2K5" will get a single 2500-piece Tape and Reel. For detailed Tape and Reel mechanical information, refer to Appendix B of Burr-Brown IC Data Book.

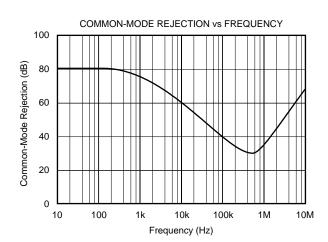


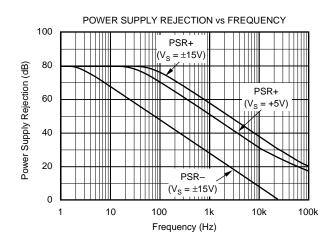
TYPICAL PERFORMANCE CURVES

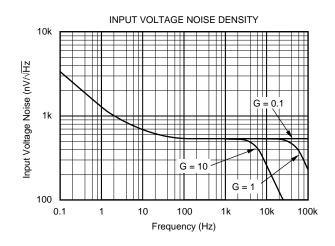
At $T_A = +25^{\circ}C$, $V_S = \pm 15V$, G = 0.1, $R_L = 10k\Omega$ connected to ground and Ref pin connected to ground, unless otherwise noted.

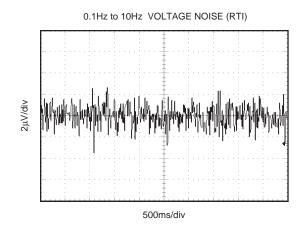






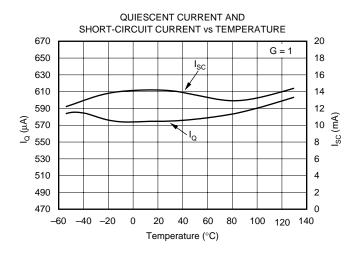


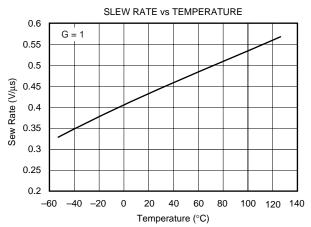


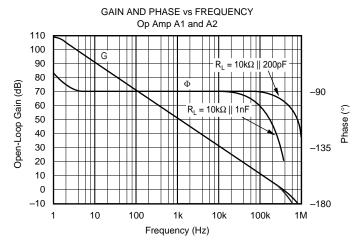


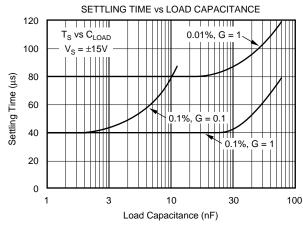
TYPICAL PERFORMANCE CURVES (Cont.)

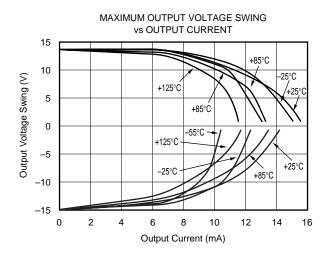
At T_A = +25°C, V_S = ±15V, G = 0.1, R_L = 10k Ω connected to ground and Ref pin connected to ground, unless otherwise noted.

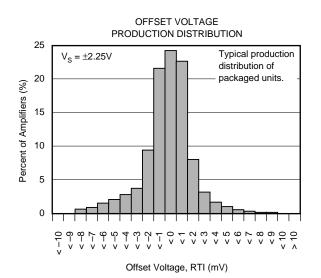






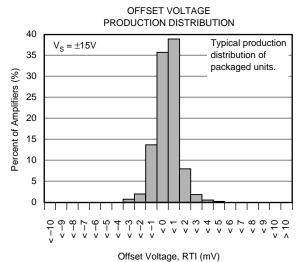




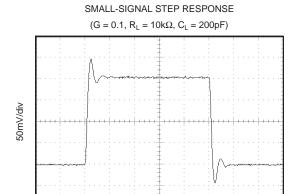


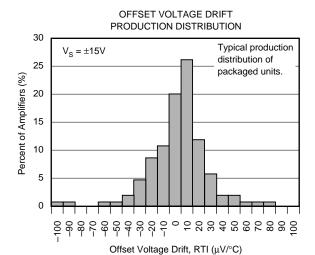
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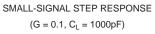
At T_A = +25°C, V_S = ±15V, G = 0.1, R_L = 10k Ω connected to ground and Ref pin connected to ground, unless otherwise noted.

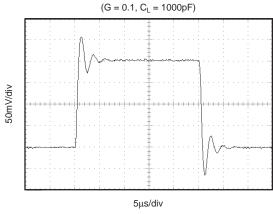






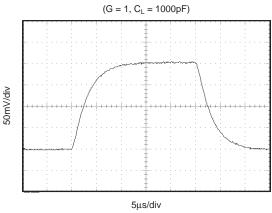




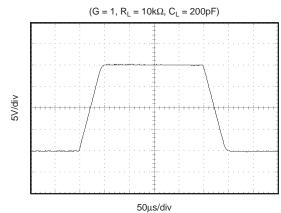


SMALL-SIGNAL STEP RESPONSE

5μs/div



LARGE-SIGNAL STEP RESPONSE



APPLICATION INFORMATION

The INA146 is a programmable gain difference amplifier consisting of a gain of 0.1 difference amplifier and a programmable-gain output buffer stage. Basic circuit connections are shown in Figure 1. Power supply bypass capacitors should be connected close to pins 4 and 7 as shown. The amplifier is programmable in the range of G=0.1 to G=50 with two external resistors.

The output of A1 is connected to the noninverting input of A2 through a $10k\Omega$ resistor which is trimmed to $\pm 1\%$ absolute accuracy. The A2 input is available for applications such as a filter or a precision current source. See application figures for examples.

OPERATING VOLTAGE

The INA146 is fully specified for supply voltages from $\pm 2.25 \text{V}$ to $\pm 18 \text{V}$ with key parameters guaranteed over the temperature range -40°C to $+85^{\circ}\text{C}$. The INA146 can be operated with single or dual supplies with excellent performance. Parameters that vary significantly with operating voltage, load conditions or temperature are shown in the typical performance curves.

SETTING THE GAIN

The gain of the INA146 is set by using two external resistors, R_{G1} and R_{G2} , according to the equation:

$$G = 0.1 \cdot (1 + R_{G2}/R_{G1})$$

For a total gain of 0.1, A2 is connected as a buffer amplifier with no R_{G1} . A feedback resistor, $R_{G2}=10k\Omega$, should be used in the buffer connection. This provides bias current cancellation (in combination with internal R_5) to assure specified offset voltage performance. Commonly used values are shown in the table of Figure 1. Resistor values for other gains should be chosen to provide a $10k\Omega$ parallel resistance.

COMMON-MODE RANGE

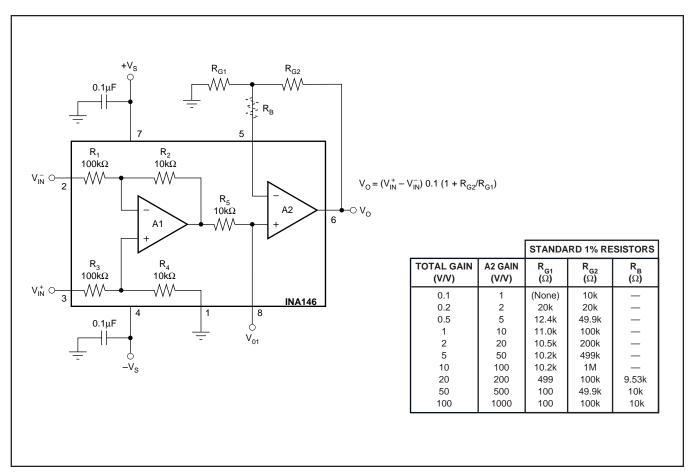


FIGURE 1. Basic Circuit Connections.

OFFSET TRIM

The INA146 is laser-trimmed for low offset voltage and drift. Most applications require no external offset adjustment. Figure 2 shows an optional circuit for trimming the offset voltage. A voltage applied to the Ref terminal will be summed with the output signal. This can be used to null offset voltage. To maintain good common-mode rejection, the source impedance of a signal applied to the Ref terminal should be less than 10Ω and a resistor added to the positive input terminal should be 10 times that, or 100Ω . Alternatively, the trim voltage can be buffered with an op amp such as the OPA277.

INPUT IMPEDANCE

The input impedance of the INA146 is determined by the input resistor network and is approximately $100 \mathrm{k}\Omega.$ The source impedance at the two input terminals must be nearly equal to maintain good common-mode rejection. A 12Ω mismatch in impedance between the two inputs will cause the typical common-mode rejection to be degraded to approximately 72dB. Figure 7 shows a common application measuring power supply current through a shunt resistor. The source impedance of the shunt resistor, R_{S} , is balanced by an equal compensation resistor, R_{C} .

Source impedances greater than 800Ω are not recommended, even if they are perfectly matched. Internal resistors are laser trimmed for accurate ratios, not to absolute values. Adding equal resistors greater than 800Ω can cause a mismatch in the total resistor ratios, degrading CMR.

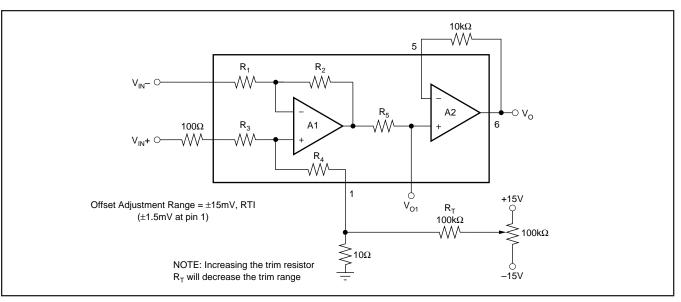
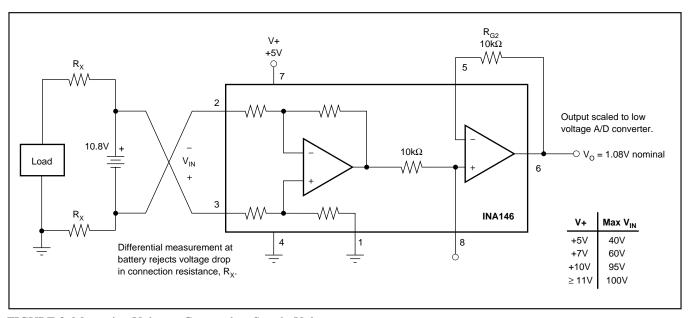
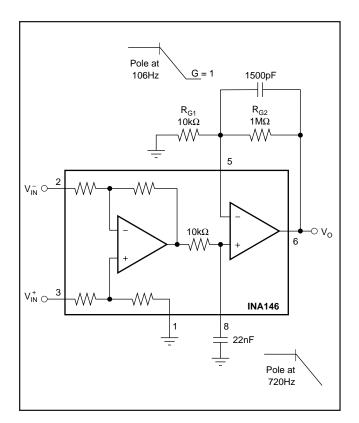


FIGURE 2. Optional Offset Trim Circuit.



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FIGURE 3. Measuring Voltages Greater than Supply Voltage.



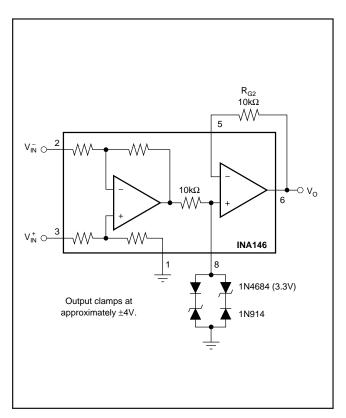


FIGURE 4. Noise Filtering.

FIGURE 5. Output Clamp.

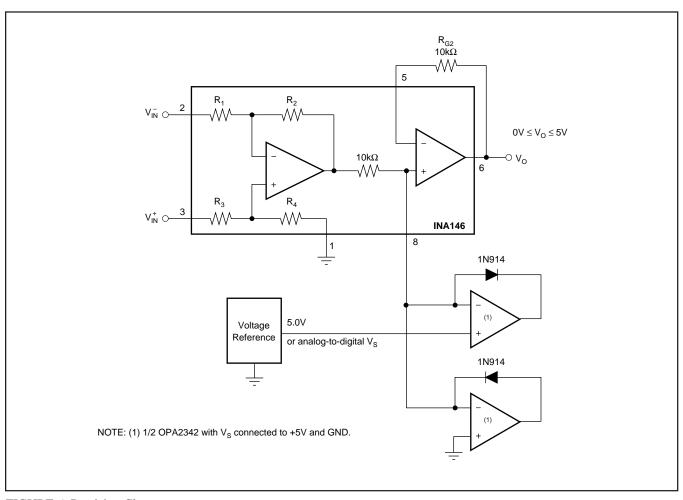


FIGURE 6. Precision Clamp.



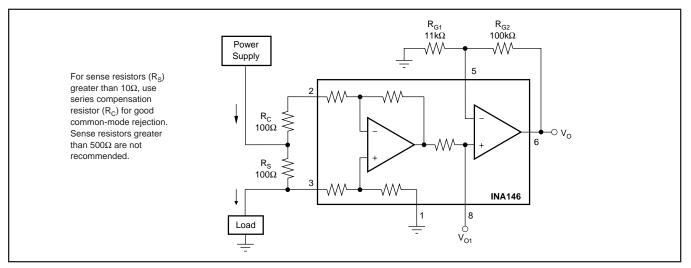


FIGURE 7. Current Monitor, G = 1.

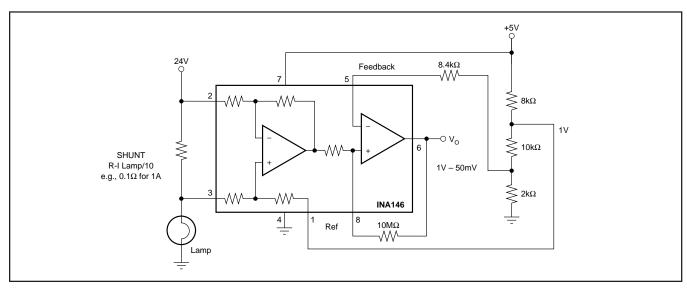


FIGURE 8. Comparator Output with Optional Hysteresis Application to Sense Lamp Burn-Out.

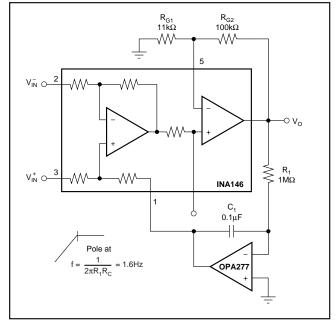


FIGURE 9. AC Coupling (DC Restoration).

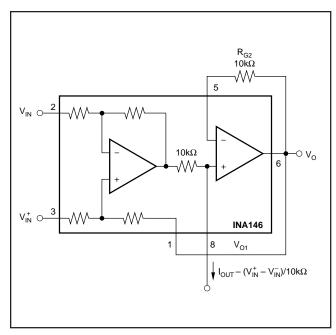


FIGURE 10. Precision Current Source.







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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
INA146UA	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR
INA146UA/2K5	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR
INA146UA/2K5E4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR
INA146UAE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR
SN0406068DR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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TAPE AND REEL INFORMATION





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	A0	Dimension designed to accommodate the component width
Γ	B0	Dimension designed to accommodate the component length
		Dimension designed to accommodate the component thickness
	W	Overall width of the carrier tape
Γ	P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device		Package Drawing			Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
INA146UA/2K5	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1





*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
INA146UA/2K5	SOIC	D	8	2500	346.0	346.0	29.0

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