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January 2016

BC556 / BC557 / BC558 / BC559 / BC560 PNP Epitaxial Silicon Transistor

Features

· Switching and Amplifier

• High-Voltage: BC556, V_{CEO} = -65 V

• Low-Noise: BC559, BC560

• Complement to BC546, BC547, BC548, BC549, and BC550



Ordering Information

Part Number	Marking	Package	Packing Method	
BC556ABU	BC556A	TO-92 3L	Bulk	
BC556ATA	BC556A	TO-92 3L	Ammo	
BC556BTA	BC556B	TO-92 3L	Ammo	
BC556BTF	BC556B	TO-92 3L	Tape and Reel	
BC556BTFR	BC556B	TO-92 3L	Tape and Reel	
BC557ATA	BC557A	TO-92 3L	Ammo	
BC557BTA	BC557B	TO-92 3L	Ammo	
BC557BTF	BC557B	TO-92 3L	Tape and Reel	
BC558BTA	BC558B	TO-92 3L	Ammo	
BC559BTA	BC559B	TO-92 3L	Ammo	
BC559CTA	BC559C	TO-92 3L	Ammo	
BC560CTA	BC560C	TO-92 3L	Ammo	

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parame	Value	Unit		
V _{CBO}		BC556	-80		
	Collector-Base Voltage	BC557 / BC560	-50	V	
		BC558 / BC559	-30		
V _{CEO} (BC556	-65		
	Collector-Emitter Voltage	BC557 / BC560	-45	V	
		BC558 / BC559	-30		
V _{EBO}	Emitter-Base Voltage		-5	V	
I _C	Collector Current (DC)		-100	mA	
I _{CP}	Peak Collector Current (Pulse)		-200	mA	
I _{BP}	Peak Base Current (Pulse)		-200	mA	
TJ	Junction Temperature		150	°C	
T _{STG}	Storage Temperature Range		-65 to +150	°C	

Thermal Characteristics(1)

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
D	Total Power Dissipation	500	mW
P _D	Derate Above 25°C	4.0	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	250	°C/W

Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Тур.	Max.	Unit	
I _{CBO}	Collector Cut-Off Current		V _{CB} = -30 V, I _E = 0			-15	nA	
h _{FE}	DC Curr	ent Gain	$V_{CE} = -5 \text{ V}, I_{C} = -2 \text{ mA}$	110		800		
\/ (cot)	V _{CE} (sat) Collector-Emitter Saturation Voltage		$I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$		-90	-300	mV	
v CE(Sat)			$I_C = -100 \text{ mA}, I_B = -5 \text{ mA}$		-250	-650		
V (ant) Callegton Bone Setunation Voltage		r Page Caturation Voltage	$I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$		-700		mV	
V _{BE} (sat)	Collecto	r-Base Saturation Voltage	$I_C = -100 \text{ mA}, I_B = -5 \text{ mA}$		-900		IIIV	
\/ (on)	V _{RF} (on) Base-Emitter On Voltage		$V_{CE} = -5 \text{ V}, I_{C} = -2 \text{ mA}$	-600	-660	-750	m\/	
v _{BE} (OII)	V _{BE} (on) Base-En	Tiller Off Vollage	$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}$			-800	mV	
f _T	Current Gain Bandwidth Product		$V_{CE} = -5 \text{ V, } I_{C} = -10 \text{ mA,}$ f = 10 MHz		150		MHz	
C _{ob}	Output Capacitance		$V_{CB} = -10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$			6	pF	
NF Noise Figure		BC556 / BC557 / BC558	$V_{CE} = -5 \text{ V}, I_{C} = -200 \mu\text{A},$		2	10		
	Noise	BC559 / BC560	$f = 1 \text{ kHz}, R_G = 2 \text{ k}\Omega$		1	4	dB	
	BC559	$V_{CE} = -5 \text{ V}, I_{C} = -200 \mu\text{A},$		1.2	4.0	UD		
	BC560	$R_G = 2 k\Omega$, $f = 30 \text{ to } 15000 \text{ MHz}$		1.2	2.0			

h_{FE} Classification

Classifica	ation	4	В	С
h _{FE}			200 ~ 450	420 ~ 800

Typical Performance Characteristics

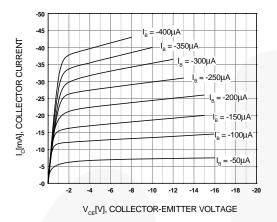


Figure 1. Static Characteristic

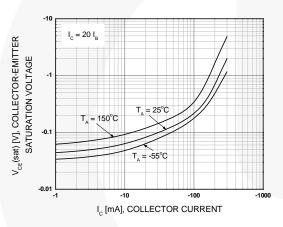


Figure 3. Collector-Emitter Saturation Voltage

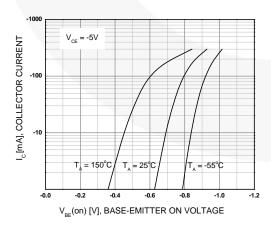


Figure 5. Base-Emitter On Voltage

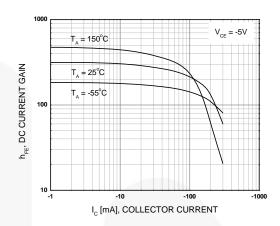


Figure 2. DC Current Gain

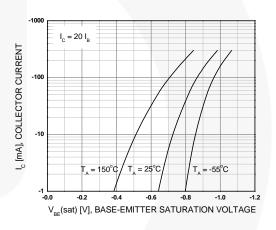


Figure 4. Base-Emitter Saturation Voltage

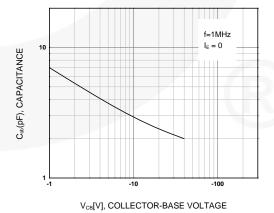


Figure 6. Collector Output Capacitance

Typical Performance Characteristics (Continued)

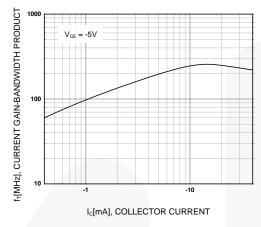


Figure 7. Current Gain Bandwidth Product

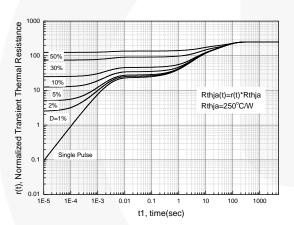


Figure 9. Normalized Transient Thermal Resistance

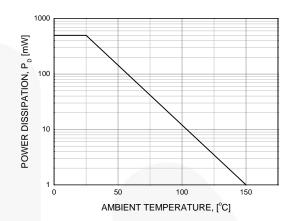
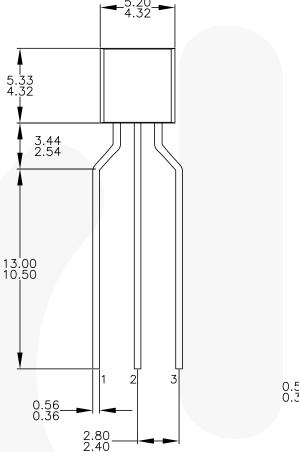


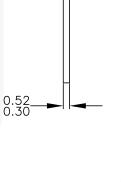
Figure 8. Power Deration

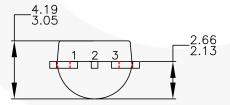
Physical Dimensions $\binom{0.81}{0.79}$ 15.62 13.20 2 0.52 0.56 0.36 1.27 NOTES: UNLESS OTHERWISE SPECIFIED DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZAO3DREV4. A) _4.19 3.05 2 3 FAIRCHILD

Figure 10. 3-LEAD, TO92, JEDEC TO-92 COMPLIANT STRAIGHT LEAD CONFIGURATION, BULK

Physical Dimensions (Continued)







NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC.
 ALL DIMENSIONS ARE IN MILLIMETERS.
 DRAWING CONFORMS TO ASME Y14.5M-2009.
 DRAWING FILENAME: MKT-ZAO3FREV3.
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Figure 11. 3-LEAD, TO92, MOLDED 0.200 IN LINE SPACING LEAD FORM, AMMO, TAPE AND REEL





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Definition of Terms

Definition of Terms				
Datasheet Identification	Product Status	Definition		
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
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