



Micro Commercial Components 20736 Marilla Street Chatsworth

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SI2301

Features

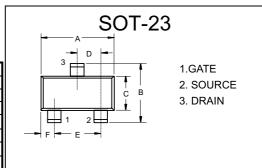
- Halogen free available upon request by adding suffix "-HF"
- -20V,-2.8A, $R_{DS(ON)}$ =120m Ω @ V_{GS} =-4.5V $R_{DS(ON)}$ =150m Ω @V_{GS}=-2.5V
- High dense cell design for extremely low R_{DS(ON)}
- Rugged and reliable
- High Speed Switching
- SOT-23 Package
- Marking Code: S1
- Epoxy meets UL 94 V-0 flammability rating

Moisture Sensitivity Level 1

Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Parameter	Rating	Unit	
V_{DS}	Drain-source Voltage	-20	V	
I _D	Drain Current-Continuous	-2.8	Α	
I _{DM}	Drain Current-Pulsed ^a	-10	Α	
V_{GS}	Gate-source Voltage	±8	V	
P_{D}	Total Power Dissipation	1.25	W	
R _{⊕JA}	Thermal Resistance Junction to Ambient ^b	100	°C/W	
T_J	Operating Junction Temperature	-55 to +150	$^{\circ}\mathbb{C}$	
T _{STG}	Storage Temperature	-55 to +150	°C	

P-Channel Enhancement Mode Field Effect Transistor

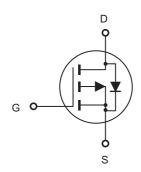




DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.110	.120	2.80	3.04	
В	.083	.104	2.10	2.64	
С	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
Е	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
Н	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	015	020	37	51	

Suggested Solder Pad Layout inches mm

Internal Block Diagram





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Electrical Characteristics T_A = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min	Тур	Max	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS} = 0V, I_{D} = -250\mu A$	-20			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0V			-1	μΑ
Gate Body Leakage Current, Forward	I _{GSSF}	$V_{GS} = 8V, V_{DS} = 0V$			100	nA
Gate Body Leakage Current, Reverse	Igssr	V_{GS} = -8V, V_{DS} = 0V			-100	nA
On Characteristics °						
Gate Threshold Voltage	V _{GS(th)}	$V_{GS} = V_{DS}, I_{D} = -250 \mu A$	-0.45			V
Static Drain-Source		$V_{GS} = -4.5V, I_{D} = -2.8A$		80	120	mΩ
On-Resistance	R _{DS(on)}	$V_{GS} = -2.5V, I_{D} = -2.0A$		110	150	mΩ
Forward Transconductance	9 _{FS}	$V_{DS} = -5V, I_{D} = -2.8A$		8		S
Dynamic Characteristics d						
Input Capacitance	C _{iss}	.,		880		pF
Output Capacitance	C _{oss}	V _{DS} = -6V, V _{GS} = 0V, f = 1.0 MHz		270		pF
Reverse Transfer Capacitance	C _{rss}			175		pF
Switching Characteristics d						
Turn-On Delay Time	t _{d(on)}			11	20	ns
Turn-On Rise Time	t _r	$V_{DD} = -6V, I_{D} = -1A,$		5	10	ns
Turn-Off Delay Time	t _{d(off)}	$V_{GS} = -4.5V$, $R_{GEN} = 6\Omega$		32	65	ns
Turn-Off Fall Time	t _f			23	45	ns
Total Gate Charge	Qg	\/ O\/ O O A		11	14.5	nC
Gate-Source Charge	Q _{gs}	$V_{DS} = -6V, I_{D} = -2.8A,$ $V_{GS} = -4.5V$		1.5		nC
Gate-Drain Charge	Q _{gd}	- VGS 1.0V		2.1		nC
Drain-Source Diode Characteristics and Maximun Ratings						
Drain-Source Diode Forward Current b	I _S				-0.75	Α
Drain-Source Diode Forward Voltage c	V _{SD}	$V_{GS} = 0V, I_{S} = -0.75A$			-1.2	V

Notes:
a.Repetitive Rating: Pulse width limited by maximum junction temperature.b.Surface Mounted on FR4 Board, t < 5 sec.
c.Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
d.Guaranteed by design, not subject to production testing.



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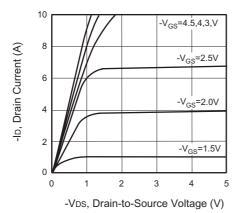


Figure 1. Output Characteristics

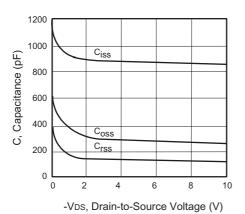


Figure 3. Capacitance

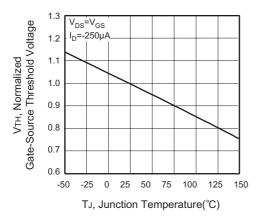


Figure 5. Gate Threshold Variation with Temperature

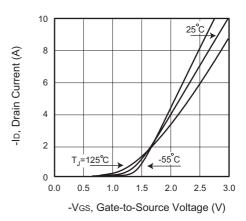


Figure 2. Transfer Characteristics

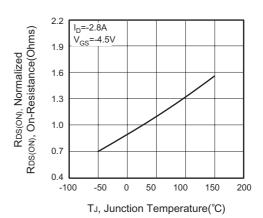


Figure 4. On-Resistance Variation with Temperature

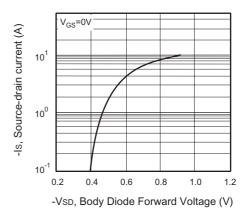


Figure 6. Body Diode Forward Voltage Variation with Source Current



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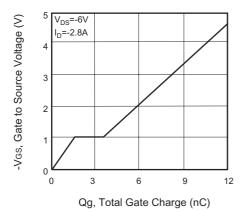


Figure 7. Gate Charge

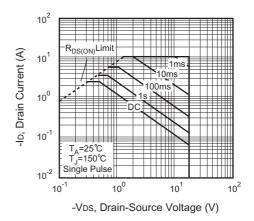


Figure 8. Maximum Safe Operating Area

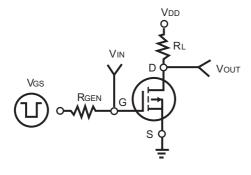


Figure 9. Switching Test Circuit

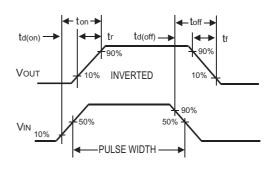


Figure 10. Switching Waveforms

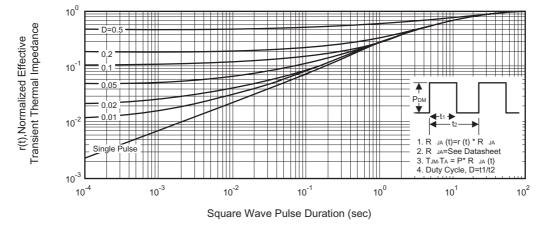


Figure 11. Normalized Thermal Transient Impedance Curve

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Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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