

Dual N-Ch MOSFET

General Description

The WSD2098 is the highest performance trench N-ch MOSFETs with extreme high cell density , which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WSD2098 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

Product Summery

BV _{DSS}		I _D
20V	7.0mΩ	9.7A

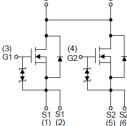
Applications

• Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

- DC-DC Power System
- ESD:2KV

DFN2X3A-6_EP Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	20	V
V _{GS}	Gate-Source Voltage	±12	V
I _D @T _A =25℃	Continuous Drain Current, V _{GS} @ 4.5V ¹	9.7	A
I _D @T _A =70℃	Continuous Drain Current, V _{GS} @ 4.5V ¹	7.5	A
I _{DM}	Pulsed Drain Current ²	38	A
P₀@T _A =25℃	Total Power Dissipation ³	1.0	W
P _D @T _A =70℃	Total Power Dissipation ³	0.6	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter		Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹ (Steady State)		127	°C/W
R _{0JA}	Thermal Resistance Junction-ambient ¹ (t<10S)		80	°C/W



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Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	20			V
$\triangle BV_{DSS} / \triangle T_J$	BVDSS Temperature Coefficient	Reference to 25 $^\circ\!\!{\rm C}$, I_D=1mA		0.022		V/℃
Б	Static Drain-Source On-Resistance ²	V _{GS} =4.5V , I _D =5.5A		7.0	9.0	
R _{DS(ON)}	Static Drain-Source On-Resistance	V_{GS} =2.5V , I _D =5.5A		10.5	13.5	mΩ
V _{GS(th)}	Gate Threshold Voltage		0.5	0.7	1.0	V
$ riangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_{D}=250 \text{uA}$		-2.32		mV/℃
	Drain Source Lookage Current	V_{DS} =16V , V_{GS} =0V , T _J =25 $^{\circ}$ C			1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =16V , V _{GS} =0V , T _J =55℃			5	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} = \pm 12V , V_{DS} =0V			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =10A		65		S
R _g	Gate Resistance	V_{DS} =0V , V_{GS} =0V , f=1MHz		11		Ω
Qg	Total Gate Charge (4.5V)		10	23.2	15	
Q _{gs}	Gate-Source Charge	V_{DS} =10V , V_{GS} =4.5V , I_{D} =5.5A		1.9		nC
Q _{gd}	Gate-Drain Charge			4.8		
T _{d(on)}	Turn-On Delay Time			8		
Tr	Rise Time	V_{DD} =10V , V_{GS} =10V , R_G =1 Ω ,		20		
T _{d(off)}	Turn-Off Delay Time	I _D =1A ,RL=10Ω		935		ns
T _f	Fall Time			410		
C _{iss}	Input Capacitance		1000	1470	1920	
Coss	Output Capacitance	V _{DS} =10V , V _{GS} =0V , f=1MHz	150	258	295	pF
C _{rss}	Reverse Transfer Capacitance		100	202	288	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,4}				2	А
I _{SM}	Pulsed Source Current ^{2,4}	$V_G = V_D = 0V$, Force Current			8	А
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25℃		0.7	1.3	V
t _{rr}	Reverse Recovery Time			445		nS
Q _{rr}	Reverse Recovery Charge	l͡₣=5.5A,dl/dt=100A/μs , Tյ=25℃		2175		nC

Note :

3.The power dissipation is limited by 150 $^\circ\!\!\!\mathrm{C}$ junction temperature

4. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

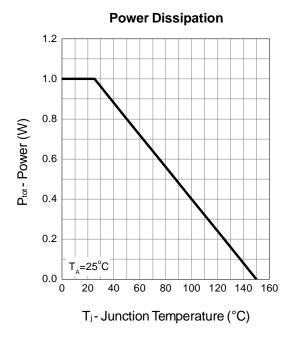
^{1.} The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t<10sec.

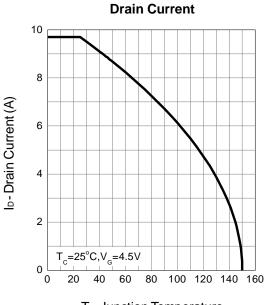
^{2.}The data tested by pulsed , pulse width $\,\leq\,$ 300us , duty cycle $\,\leq\,$ 2%



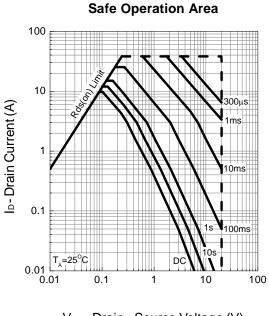
Dual N-Ch MOSFET

Typical Characteristics



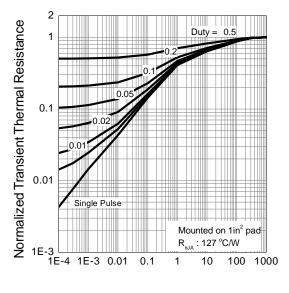


T_j-Junction Temperature



V_{DS} - Drain - Source Voltage (V)

Thermal Transient Impedance

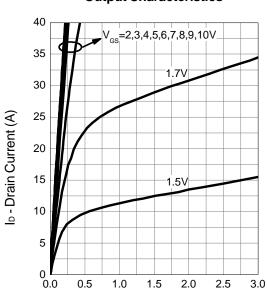


Square Wave Pulse Duration (sec)

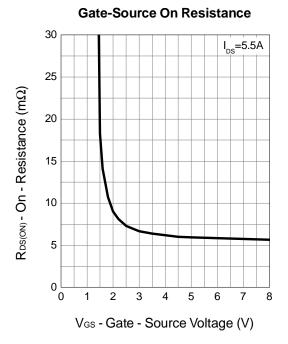


Dual N-Ch MOSFET

Typical Characteristics



VDS - Drain - Source Voltage (V)

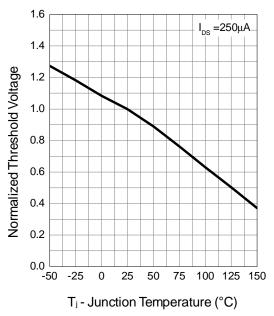


Output Characteristics

11 10 9 $R_{DS(ON)}$ - On - Resistance (m Ω) 8 V_{GS}=2.5V V_{GS}=3.1V 7 6 V_{GS}=4.5V V_{GS}=3.7V V_{GS}=4V 5 4 3 0 6 12 18 24 30 36

ID- Drain Current (A)



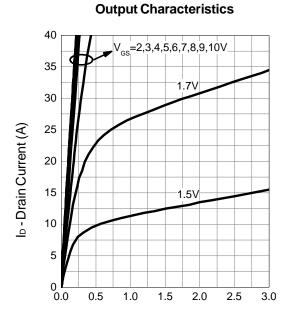


Drain-Source On Resistance

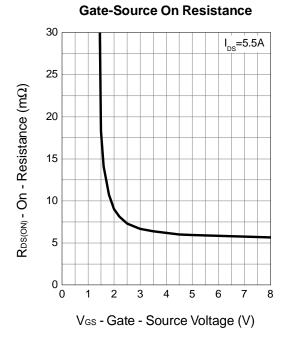


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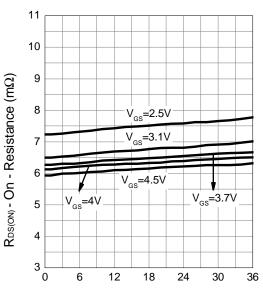
Typical Characteristics



VDS-Drain - Source Voltage (V)

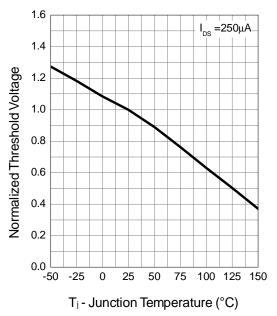


Drain-Source On Resistance



ID-Drain Current (A)

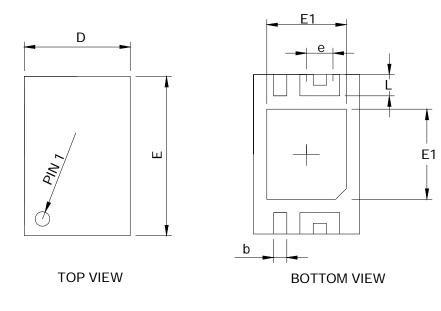


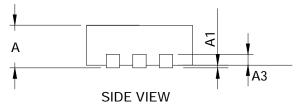




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Package Information DFN2x3A-6_EP





RECOMMENDED LAND PATTERN

	1.6	
		0.5
		Å
	+	1.6 3
0.4		
A	0.25	

S		DFN2x3	3A-6_EP		
SY - BO L	MILLIMETERS		INCHES		
0 L	MIN.	MAX.	MIN.	MAX.	
А	0.70	1.00	0.028	0.039	
A1	0.00	0.05	0.000	0.002	
A3	0.203 REF		0.008 REF		
b	0.20	0.30	0.008	0.012	
D	1.90	2.10	0.075	0.083	
E1	1.60	1.80	0.063	0.071	
E	2.90	3.10	0.114	0.122	
D1	1.40	1.60	0.055	0.063	
е	0.50 BSC		0.02	BSC	
L	0.30	0.50	0.012	0.020	



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