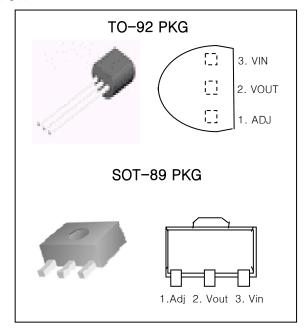
3-TERMINAL 100mA POSITIVE ADJUSTABLE REGULATOR

FEATURES

- Output current in Excess of 100mA
- Output Adjustable Between 1.2V and 37V
- Internal Thermal-Overload Protection
- Internal Short-Circuit Current-Limiting
- Output Transistor Safe-Area Compensation
- Floating operation for high voltage applications
- Moisture Sensitivity Level 3

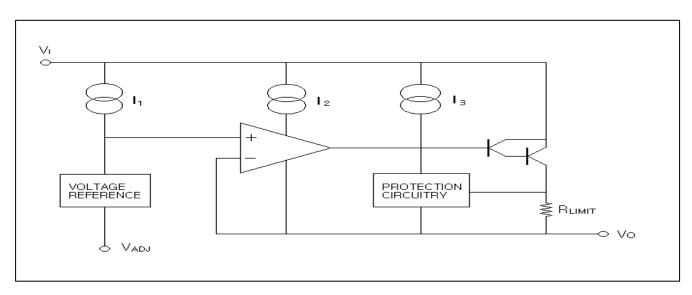


DISCRIPTION

This monolithic integrated circuit is an adjustable 3-terminal positive voltage regulator designed to supply more than 100mA of load current with an output voltage adjustable over a 1.2 to 37V. It employs internal current limiting, thermal shut-down and safe area compensation.

ORDERING INFORMATION						
Device	Marking	Package				
LM317L	LM317L	TO-92				
LM317F	317	SOT-89				

BLOCK DIAGRAM



TYPICAL APPLICATIONS

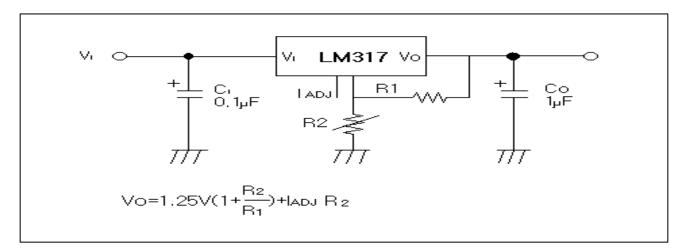


Fig.5 Programmable Regulator

 C_l is required when regulator is located in appreciable distance from power supply filter. Co is not needed for stability, however, it does improve transient response. Since l_{ADJ} is controlled to less than $100\mu\text{A}$, the error associated with this term is negligible in most applications.

ABSOLUTE MAXIMUM RATINGS (TA=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit	
Input-output Voltage Differential	V _I -V _O	40	V	
Lead Temperature	TLEAD	230	$^{\circ}$	
Power Dissipation	Po	Internally limited	_	
Operating Temperature Range	Topr	0 ~ +125	$^{\circ}$	
Storage Temperature Range	Tstg	−65 ~ +125	$^{\circ}$	

ELECTRICAL CHARACTERISTICS

(V_I-V_O=5V, I_O=40mA, 0 °C \leq T_J \leq 125 °C, I_{MAX}=100mA, unless otherwise specified)

Characteristic	Symbol	Test condition		Min.	Тур.	Max.	Unit
Line Regulation	△Vo	T _A =0 ~ 125℃	3V≤Vı-Vo≤40V		0.01	0.04	%/V
		1A=0 ~ 125 C	3V≤Vı-Vo≤40V		0.02	0.07	%/V
Load Regulation	△Vo	T _A =25°C, 10 ^m A≤ ₀ ≤ _{MAX}					
		Vo≤5V			10	25	mV
		Vo≥5V			0.1	0.5	%/Vo
		10mA≤ ₀ ≤ _{MAX}					
		Vo≤5V			20	70	mV
		Vo≥5V			0.3	1.5	%/Vo
Adjustable Pin Current	ladj			46	100	μA	
Adjustable Pin Current Cha	△ladj	3V≤V:-Vo≤40V					
		10mA≤ ₀ ≤ _{MAX}			0.2	5	μA
		P≤P _{MAX}					
Reference Voltage	VREF	3V≤VIN-Vout≤40V					
		10mA≤ ₀ ≤ _{MAX}		1.20	1.25	1.30	V
		P _D ≤P _{MAX}					
Temperature Stability	ST⊤				0.7		%/Vo
Minimum Load Current to	Lama	V _I -V _O =40V			3.5	10	mA
Maintain Regulation	L(MIN)				3.5	10	IIIA
Maximum Output Current	IO(MAX)	Vı−Vo≤5V, PD≤PMAX		100	200		mA
		V _I -V _O ≤40V, P _D ≤P _{MAX} , T _A = 25 °C		156	400		mA
RMS Noise, % of Vout	θΝ	T _A =25℃, 10ŀ		0.003	0.01	%/Vo	
Ripple Rejection	RR	Vo=10V, f=120Hz					
		without Cadu			60		dB
		CadJ=10 μ F		66	75		
Long-Term Stability,	ST	T _A =25℃, for end point			0.3	1	9/
TJ=THIGH		measurements, 1000HR			0.3	I	%

^{*} Load and line regulation are specified at constant junction temperature. Change in Vodue to heating effects must be taken into account separately. Pulse testing with low duty is used.