

SE100250GTS

N-Channel Enhancement-Mode MOSFET

Revision: A

General Description

Advanced trench technology to provide excellent RDS(ON), low gate charge and low operation voltage. This device is suitable for using as a load switch or in PWM applications.

- Simple Drive Requirement
- Small Package Outline
- Surface Mount Device

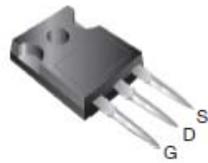
Features

For a single MOSFET

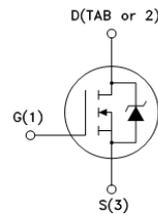
- $V_{DS} = 100V$
- $R_{DS(ON)} = 2.5m\Omega @ V_{GS}=10V$

Pin configurations

See Diagram below



TO-247



Absolute Maximum Ratings

Parameter		Symbol	Rating	Units
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	V
Drain Current	Continuous	I_D	250	A
	Pulsed		1000	
Single pulse avalanche energy		E_{AS}	2000	mJ
Total Power Dissipation	@TA=25°C	P_D	400	W
Operating Junction Temperature Range		T_J	-55 to 175	°C

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Electrical Characteristics (T _J =25°C unless otherwise noted)						
Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS (Note 2)						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =250μA, V _{GS} =0 V	100			V
I _{DSS}	Drain to Source Leakage Current	V _{DS} =100V, V _{GS} =0V			1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =20V			100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D =250μA	3	3.6	5	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =125A	-	2.5	3.0	mΩ
g _{FS}	Forward Transconductance	V _{DS} =10V, I _D =125A	120			S
DYNAMIC PARAMETERS						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =50V, f=1MHz		15700		pF
C _{oss}	Output Capacitance			1600		pF
C _{rss}	Reverse Transfer Capacitance			101		pF
SWITCHING PARAMETERS						
Q _g	Total Gate Charge ²	V _{GS} =10V, V _{DS} =30V, I _D =30A		208		nC
Q _{gs}	Gate Source Charge			86		nC
Q _{gd}	Gate Drain Charge			38.4		nC
t _{d(on)}	Turn-On Delay Time	V _{GS} =10V, V _{DS} =50V, R _{GEN} =1.8Ω I _D =125A		30		ns
t _{d(off)}	Turn-Off Delay Time			95		ns
t _{d(r)}	Turn-On Rise Time			85		ns
t _{d(f)}	Turn-Off Fall Time			38		ns
Thermal Resistance						
Symbol	Parameter		Typ	Max	Units	
R _{θJC}	Thermal Resistance Junction to Case(t≤10s)		-	0.38	°C/W	

Typical Characteristics

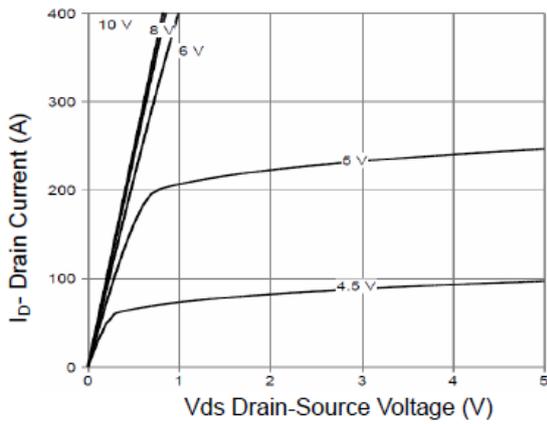


Figure 1 Output Characteristics

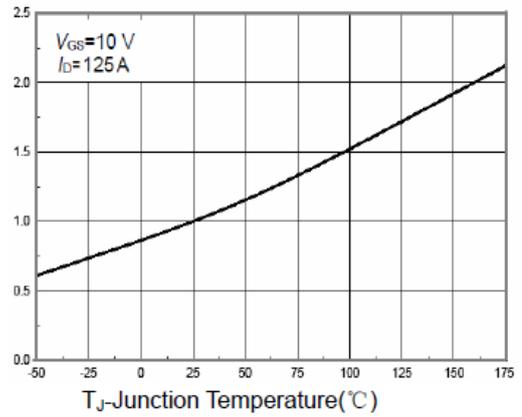


Figure 4 R_{dson} -Junction Temperature

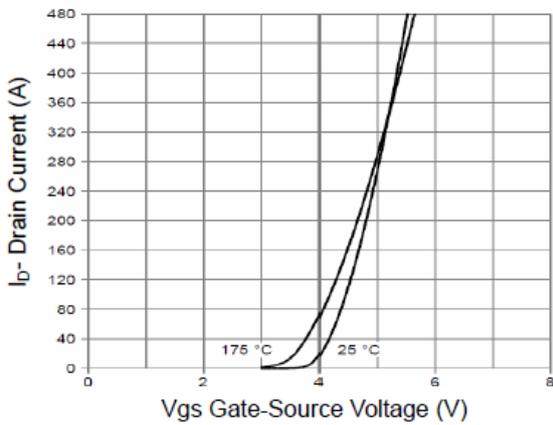


Figure 2 Transfer Characteristics

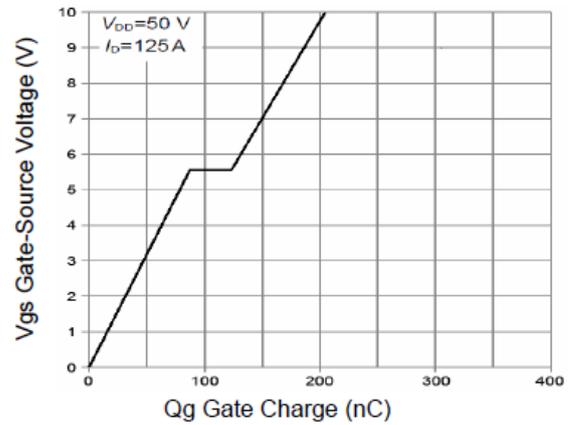


Figure 5 Gate Charge

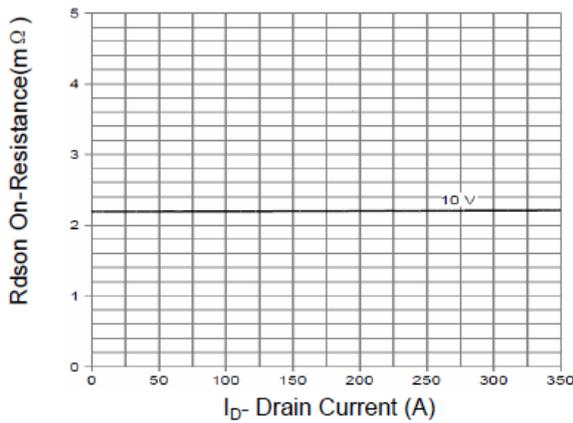


Figure 3 R_{dson} - Drain Current

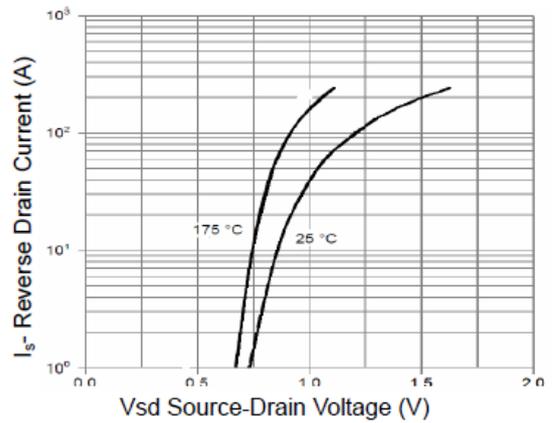


Figure 6 Source- Drain Diode Forward

Typical Characteristics

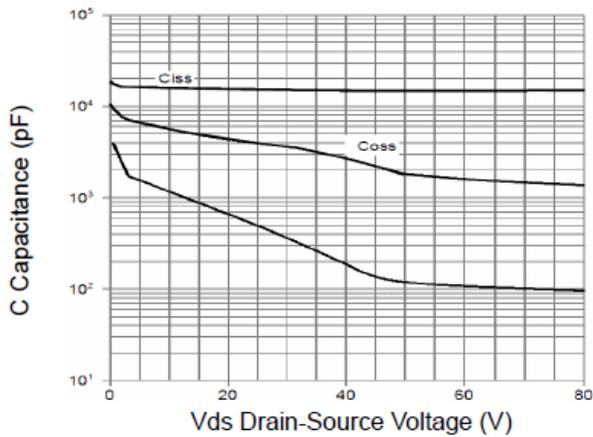


Figure 7 Capacitance vs Vds

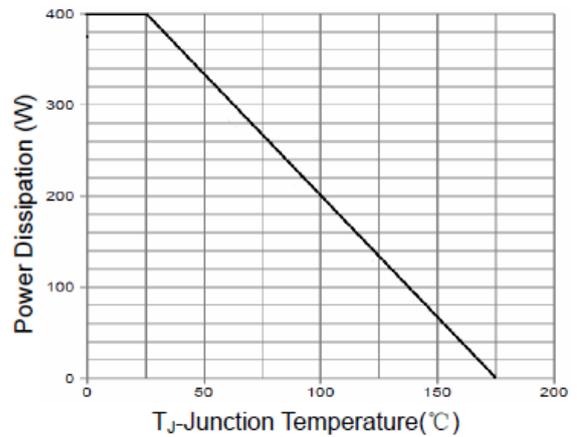


Figure 9 Power De-rating

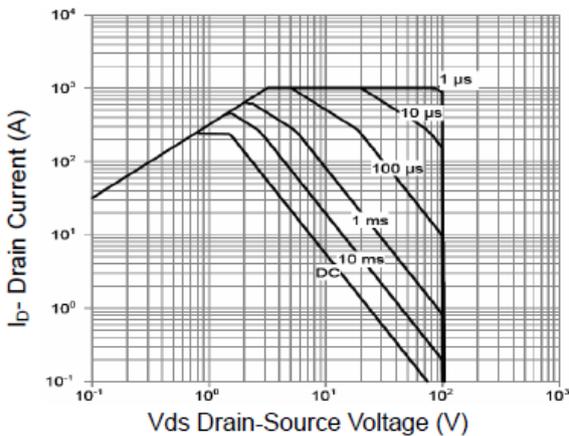


Figure 8 Safe Operation Area

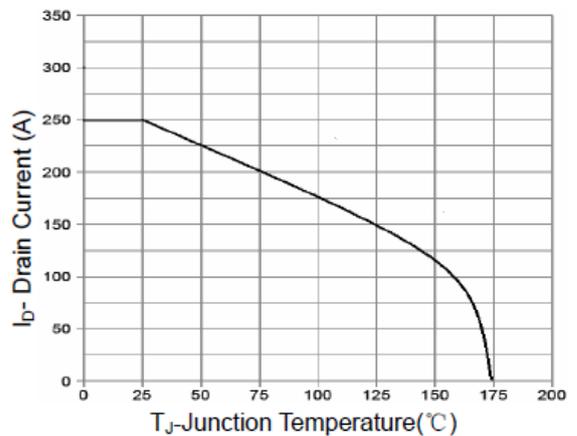


Figure 10 Current De-rating

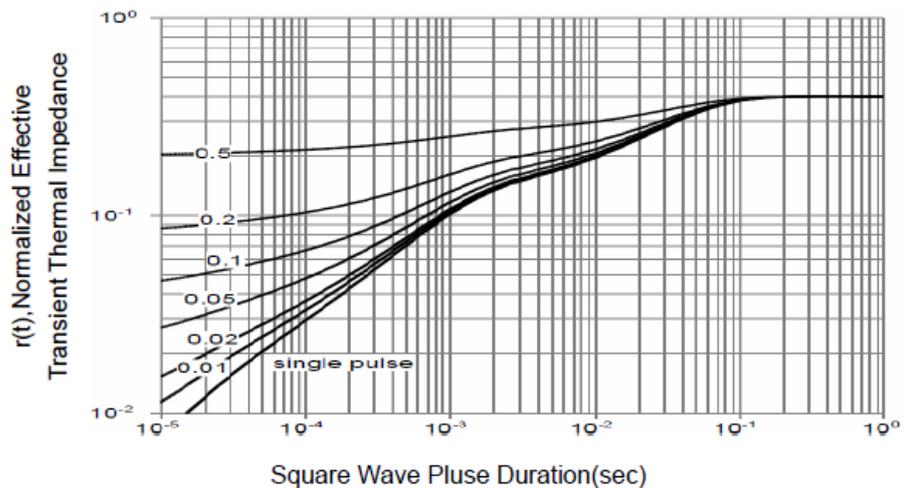
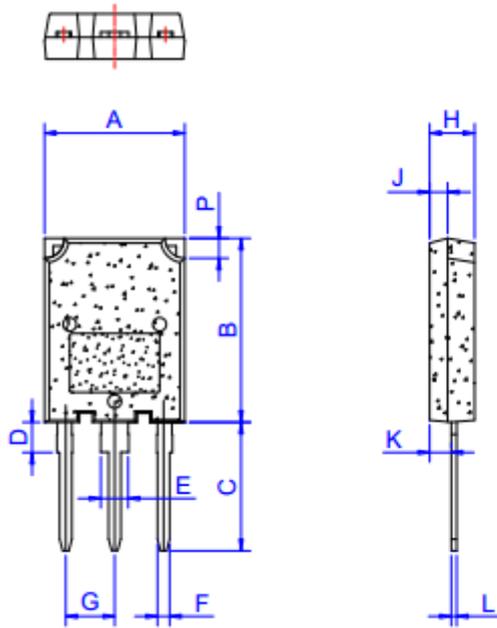


Figure 11 Normalized Maximum Transient Thermal Impedance

SE100250GTS

Package Outline Dimension

TO-247S



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	15.1		16.1	0.594		0.634
B	19.8		20.8	0.78		0.819
C	13.8		14.8	0.543		0.583
D	3.00		4.00	0.118		0.157
E	2.75		3.35	0.108		0.132
F	1.30		1.50	0.051		0.059
G	5.10		5.80	0.201		0.228
H	4.50		5.50	0.177		0.217
J	1.45		2.15	0.057		0.085
K	1.90		2.80	0.075		0.110
L	0.55		0.80	0.022		0.031
P	2.00		2.40	0.079		0.094

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