

Temperature Switch with Selectable Hysteresis

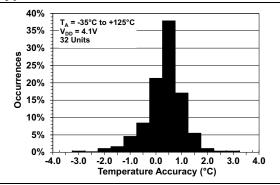
Features

- · Factory Set Temperature Switch
- · Available Temperature Switch Thresholds:
 - T_{SET} = -35°C, -25°C, -15°C, -5°C, +5°C, +15°C, +25°C, +35°C, +45°C, +55°C, +65°C, +75°C, +85°C, +95°C, +105°C, +115°C, +125°C
- Wide Operating Voltage Range: 2.7V to 5.5V
- Low Supply Current: 25 µA (typical)
- Qualification: AEC-Q100 Rev. G, Grade 1 (-40°C to +125°C)
- Temperature Switch Accuracy:
 - ±1°C (typical)
 - ±4°C (maximum) between -15°C to +75°C
 - ±6°C (maximum) between -40°C to +125°C
- Switch Threshold Options (Hot/Cold):
 - Rising temperature: MCP9501/2 (Hot option)
 - Falling temperature: MCP9503/4 (Cold option)
- Output Configuration Options:
 - Active-low, open-drain output: MCP9501/3
 - Uses external pull-up resistor
 - Active-high, push-pull output: MCP9502/4
- User-Selectable Hysteresis: +2°C or +10°C (typical)
- · 5-Lead SOT-23 Package

Applications

- Power Supply Critical Temperature Shutdown
- Temperature Alarm
- Thermostat Control
- Fan Control
- Base Stations
- · Automotive

Typical Performance



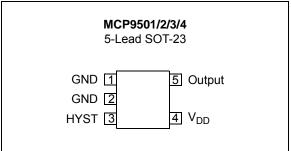
Description

Microchip Technology's MCP9501/2/3/4 family devices are temperature switches with $\pm 1^{\circ}$ C (typical) accurate factory set output thresholds. These devices are ideal for high-power supply systems where an overtemperature protection circuit is needed. These devices do not require external components, consume 25 µA (typical), and the factory set thresholds provide simplicity.

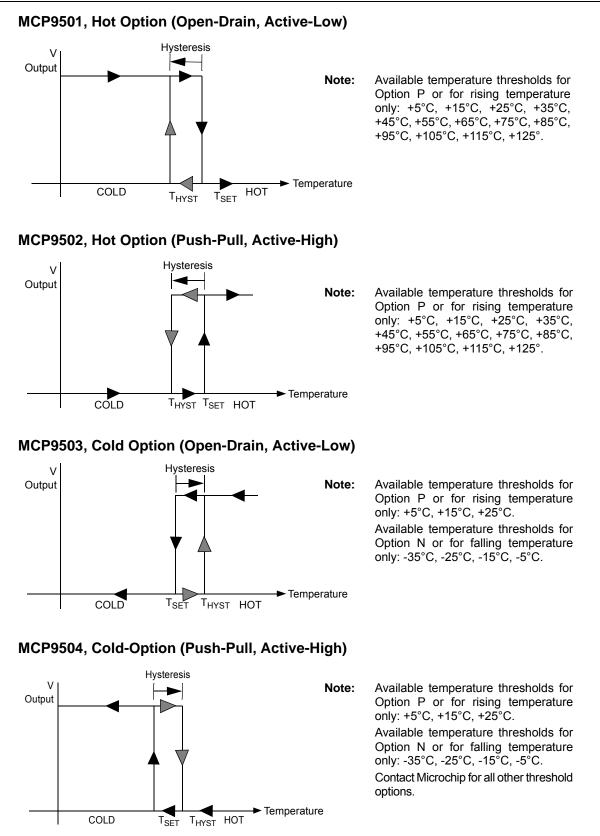
In addition, this family of devices provides user-selectable +2°C and +10°C (typical) switch hysteresis, and various output configurations. The MCP9501/2 outputs switch for rising temperatures, while the MCP9503/4 devices switch for falling temperatures, with the relative hysteresis at the set thresholds. This family of devices is also available with an active-high, push-pull output (MCP9502/4) and an active-low, open-drain output (MCP9501/3). The push-pull output is ideal for a microcontroller interface, while the open-drain output can be used for level shifting, wired-OR configuration or as a heater on/off switch.

The MCP9501/2/3/4 devices operate from a 2.7V to 5.5V supply. This family is available with the space-saving 5-lead SOT-23 package.

Package Types



OUTPUT FUNCTIONAL DESCRIPTION



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings^(†)

V _{DD}	6.0V
Voltage on All Input/Output Pins – GND	0.3V to 6.0V
Input/Output Current	20 mA
Storage Temperature	65°C to +150°C
Ambient Temperature with Power Applied	40°C to +125°C
Junction Temperature (T _J)	+150°C
ESD Protection on All Pins:	
НВМ	4 kV
MM	400V
Latch-up Current at Each Pin (+25°C)	±200 mA

† Notice: Stresses above those listed under "Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational listings of this specification is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.

DC CHARACTERISTICS

Electrical Specifications: Unless otherwise indicated, V_{DD} = 2.7V to 5.5V, T_A = -40°C to +125°C, GND = Ground.							
Parameters	Symbol	Minimum	Typical	Maximum	Unit	Conditions	
Sensor Accuracy		-4	±1	+4	°C	-15°C ≤ T _A ≤ +75°C (Note 1)	
		-6	±2	+6	°C	-40°C ≤ T _A ≤ +125°C	
Power Supply							
Operating Voltage	V _{DD}	2.7	_	5.5	V		
Operating Current	I _{DD}	_	25	40	μA		
Line Regulation	$\Delta^{\circ}C/\Delta V$	_	0.2	_	°C/V	V _{DD} = 2.7V to 5.5V	
Hysteresis							
Trip Point Hysteresis	T _{HYST}	_	2	_	°C	HYST = GND	
			10	—	°C	HYST = V _{DD}	
Hysteresis Select Input	V _{IH}	0.8 V _{DD}	_		V		
	V _{IL}	_	_	0.2 V _{DD}	V		
	I _{LEAK}	_	0.1	_	μA		
Open-Drain Output Leakage	I _{LEAK}	_	0.1	10	μA	MCP9501/3	
Output Voltage High	V _{OH}	0.8 V _{DD}	_		V	I _{OUT} = 5 mA (MCP9502/4)	
Output Voltage Low	V _{OL}	—	_	0.2 V _{DD}	V	I _{OUT} = 5 mA	
Turn On Time	T _{ON}	—	1	_	ms		
Response Time to Thermal Shock: SOT23-5	T _{RES}	—	1.7	_	S	Time to 63% (+89°C), +25°C (air) to +125°C (oil bath)	

Note 1: This specification is tested at mid-supply of 4.1V for optimum operation across the supply voltage range of 2.7V to 5.5V.

TEMPERATURE SPECIFICATIONS

Parameters	Symbol	Minimum	Typical	Maximum	Unit	Conditions	
Temperature Ranges							
Specified Temperature Range	T _A	-40	_	+125	°C	(Note 1)	
Operating Temperature Range	T _A	-40	_	+125	°C		
Storage Temperature Range	T _A	-65	_	+150	°C		
Thermal Package Resistances	-						
Thermal Resistance	θ_{JA}	_	220.7	_	°C/W		

Note 1: Operation in this range must not cause T_J to exceed the maximum junction temperature (+150°C).

2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

Note: Unless otherwise indicated, V_{DD} = 2.7V to 5.5V, T_A = -40°C to +125°C, GND = Ground, $R_{PULL-UP}$ = 10 k Ω (**MCP9501/3** only) and 0.1 μ F bypass capacitor.

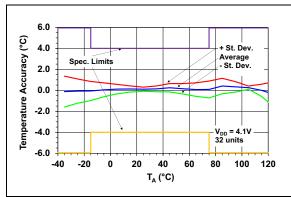


FIGURE 2-1:

Temperature Accuracy.

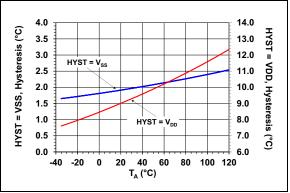


FIGURE 2-2:

Hysteresis vs. Temperature.

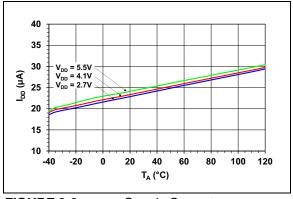


FIGURE 2-3: Temperature.

Supply Current vs.

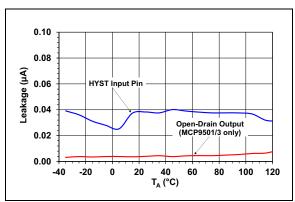


FIGURE 2-4:

Leakage vs. Temperature.

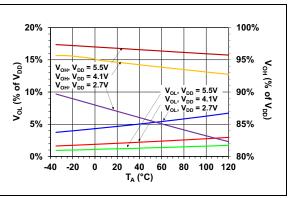


FIGURE 2-5: V_{OL}, V_{OH} vs. Temperature.

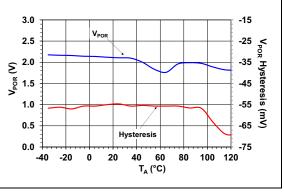


FIGURE 2-6: Power-on Reset Threshold vs. Temperature.

Note: Unless otherwise indicated, V_{DD} = 2.7V to 5.5V, T_A = -40°C to +125°C, GND = Ground, $R_{PULL-UP}$ = 10 k Ω (MCP9501 only) and 0.1 µF bypass capacitor.

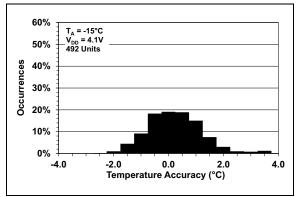


FIGURE 2-7: Temperature Accuracy Distribution at -15°C.

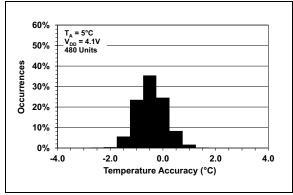


FIGURE 2-8: Temperature Accuracy Distribution at +5°C.

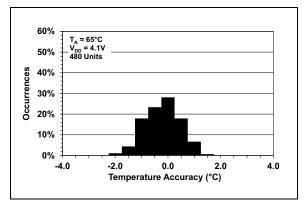


FIGURE 2-9: Temperature Accuracy Distribution at +65°C.

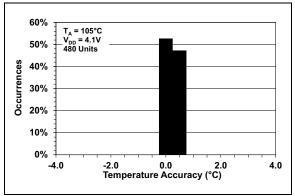


FIGURE 2-10: Temperature Accuracy Distribution at +105°C.

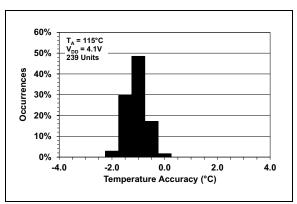


FIGURE 2-11: Temperature Accuracy Distribution at +115°C.

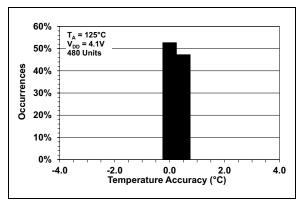


FIGURE 2-12: Temperature Accuracy Distribution at +125°C.

3.0 PIN DESCRIPTION

The description of the pins is listed in Table 3-1.

MCP9501/2/3/4	501/2/3/4 Description				
5-Lead SOT-23	Symbol	Description			
1	GND	Ground			
2	GND	Ground (must be connected to ground)			
3	HYST	Hysteresis Selection Input: HYST = GND \rightarrow Hysteresis is +2°C (typical) HYST = V _{DD} \rightarrow Hysteresis is +10°C (typical)			
4	V _{DD}	Power Pin			
5	Output	Output Options: MCP9501 → Open-Drain, Active-Low Output (Hot Option) MCP9502 → Push-Pull, Active-High Output (Hot Option) MCP9503 → Open-Drain, Active-Low Output (Cold Option) MCP9504 → Push-Pull, Active-High Output (Cold Option)			

3.1 Ground (GND)

The GND pin is the system ground pin. Pin 2 must be connected to system ground. Pin 1 can also be connected to system ground which would provide better thermal conduction to the die.

3.2 Hysteresis Input (HYST)

This is an input pin which can be connected to V_{DD} or GND to select the output hysteresis. Either +2°C (HYST = GND) or +10°C (HYST = V_{DD}) of the typical hysteresis can be selected.

3.3 Power Pin (V_{DD})

The operating voltage range, as specified in the "DC Characteristics" table, is applied to this pin.

3.4 Switch Output (Output)

This output is triggered when the temperature rises or falls beyond the programmed trip temperature threshold. MCP9501/3 devices require an external pull-up resistor.

4.0 FUNCTIONAL DESCRIPTION

The MCP9501/2/3/4 temperature switch family integrates a thermal diode, a comparator and a factory-selectable resistive network used to set the temperature thresholds. The available output thresholds range from -35°C to +125°C at 10°C increments. There is no additional configuration required to operate this device. The selectable output hysteresis is controlled using a single input pin. When this pin is connected to ground, the output hysteresis is +2°C (typical), and when connected to V_{DD}, the output hysteresis is +10°C (typical). Figure 4-1 shows the functional block diagram.

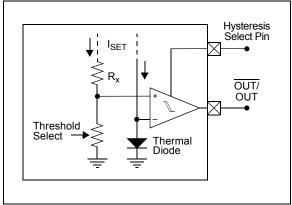
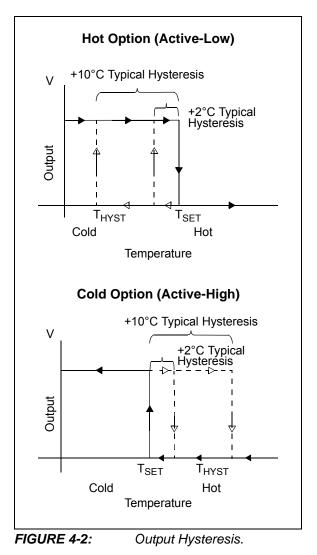


FIGURE 4-1:

Functional Block Diagram.

There are two output configurations for this family: a push-pull, and an open-drain output with active-high and active-low assertions. These assertion options are referred to as Cold and Hot options, primarily due to the direction of the selected hysteresis. For the Cold option, temperature has to fall below the threshold for the output to assert high and deassert low when the temperature rises above the threshold plus the hysteresis. For example, for a +65°C threshold and +2°C (typical) hysteresis, when temperature falls below +65°C, the output asserts high and deasserts low when temperature rises above +67°C. For the Hot option, the opposite is true. When temperature rises above +65°C, the output asserts low and deasserts high when the temperature falls below +63°C. Figure 4-2 shows a graphical description for the Hot and Cold options.



The push-pull output is ideal for a microcontroller interface using an input/output pin or an interrupt input pin. The open-drain option can be used with multiple sensors in a wired-OR configuration or as a level shifter.

4.1 Application Information

The MCP9501/2/3/4 temperature switch family integrates a temperature sensor and a comparator circuit, which outputs an alert signal when the factory set temperature threshold is exceeded. No additional component is required for device operation, which provides simplicity to the system designer. The device output options provide design flexibility for various applications, such as overtemperature protection circuit or a closed-loop temperature control unit. This device can be interfaced to a closed-loop fan controller network without the need for a microcontroller.

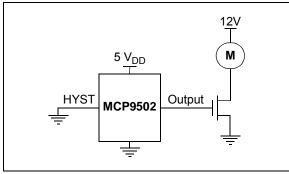


FIGURE 4-3: Fan Controller Using MCP9502.

The MCP9501/2/3/4 family provides an open-drain output, where multiple sensors from multiple PCB hotspots, can be connected to a single processor I/O input with a wired-OR configuration. The MCP9501 requires an external pull-up resistor, which can be used to level shift the alert signal. For example, if the sensors are powered with 5 V_{DD} and the controller or processor is powered with 3 V_{DD}, the external resistor can be level shifted by connecting 3 V_{DD} to the pull-up resistor, as shown in Figure 4-4.

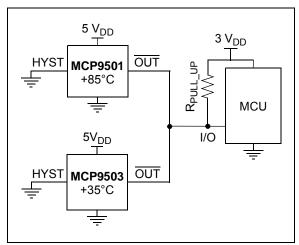


FIGURE 4-4: MCP9501 Wired-OR Output Configuration with Level Shift.

4.1.1 LAYOUT CONSIDERATION AND THERMAL CONSIDERATION

This family of sensors measures temperature by monitoring the voltage level of a thermal diode located in the die. A low-impedance thermal path between the die and the PCB is provided by the pins. Therefore, the sensor effectively monitors PCB temperature. For efficient performance, it is recommended to layout the device as close to the heat source as possible.

When connecting an external resistor to the MCP9501/3, the current through the pull-up resistor must be considered to prevent self-heat due to power. This can be determined using Equation 4-1.

EQUATION 4-1: EFFECT OF SELF-HEATING

$$T_{J} - T_{A} = \theta_{JA}(V_{DD} \times I_{DD} + V_{OL} \times I_{OUT})$$

Where:
$$T_{J} = Junction Temperature$$
$$T_{A} = Ambient Temperature$$
$$\theta_{JA} = Package Thermal Resistance$$
$$(220.7^{\circ}C/W)$$
$$V_{OL} = Sensor Output Low Voltage$$

 $v_{OL} = \text{Sensor Output Low viscout}$

I_{OUT} = Output Current

For example, at room temperature, when the output asserts active-low and the maximum $I_{DD} = 50 \ \mu A$, $V_{DD} = 5.5V$, $V_{OL} = 0.3V$ and $I_{OUT} = 5 \ mA$ (see "DC Characteristics"), the self-heating due to power dissipation $(T_J - T_A)$ is ~0.4°C.

4.1.2 POWER SUPPLY REJECTION

The MCP9501/2/3/4 family does not require any additional components. However, it is recommended that a decoupling capacitor of 0.1 μ F to 1 μ F be used between the V_{DD} and GND pins. A high-frequency ceramic capacitor is recommended. It is necessary for the capacitor to be located as close as possible to the power pins in order to provide effective noise protection.

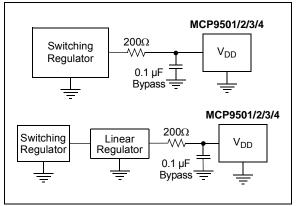
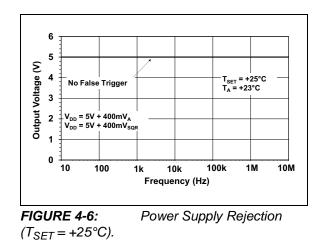


FIGURE 4-5: Power Supply Filter Using a Single Resistor.

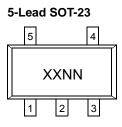
For applications where a switching regulator is used to power the sensor, it is recommended to add a 200Ω resistor in series to V_{DD} to filter out the switcher noise. It is also recommended to add the series resistor in applications where a linear regulator is used to step-down a switching regulator voltage to power the sensor, as shown in Figure 4-5. For example, if a linearly regulated 3.3V from a 5V switching regulator is used to power the sensor, add a 200Ω series resistor.

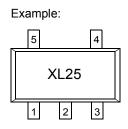
The MCP9501/2/3/4 family of sensors is designed to prevent false output triggers due to high-frequency power supply or system noise. Figure 4-6 shows the device performance with a high-frequency signal added on V_{DD} . The output is not triggered due to the signal added on V_{DD} . With some applications, it is recommended to add a bypass capacitor of 0.1 µF to 1 µF.



5.0 PACKAGING INFORMATION

5.1 Package Marking Information





Device	Code	Device	Code					
Hot Options								
MCP9501PT-005E/OT	WVNN	MCP9502PT-005E/OT	XLNN					
MCP9501PT-015E/OT	WWNN	MCP9502PT-015E/OT	XMNN					
MCP9501PT-025E/OT	WXNN	MCP9502PT-025E/OT	XPNN					
MCP9501PT-035E/OT	WYNN	MCP9502PT-035E/OT	XQNN					
MCP9501PT-045E/OT	WZNN	MCP9502PT-045E/OT	XRNN					
MCP9501PT-055E/OT	X1NN	MCP9502PT-055E/OT	XSNN					
MCP9501PT-065E/OT	X2NN	MCP9502PT-065E/OT	XTNN					
MCP9501PT-075E/OT	X3NN	MCP9502PT-075E/OT	XUNN					
MCP9501PT-085E/OT	X4NN	MCP9502PT-085E/OT	XVNN					
MCP9501PT-095E/OT	X5NN	MCP9502PT-095E/OT	XWNN					
MCP9501PT-105E/OT	X6NN	MCP9502PT-105E/OT	XXNN					
MCP9501PT-115E/OT	X7NN	MCP9502PT-115E/OT	XYNN					
MCP9501PT-125E/OT	X8NN	MCP9502PT-125E/OT	XZNN					
MCP9503PT-005E/OT	XHNN	MCP9504PT-005E/OT	Y9NN					
MCP9503PT-015E/OT	XJNN	MCP9504PT-015E/OT	YANN					
MCP9503PT-125E/OT	XKNN	MCP9504PT-025E/OT	YBNN					
	Cold C	Options						
MCP9503NT-005E/OT	XBNN	MCP9504NT-005E/OT	Y3NN					
MCP9503NT-015E/OT	XCNN	MCP9504NT-015E/OT	Y4NN					
MCP9503NT-025E/OT	XDNN	MCP9504NT-025E/OT	Y5NN					
MCP9503NT-035E/OT	XENN	MCP9504NT-035E/OT	Y6NN					

Note: Contact Microchip for all other threshold options.

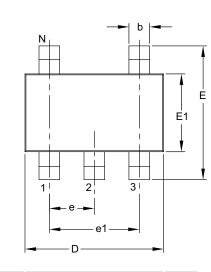
Legend:	XXX Y YY WW NNN @3 *	stomer-specific information ar code (last digit of calendar year) ar code (last 2 digits of calendar year) eek code (week of January 1 is week '01') hanumeric traceability code free JEDEC [®] designator for Matte Tin (Sn) s package is Pb-free. The Pb-free JEDEC [®] designator (€3) n be found on the outer packaging for this package.	
	be carried	nt the full Microchip part number cannot be marked on one line, it will d over to the next line, thus limiting the number of available for customer-specific information.	

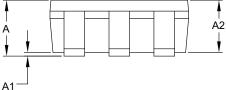
5.2 Package Details

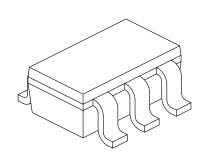
The following section gives the technical details of the packages.

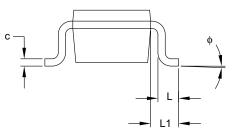
5-Lead Plastic Small Outline Transistor (OT) [SOT-23]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging









	Units		MILLIMETERS			
	Dimension Limits	MIN	NOM	MAX		
Number of Pins	N		5			
Lead Pitch	е		0.95 BSC			
Outside Lead Pitch	e1		1.90 BSC			
Overall Height	A	0.90	-	1.45		
Molded Package Thickness	A2	0.89	-	1.30		
Standoff	A1	0.00	-	0.15		
Overall Width	E	2.20	-	3.20		
Molded Package Width	E1	1.30	-	1.80		
Overall Length	D	2.70	-	3.10		
Foot Length	L	0.10	-	0.60		
Footprint	L1	0.35	-	0.80		
Foot Angle	φ	0°	-	30°		
Lead Thickness	С	0.08	-	0.26		
Lead Width	b	0.20	-	0.51		

Notes:

1. Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.127 mm per side.

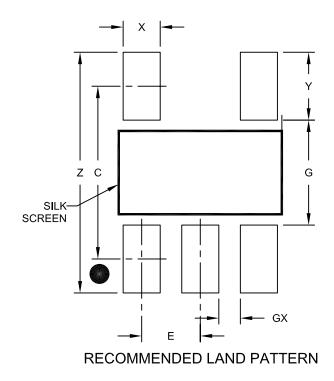
2. Dimensioning and tolerancing per ASME Y14.5M.

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing C04-091B

5-Lead Plastic Small Outline Transistor (OT) [SOT-23]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging



	Ν	ILLIMETER	S	
Dimension	Dimension Limits			
Contact Pitch	E	0.95 BSC		
Contact Pad Spacing	С		2.80	
Contact Pad Width (X5)	X			0.60
Contact Pad Length (X5)	Y			1.10
Distance Between Pads	G	1.70		
Distance Between Pads	GX	0.35		
Overall Width	Z			3.90

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing No. C04-2091A

APPENDIX A: REVISION HISTORY

Revision B (July 2016)

- Added mention of AEC-Q100 qualification (automotive) in the "Features". section.
- Corrected Section 5.1 "Package Marking Information".

Revision A (January 2011)

· Initial release of this document.

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.

PART NO.	X	<u>[X]</u> (1)	<u>-xxxx</u>	X	<u>/xx</u>	Exa	imples:
Device 1 (Temp. Option	Tape and Reel Option	Temp. Switch Threshold	Temp. Range	Package	a)	MCP9501PT-025E/OT: Active-Low, Open-Drain Output, +25°C Switch Threshold, Hot Option, Tape and Reel, Extended Temp., 5LD SOT-23
Device:	M	CP9501/2/3/4:	Resistor Progra Temperature Sv			b)	package. MCP9502PT-025E/OT: Active-High, Push-Pull Output, +25°C Switch Threshold, Hot Option, Extended Temp., 5LD SOT-23 package.
Temperature Option:	e P N	= Hot Option = Cold Option				c) d)	MCP9503NT-025E/OT: Active-Low, Open-Drain Output, -25°C Switch Threshold, Cold Option, Extended Temp., 5LD SOT-23 package. MCP9504NT-025E/OT: Active-High, Push-Pull
Tape and Re Option:	el Bla T	ank = Standard = Tape and	packaging (tube Reel ⁽¹⁾	or tray)		e)	Output, -25°C Switch Threshold, Cold Option, Extended Temp., 5LD SOT-23 package. MCP9501PT-105E/OT: Active-Low, Open-Drain
Temperature Switch Threshold:	0 0 0	$05 = +5^{\circ}C (MCI)$ = -5°C (MCF) 15 = +15°C (MC) = -15°C (MC) 25 = +25°C (MC) = -25°C (MC) 35 = +35°C (MC) = -35°C (MC)	P9503/4) CP9501/2/3/4) P9503/4) CP9501/2/3/4) P9503/4) CP9501/2)			e) f) g) h)	MCP9501P1-105L/OT: Active-Low, Open-Drain Output, +105°C Switch Threshold, Hot Option, Extended Temp., 5LD SOT-23 package. MCP9502PT-105E/OT: Active-High, Push-Pull Output, +105°C Switch Threshold, Hot Option, Extended Temp., 5LD SOT-23 package. MCP9503NT-035E/OT: Active-Low, Open-Drain Output, -35°C Switch Threshold, Cold Option, Extended Temp., 5LD SOT-23 package. MCP9504NT-035E/OT: Active-High, Push-Pull Output, -35°C Switch Threshold, Cold Option, Extended Temp., 5LD SOT-23 package.
	0	45 = +45°C (MC = -45°C (MC	,				
	0	55 = +55°C (MC = -55°C (MC	,			NO	te 1: Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes
		$65 = +65^{\circ}C$ (MC 75 = +75^{\circ}C (MC)					and is not printed on the device pack- age. Check with your Microchip Sales Office for package availability with the
		85 = +85°C (MC					Tape and Reel option.
	0	95 = +95°C (MC	CP9501/2)				
	1	05 = +105°C (N	ICP9501/2)				
	1	15 = +115°C (M	CP9501/2)				
	1	25 = +125°C (N	ICP9501/2)				
Temp. Rang	e: E	= -40°C to +	·125°C				
Package:	0	T = Plastic Sm	all Outline Transis	tor (SOT-2	23), 5-lead		

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