EVERLIGHT EVERLIGHT

DATASHEET

SMD • MID Power LED 67-21S/KK7C-HXXX34Z15/2T(GC)

Preliminary



Features

- PLCC-2 package
- Top view white LED
- High luminous intensity output
- Wide viewing angle
- Pb-free
- RoHS compliant
- ANSI Binning

Description

The Everlight 67-21S package has high efficacy, high CRI, low power consumption, wide viewing angle and a compact form factor. These features make this package an ideal LED for all lighting applications.

Applications

- General lighting
- Decorative and Entertainment Lighting
- Indicators
- Illumination
- Switch lights

Product Number Explanation

67–21S / K K 7 C – H XX XX XX Z15 / 2T

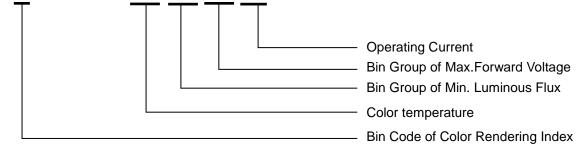


Table of Color Rendering Index

Symbol	Description
М	CRI(Min.) : 60
N	CRI(Min.) : 65
L	CRI(Min.) : 70
Q	CRI(Min.) : 75
K	CRI(Min.) : 80
Р	CRI(Min.) : 85
Н	CRI(Min.) : 90

Note:

Tolerance of Color Rendering Index: ±2

Table of Forward Current Index

Symbol	Description
Z15	I _F :150mA

Example:

67-21S/KK7C-H407034Z15/2T(GC)

CRI	80(Min.)		
ССТ	4000K		
Flux	70lm min		
V _F	3.4V max		
I _F	150mA		

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Mass Production List

Product	CRI Min. ₍₁₎	ССТ(К)	Ф(lm) Min. ₍₂₎
67-21S/KK7C-H306534Z15/2T(GC)	80	3000K	65
67-21S/KK7C-H356534Z15/2T(GC)	80	3500K	65
67-21S/KK7C-H407034Z15/2T(GC)	80	4000K	70
67-21S/KK7C-H507034Z15/2T(GC)	80	5000K	70

Notes:

Tolerance of Color Rendering Index: ±2
Tolerance of Luminous flux: ±11%.

Device Selection Guide

Chip Materials	Emitted Color	Resin Color
	Cool White	
InGaN	Neutral White	Water Clear
	Warm White	

Absolute Maximum Ratings (T_{Soldering}=25°C)

Parameter	Symbol	Rating	Unit		
Forward Current	I _F	180	mA		
Peak Forward Current (Duty 1/10 @10ms)	I _{FP}	300	mA		
Power Dissipation	P _d	612	mW		
Operating Temperature	T _{opr}	-40 ~ +85	C°		
Storage Temperature	T _{stg}	-40 ~ +100	C°		
Thermal Resistance (Junction / Soldering point)	R _{th J-S}	21	°C/W		
Junction Temperature	Τ _j	115	C°		
Soldering Temperature	T _{sol}	Reflow Soldering : 2	60 °C for 10 sec.		
	• SOI	Hand Soldering : 3	Hand Soldering : 350 °C for 3 sec.		

Note:

The products are sensitive to static electricity and must be carefully taken when handling products

Electro-Optical Characteristics ($T_{Soldering}=25^{\circ}C$)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Luminous Flux ₍₁₎	Φ	65			lm	I _F =150mA
Forward Voltage(2)	V _F			3.4	V	I _F =150mA
Color Rendering Index ₍₃₎	Ra	80				I _F =150mA
Viewing Angle	20 _{1/2}		120		deg	I _F =150mA
Reverse Current	IR			50		V _R =5V

Notes:

1. Tolerance of Luminous flux: ±11%.

2. Tolerance of Forward Voltage: ±0.1V.

3. Tolerance of Color Rendering Index: ±2

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Bin Range of Luminous Flux

Bin Code	Min.	Max.	Unit	Condition
R4	65	70		
R5	70	76	Im	1 150 - 1
R6	76	83	Im	I _F =150mA
R7	83	90		

Note:

Tolerance of Luminous flux: ±11%.

Bin Range of Forward Voltage

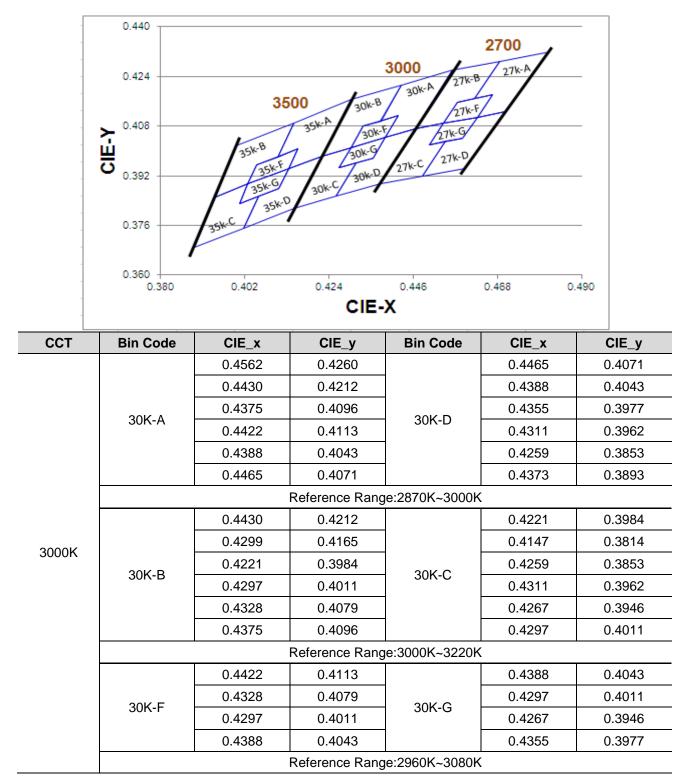
Group	Bin Code	Min.	Max.	Unit	Condition
	35	2.8	2.9		
	36	2.9	3.0		
2024	37	37 3.0 3	3.1		1 450 1
2834	38	3.1	3.2	V	I _F =150mA
	39	3.2	3.3		
	40	3.3	3.4		

Note:

Tolerance of Forward Voltage: ±0.1V.

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The C.I.E. 1931 Chromaticity Diagram

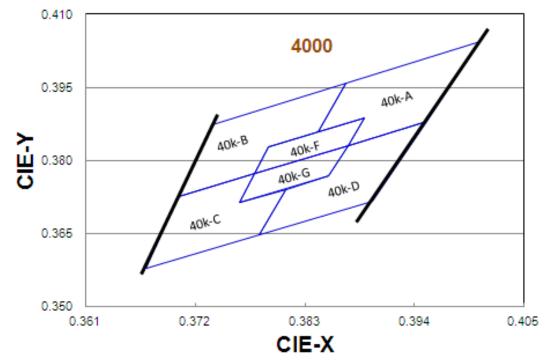


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ССТ	Bin Code	CIE_x	CIE_y	Bin Code	CIE_x	CIE_y
		0.4299	0.4165		0.4221	0.3984
		0.4148	0.4090		0.4134	0.3943
	35K-A	0.4106	0.3981	35K-D	0.4108	0.3878
	30K-A	0.4159	0.4007	33K-D	0.4057	0.3853
		0.4134	0.3943		0.4018	0.3752
		0.4221	0.3984		0.4147	0.3814
		R	eference Range:	3220K~3500K		
		0.4148	0.4090	35K-C	0.3943	0.3853
3500K	35K-B	0.3996	0.4015		0.3889	0.3690
3300K		0.3943	0.3853		0.4018	0.3752
	33K-D	0.4029	0.3893		0.4057	0.3853
		0.4051	0.3954		0.4006	0.3829
		0.4106	0.3981		0.4029	0.3893
		R	eference Range:	3500K~3710K		
		0.4159	0.4007		0.4134	0.3943
	25K E	0.4051	0.3954		0.4029	0.3893
	35K-F	0.4029	0.3893	35K-G	0.4006	0.3829
		0.4134	0.3943		0.4108	0.3878
		R	eference Range:	3360K~3540K		

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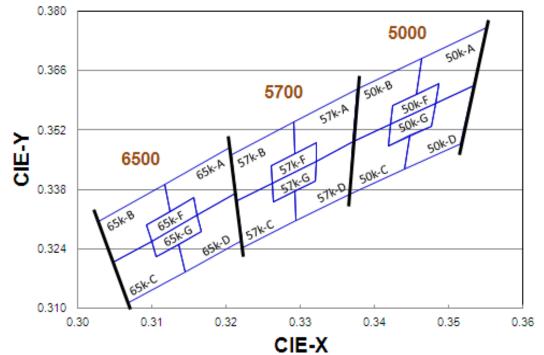
The C.I.E. 1931 Chromaticity Diagram



Bin Range of Chromaticity Coordinates

ССТ	Bin Code	CIE_x	CIE_y	Bin Code	CIE_x	CIE_y
		0.4006	0.4044		0.3952	0.3880
		0.3871	0.3959	40K-D	0.3873	0.3831
		0.3843	0.3858		0.3854	0.3768
	40K-A	0.3890	0.3887	40K-D	0.3810	0.3741
		0.3873	0.3831		0.3784	0.3647
		0.3952	0.3880		0.3898	0.3716
		F	Reference Range:	3700K~3970K		
		0.3871	0.3959	40K-C	0.3703	0.3726
4000K	40K-B	0.3736	0.3874		0.3670	0.3578
4000K		0.3703	0.3726		0.3784	0.3647
		0.3779	0.3773		0.3810	0.3741
		0.3793	0.3828		0.3764	0.3713
		0.3843	0.3858		0.3779	0.3773
		F	Reference Range:	3970K~4270K		
		0.3890	0.3887		0.3873	0.3831
	40K-F	0.3793	0.3828	40K-G	0.3779	0.3773
	4UN-F	0.3779	0.3773	401-9	0.3764	0.3713
		0.3873	0.3831		0.3854	0.3768
		F	Reference Range:	3870K~4080K		

The C.I.E. 1931 Chromaticity Diagram



Bin Range of Chromaticity Coordinates

ССТ	Bin Code	CIE_x	CIE_y	Bin Code	CIE_x	CIE_y	
		0.3551	0.3760		0.3533	0.3624	
		0.3464	0.3688		0.3482	0.3583	
		0.3456	0.3604	50K-D	0.3477	0.3530	
	50K-A	0.3487	0.3629	- JUK-D	0.3448	0.3507	
		0.3482	0.3583		0.3441	0.3428	
		0.3533	0.3624		0.3515	0.3487	
			Reference Rang	je:4745K~5000K		·	
		0.3464	0.3688	50K-C	0.3371	0.3493	
50001/	50K-B	0.3376	0.3616		0.3366	0.3369	
5000K		0.3371	0.3493		0.3441	0.3428	
		0.3422	0.3533		0.3448	0.3507	
		0.3425	0.3579		0.3418	0.3483	
		0.3456	0.3604		0.3422	0.3533	
			Reference Rang	je:5000K~5310K		·	
		0.3487	0.3629		0.3482	0.3583	
		0.3425	0.3579	FOK O	0.3422	0.3533	
	50K-F	0.3422	0.3533	50K-G	0.3418	0.3483	
		0.3482	0.3583		0.3477	0.3530	
	Reference Range:4910K~5120K						

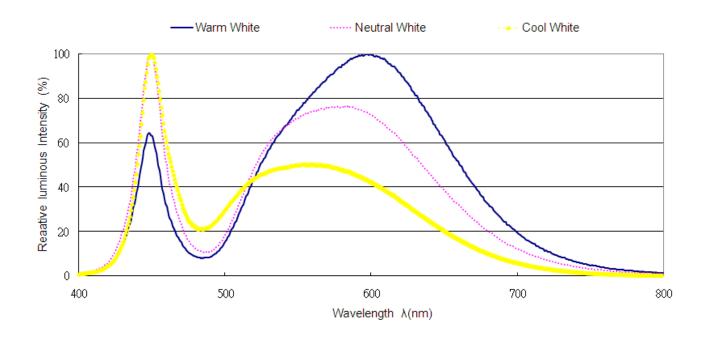
Notes:

1. The value is based on driving current by 150mA.

2. Tolerance of Chromaticity Coordinates: ±0.01.

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Spectrum Distribution



Typical Electro-Optical Characteristics Curves

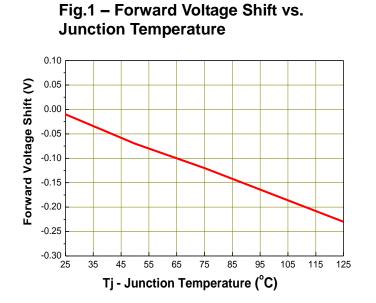
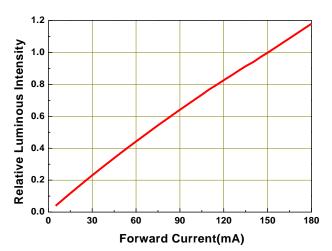


Fig.2 - Relative Luminous Intensity vs. Forward Current



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Typical Electro-Optical Characteristics Curves



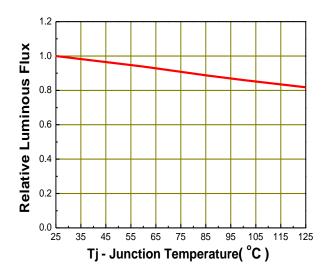


Fig.5 – Max. Driving Forward Current vs. Soldering Temperature

Rth j-s=21 °C/W



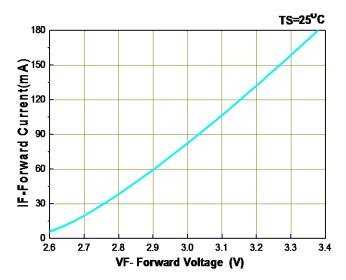
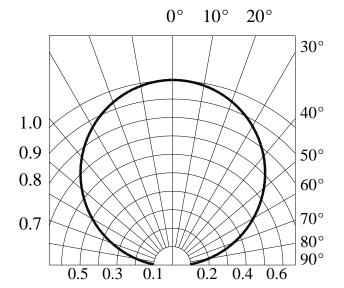


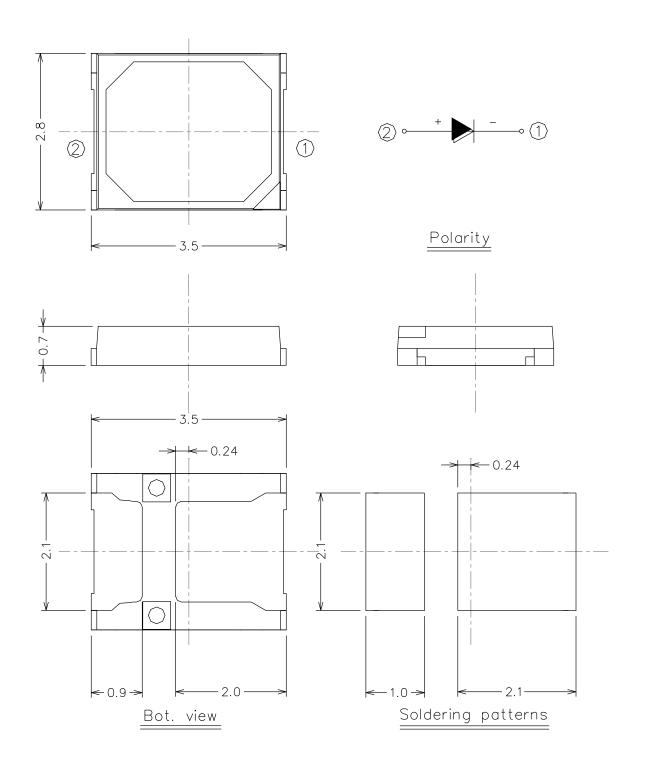
Fig.6 – Radiation Diagram





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Package Dimension



Note: Tolerance unless mentioned is ± 0.15 mm; Unit = mm

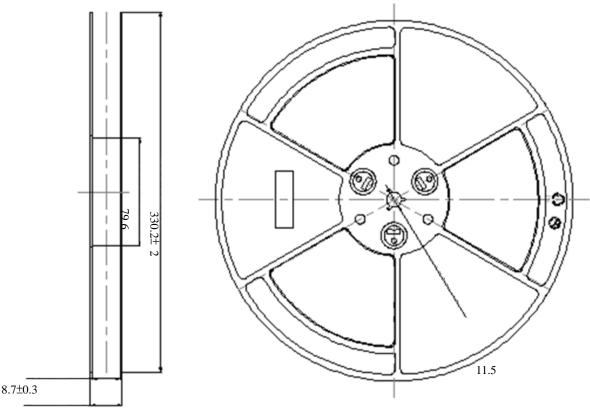
Moisture Resistant Packing Materials

Label Explanation



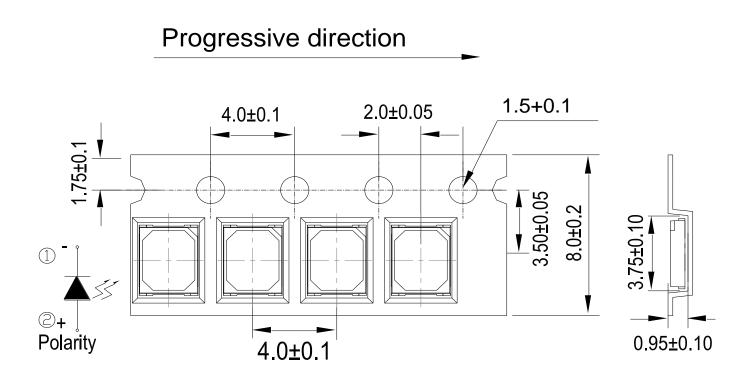
- CPN: Customer's Product Number
- P/N: Product Number
- QTY: Packing Quantity
- CAT: Luminous Intensity Rank
- HUE: Dom. Wavelength Rank
- REF: Forward Voltage Rank
- LOT No: Lot Number

Reel Dimensions



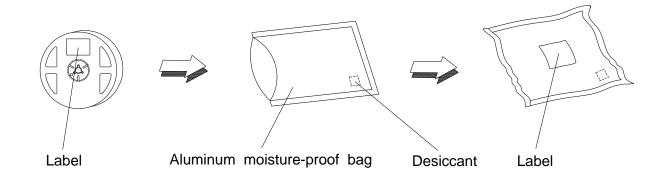
Note: Tolerances unless mentioned ± 0.1 mm. Unit = mm

Carrier Tape Dimensions: Loaded Quantity 16000 pcs Per Reel



Note: Tolerance unless mentioned is ±0.1mm; Unit = mm

Moisture Resistant Packing Process



Reliability Test Items and Conditions

The reliability of products shall be satisfied with items listed below. Confidence level : 90% LTPD : 10%

No.	Items	Test Condition	Test Hours/Cycles	Sample Size	Ac/Re
1	Reflow Soldering	Temp. : 260°C /10sec.	6 Min.	22 PCS.	0/1
2	Thermal Shock	H : +100°C 20min ∫ 10 sec L : -10°C 20min	200 Cycles	22 PCS.	0/1
3	Temperature Cycle	H : +100°C 30min	200 Cycles	22 PCS.	0/1
4	High Temperature/Humid ity Reverse Bias	Ta=85°C,85%RH	1000 Hrs.	22 PCS.	0/1
5	High Temperature/Humid ity Operation	Ta=85°C,85%RH, I _F = 100 mA	1000 Hrs.	22 PCS.	0/1
6	Low Temperature Storage	Ta=-40°C	1000 Hrs.	22 PCS.	0/1
7	High Temperature Storage	Ta=85°C	1000 Hrs.	22 PCS.	0/1
8	Low Temperature Operation Life	Ta=-40°C, I _F = 180 mA	1000 Hrs.	22 PCS.	0/1
9	High Temperature Operation/ Life#1	Ta=25°C, I _F = 180 mA	1000 Hrs.	22 PCS.	0/1
10	High Temperature Operation/ Life#2	Ta=55°C, I _F =180 mA	1000 Hrs.	22 PCS.	0/1
11	High Temperature Operation/ Life#3	Ta=85°C, I _F = 100 mA	1000 Hrs.	22 PCS.	0/1

Precautions for Use

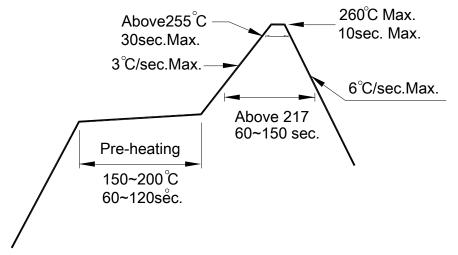
1. Over-current-proof

Customer must apply resistors for protection; otherwise slight voltage shift will cause big current change (Burn out will happen).

- 2. Storage
 - 2.1 Do not open moisture proof bag before the products are ready to use.
 - 2.2 Before opening the package: The LEDs should be kept at 30°C or less and 90%RH or less.
 - 2.3 After opening the package: The LED's floor life is 168 Hrs under 30°C or less and 60% RH or less. If unused LEDs remain, it should be stored in moisture proof packages.
 - 2.4 If the moisture absorbent material (silica gel) has faded away or the LEDs have exceeded the storage time, baking treatment should be performed using the following conditions.

Baking treatment: 60±5°C for 24 hours.

- 3. Soldering Condition
 - 3.1 Pb-free solder temperature profile



3.2 Reflow soldering should not be done more than two times.

3.3 When soldering, do not put stress on the LEDs during heating.

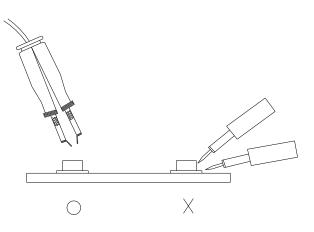
3.4 After soldering, do not warp the circuit board.

4. Soldering Iron

Each terminal is to go to the tip of soldering iron temperature less than 350°C for 3 seconds within once in less than the soldering iron capacity 25W. Leave two seconds and more intervals, and do soldering of each terminal. Be careful because the damage of the product is often started at the time of the hand solder.

5. Repairing

Repair should not be done after the LEDs have been soldered. When repairing is unavoidable, a double-head soldering iron should be used (as below figure). It should be confirmed beforehand whether the characteristics of the LEDs will or will not be damaged by repairing.



DISCLAIMER

1. EVERLIGHT reserves the right(s) on the adjustment of product material mix for the specification.

2. The product meets EVERLIGHT published specification for a period of twelve (12) months from date of shipment.

3. The graphs shown in this datasheet are representing typical data only and do not show guaranteed values.

4. When using this product, please observe the absolute maximum ratings and the instructions for using outlined in these specification sheets. EVERLIGHT assumes no responsibility for any damage resulting from the use of the product which does not comply with the absolute maximum ratings and the instructions included in these specification sheets.

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6. This product is not intended to be used for military, aircraft, automotive, medical,