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# SILICON POWER TRANSISTOR 2SA1744

## PNP SILICON EPITAXIAL TRANSISTOR FOR HIGH-SPEED SWITCHING

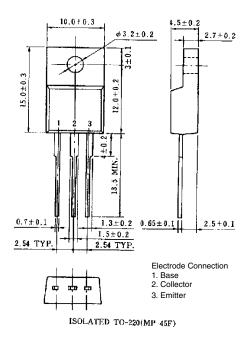
The 2SA1744 is a power transistor developed for high-speed switching and features a high here at Low VcE(sat). This transistor is ideal for use as a driver in DC/DC converters and actuators.

In addition, a small resin-molded insulation type package contributes to high-density mounting and reduction of mounting cost.

#### **FEATURES**

- High hre and low VcE(sat): hre  $\geq$  100 (VcE = -2 V, Ic = -3 A)  $VcE(sat) \leq 0.3$  V (Ic = -8 A, IB = -0.4 A)
- Full-mold package that does not require an insulating board or bushing

#### PACKAGE DRAWING (UNIT: mm)



#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

| Parameter                    | Symbol             | Ratings     | Unit |
|------------------------------|--------------------|-------------|------|
| Collector to base voltage    | Vcво               | -100        | ٧    |
| Collector to emitter voltage | VCEO               | -60         | ٧    |
| Emitter to base voltage      | VEBO               | -7.0        | ٧    |
| Collector current (DC)       | Ic(DC)             | -15         | Α    |
| Collector current (pulse)    | IC(pulse)*         | -30         | Α    |
| Base current (DC)            | I <sub>B(DC)</sub> | -7.5        | Α    |
| Total power dissipation      | P⊤ (Tc = 25°C)     | 30          | W    |
| Total power dissipation      | P⊤ (Ta = 25°C)     | 2.0         | W    |
| Junction temperature         | Tj                 | 150         | °C   |
| Storage temperature          | T <sub>stg</sub>   | -55 to +150 | °C   |

<sup>\*</sup> PW  $\leq$  300  $\mu$ s, duty cycle  $\leq$  10%

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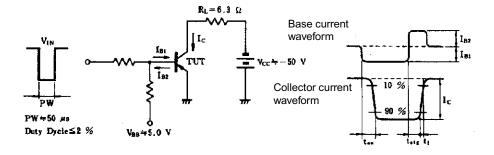
| Parameter                    | Symbol                  | Conditions  | MIN. | TYP. | MAX. | Unit |
|------------------------------|-------------------------|---|------|------|------|------|
| Collector to emitter voltage | VCEO(SUS)               | Ic = -8.0 A, I <sub>B</sub> = -0.8 A, L = 1 mH  | -60  |      |      | V    |
| Collector to emitter voltage | VCEX(SUS)               | $Ic = -8.0 \text{ A}, I_{B1} = -I_{B2} = -0.8 \text{ A},$ $V_{BE(OFF)} = 1.5 \text{ V}, L = 180 \ \mu\text{H}, clamped$ |      |      |      | V    |
| Collector cutoff current     | Ісво                    | Vcb = -60 V, IE = 0   |      |      | -10  | μΑ   |
| Collector cutoff current     | ICER                    | $V_{CE} = -60 \text{ V}, \text{ Rbe} = 50 \Omega, \text{ Ta} = 125^{\circ}\text{C}$                                     |      |      | -1.0 | mA   |
| Collector cutoff current     | ICEX1                   | Vce = -60 V, Vbe(OFF) = 1.5 V   |      |      | -10  | μΑ   |
| Collector cutoff current     | ICEX2                   | Vce = -60 V, Vbe(OFF) = 1.5 V,<br>Ta = 125°C  |      |      | -1.0 | mA   |
| Emitter cutoff current       | ІЕВО                    | V <sub>EB</sub> = -5.0 V, I <sub>C</sub> = 0  |      |      | -10  | μΑ   |
| DC current gain              | h <sub>FE1</sub> *      | Vce = -2.0 V, Ic = -1.5 A   | 100  |      |      |      |
| DC current gain              | hFE2*                   | Vce = -2.0 V, Ic = -3.0 A   | 100  |      | 400  |      |
| DC current gain              | h <sub>FE3</sub> *      | Vce = -2.0 V, Ic = -8.0 A   | 60   |      |      |      |
| Collector saturation voltage | VCE(sat)1*              | Ic = -8.0  A, IB = -0.4  A  |      |      | -0.3 | V    |
| Collector saturation voltage | VCE(sat)2*              | Ic = -12 A, I <sub>B</sub> = -0.6 A   |      |      | -0.5 | V    |
| Base saturation voltage      | V <sub>BE(sat)1</sub> * | Ic = -8.0  A, IB = -0.4  A  |      |      | -1.2 | V    |
| Base saturation voltage      | V <sub>BE(sat)2</sub> * | Ic = -12 A, I <sub>B</sub> = -0.6 A   |      |      | -1.5 | V    |
| Collector capacitance        | Сор                     | $V_{CB} = -10 \text{ V}, I_E = 0, f = 1.0 \text{ MHz}$  |      | 300  |      | pF   |
| Gain bandwidth product       | f⊤                      | Vce = -10 V, Ic = -1.5 A  |      | 80   |      | MHz  |
| Turn-on time                 | ton                     | Ic = $-8.0$ A, R <sub>L</sub> = $6.3$ Ω,  |      |      | 0.3  | μs   |
| Storage time                 | tstg                    | $I_{B1} = -I_{B2} = -0.4 \text{ A}, \text{ Vcc } \cong -50 \text{ V}$ Refer to the test circuit.                        |      |      | 1.5  | μs   |
| Fall time                    | tf                      | nerer to the test circuit.  |      |      | 0.3  | μs   |

<sup>\*</sup> Pulse test PW  $\leq$  350  $\mu$ s, duty cycle  $\leq$  2%

#### **hfe CLASSIFICATION**

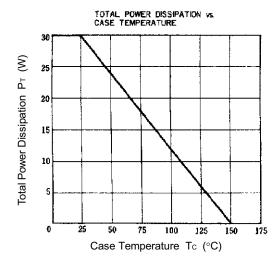
| Marking          | М          | L          | K          |
|------------------|------------|------------|------------|
| h <sub>FE2</sub> | 100 to 200 | 150 to 300 | 200 to 400 |

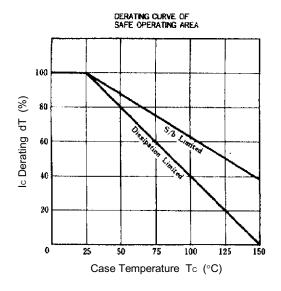
#### SWITCHING TIME (ton, tstg, tf) TEST CIRCUIT

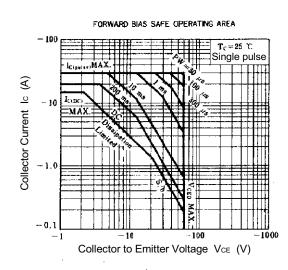


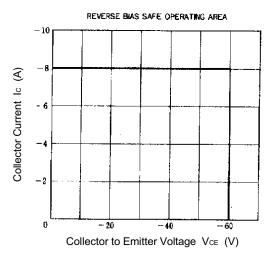


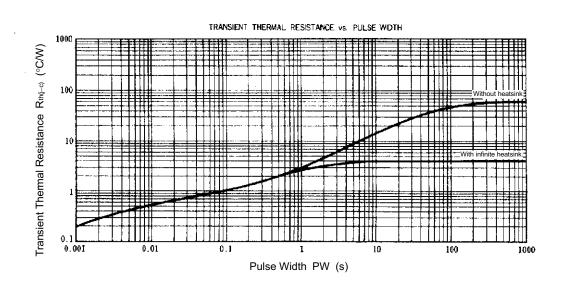
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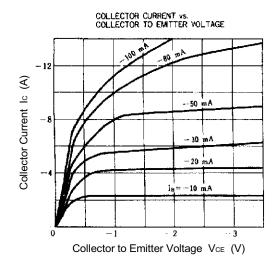


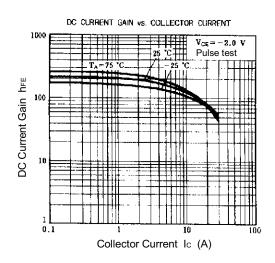


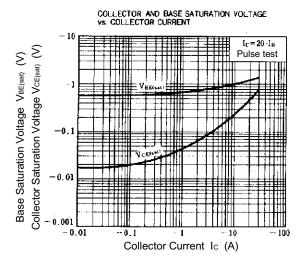


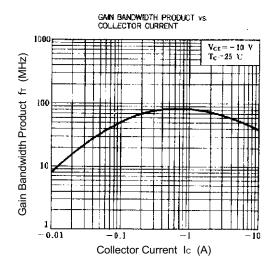


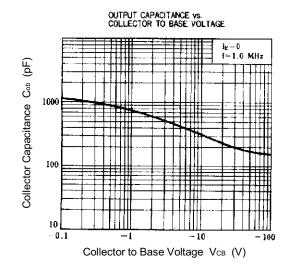
3

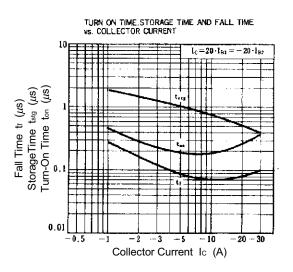














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