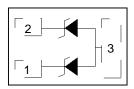




Description

The PESDUC3FD5VU protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



Top View

Feature

- DFN1006-3L package
- Unidirectional configurations
- Response time is typically < 1ns</p>
- Low clamping voltage
- Transient protection for data lines to IEC61000-4-2(ESD) ±15KV(air), ±8KV(contact); IEC61000-4-4 (EFT) 40A (5/50ns)

Applications

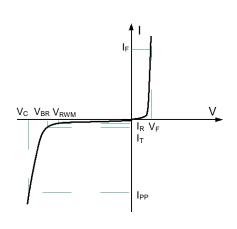
- Cell phone
- PMP
- ➤ MID
- PDA
- Digital camera
- Other electronics equipments

Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- ➤ Qualified max reflow temperature:260°C
- Device meets MSL 2 requirements
- Pure tin plating: 7 ~ 17 um
- ➤ Pin flatness:≤3mil

Electronics Parameter

Symbol	Parameter	
V_{RWM}	Peak Reverse Working Voltage	
I _R	Reverse Leakage Current @ V _{RWM}	
V_{BR}	Breakdown Voltage @ I _T	
I _T	Test Current	
I _{PP}	Maximum Reverse Peak Pulse Current	
V _C	Clamping Voltage @ I _{PP}	
P _{PP}	Peak Pulse Power	
CJ	Junction Capacitance	
I _F	Forward Current	
V _F	Forward Voltage @ I _F	



Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t _p =8/20µs)	P _{pp}	75	W
Lead Soldering Temperature	TL	260(10 sec)	$^{\circ}\! C$
Operating Temperature	TJ	-55 to +125	$^{\circ}$ C
Storage Temperature	T _{STG}	-55 to +150	$^{\circ}$

Electrical characteristics per line@25℃ (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V_{RWM}				5	V
Breakdown Voltage	V_{BR}	I _t =1mA	5.4	7.0	8.5	V
Reverse Leakage Current	I _R	V _{RWM} =5V			1	μΑ
Forward Voltage	V _F	I _F =10mA		0.8	1.25	V
Clamping Voltage	Vc	I _{PP} =1A t _P = 8/20μS		8	9.2	V
Clamping Voltage	V _C	$I_{PP}=5.5A$ $t_P = 8/20 \mu S$		13	15	V
Junction Capacitance	C _j	V _R =0V f = 1MHz		0.5	0.8	pF

Typical Characteristics



Fig 1.Pulse Waveform

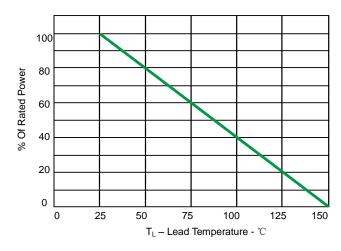


Fig 2.Power Derating Curve

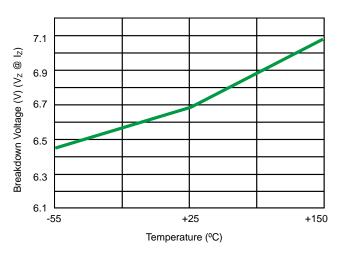


Fig 3.Typical Breakdown Voltage vs. Temperature

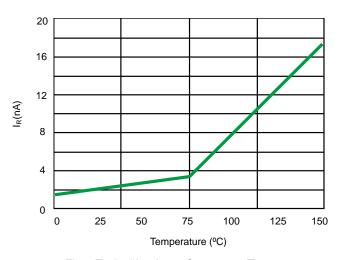


Fig 4. Typical Leakage Current vs. Temperature

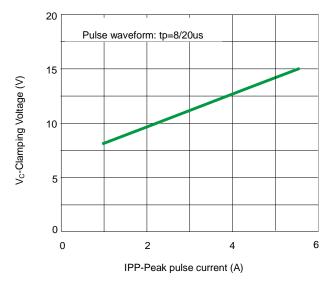


Fig 5. Clamping voltage vs. Peak pulse current

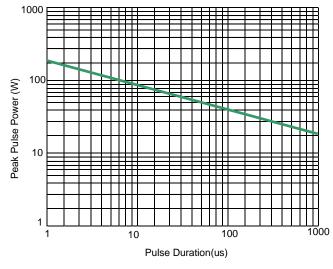
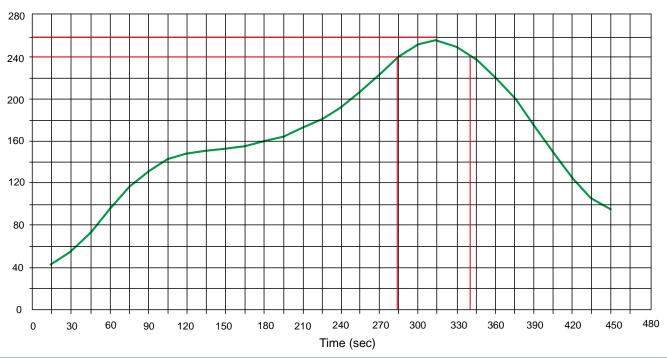


Fig 6. Non-Repetitive Peak Pulse Power vs. Pulse time

Solder Reflow Recommendation

Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec

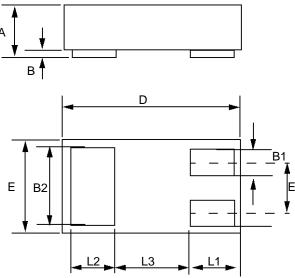


PCB Design

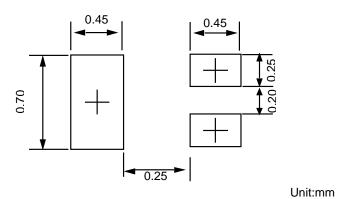
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- > Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

Product dimension (DFN1006-3L)



Dim	Millimeters			
Dilli	MIN	Тур	MAX	
Α	0.33	0.36	0.50	
В	0.0		0.05	
B1	0.10	0.15	0.20	
B2	0.45	0.50	0.55	
D	0.85	1	1.15	
Е	0.45	0.60	0.75	
е		0.35		
L1	0.20	0.25	0.30	
L2	0.21	0.26	0.31	
L3		0.39		



Ordering information

Device	Package	Shipping
PESDUC3FD5VU	DFN1006-3L (Pb-Free)	10000 / Tape & Reel

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