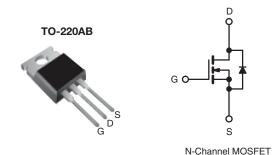


Power MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	600	600			
$R_{DS(on)}(\Omega)$	V _{GS} = 10 V	1.2			
Q _g (Max.) (nC)	60	60			
Q _{gs} (nC)	8.3	8.3			
Q _{gd} (nC)	30	30			
Configuration	Sing	Single			



FEATURES

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- Fast Switching
- · Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC



DESCRIPTION

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION		
Package	TO-220AB	
Lead (Pb)-free	IRFBC40PbF	
Lead (PD)-life	SiHFBC40-E3	
SnPb	IRFBC40	
SILD	SiHFBC40	

ABSOLUTE MAXIMUM RATINGS (T_C	= 25 °C, unl	ess otherwis	se noted)			
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V_{DS}	600	.,	
Gate-Source Voltage			V_{GS}	± 20	V	
Continuous Drain Current	V _{GS} at 10 V	$T_{\rm C} = 25 ^{\circ}{\rm C}$ $T_{\rm C} = 100 ^{\circ}{\rm C}$		6.2		
Continuous Drain Current		T _C = 100 °C	I _D	3.9	Α	
Pulsed Drain Current ^a			I _{DM}	25		
Linear Derating Factor				1.0	W/°C	
Single Pulse Avalanche Energy ^b			E _{AS}	570	mJ	
Repetitive Avalanche Current ^a			I _{AR}	6.2	А	
Repetitive Avalanche Energy ^a			E _{AR}	13	mJ	
Maximum Power Dissipation $T_C = 25 ^{\circ}C$			P_{D}	125	W	
Peak Diode Recovery dV/dt ^c			dV/dt	3.0	V/ns	
Operating Junction and Storage Temperature Range			T _J , T _{stg}	- 55 to + 150	°C	
Soldering Recommendations (Peak Temperature) for 10 s				300 ^d		
Mauring Tayous	6.00 or 1	0.00 - 140		10	lbf ⋅ in	
Mounting Torque	6-32 or M3 screw			1.1	N⋅m	

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. V_{DD} = 50 V, starting T_J = 25 °C, L = 27 mH, R_g = 25 Ω , I_{AS} = 6.2 A (see fig. 12).
- c. $I_{SD} \le 6.2$ A, $dI/dt \le 80$ A/ μ s, $V_{DD} \le V_{DS}$, $T_J \le 150$ °C.
- d. 1.6 mm from case.

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply



THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R _{thJA}	-	62	
Case-to-Sink, Flat, Greased Surface	R _{thCS}	0.50	-	°C/W
Maximum Junction-to-Case (Drain)	R_{thJC}	-	1.0	

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							,
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0$	0 V, I _D = 250 μA	600	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	to 25 °C, I _D = 1 mA	-	0.7	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = \	/ _{GS} , I _D = 250 μA	2.0	-	4.0	V
Gate-Source Leakage	I _{GSS}	Vo	_{SS} = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}		000 V, V _{GS} = 0 V V _{GS} = 0 V, T _J = 125 °C	-	-	100 500	μA
Drain-Source On-State Resistance	R _{DS(on)}	$V_{DS} = 480 \text{ V},$ $V_{GS} = 10 \text{ V}$	$I_D = 3.7A^b$		_	1.2	Ω
Forward Transconductance	9fs		00 V, I _D = 3.7 A ^b	4.7	_	-	S
Dynamic	91S	VDS = 1	00 1, 10 = 0.7 71				<u> </u>
Input Capacitance	C _{iss}	\	$I_{GS} = 0 \text{ V},$	_	1300	_	
Output Capacitance	C _{oss}	V	ns = 25 V.	_	160	-	pF
Reverse Transfer Capacitance	C _{rss}	f = 1.0	MHz, see fig. 5	-	30	_	
Total Gate Charge	Qg				-	60	
Gate-Source Charge	Q _{gs}	V _{GS} = 10 V	$I_D = 6.2 \text{ A}, V_{DS} = 360 \text{ V},$	-	-	8.3	nC
Gate-Drain Charge	Q_{gd}		see fig. 6 and 13 ^b	-	-	30	
Turn-On Delay Time	t _{d(on)}	$V_{DD}=300~V,~I_D=6.2~A,$ $R_g=9.1~\Omega,~R_D=47~\Omega,~see~fig.~10^b$		-	13	-	ns ns
Rise Time	t _r			-	18	-	
Turn-Off Delay Time	t _{d(off)}			-	55	-	
Fall Time	t _f			-	20	-	
Internal Drain Inductance	L _D	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-	
Internal Source Inductance	L _S			-	7.5	-	nH
Drain-Source Body Diode Characteristic	s						,
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	6.2	
Pulsed Diode Forward Current ^a	I _{SM}				-	25	А
Body Diode Voltage	V_{SD}	T _J = 25 °C, I	_S = 6.2 A, V _{GS} = 0 V ^b	-	-	1.5	V
Body Diode Reverse Recovery Time	t _{rr}	T - 25 °C 1	6.0 A dl/dt = 100 A/::ch	-	450	940	ns
Body Diode Reverse Recovery Charge	Q_{rr}	$T_J = 25 ^{\circ}\text{C}, I_F = 6.2 \text{A}, dI/dt = 100 \text{A/}\mu\text{s}^b$		-	3.8	7.9	μC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated			minated b	V_{S} and	L _D)

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width \leq 300 μ s; duty cycle \leq 2 %.





TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

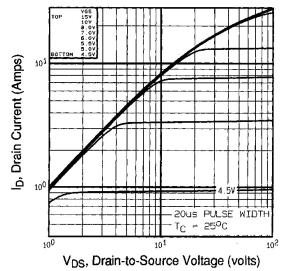


Fig. 1 - Typical Output Characteristics, $T_C = 25$ °C

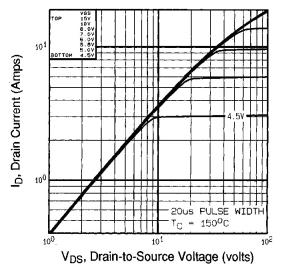
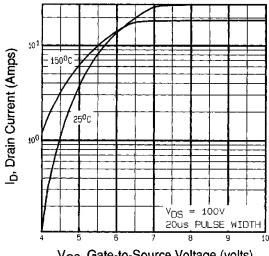


Fig. 2 - Typical Output Characteristics, T_C = 150 °C



 V_{GS} , Gate-to-Source Voltage (volts)

Fig. 3 - Typical Transfer Characteristics

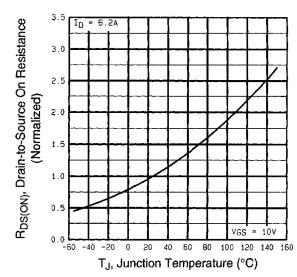


Fig. 4 - Normalized On-Resistance vs. Temperature



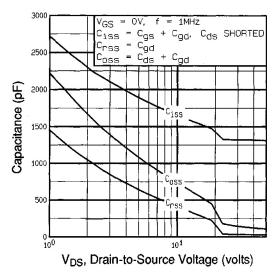


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

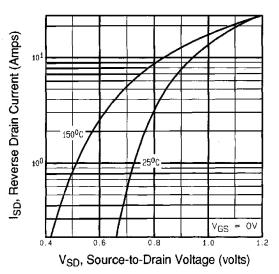


Fig. 7 - Typical Source-Drain Diode Forward Voltage

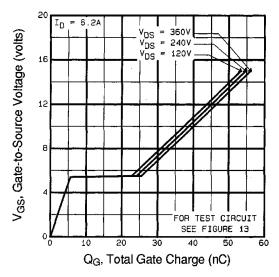


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

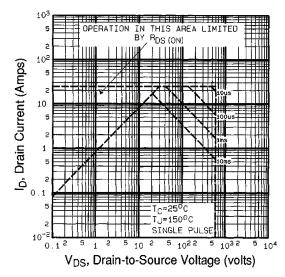


Fig. 8 - Maximum Safe Operating Area





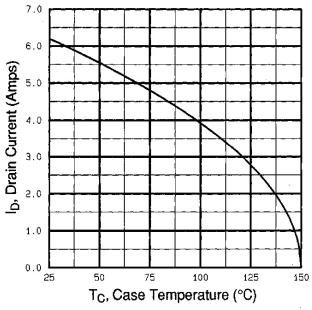


Fig. 9 - Maximum Drain Current vs. Case Temperature

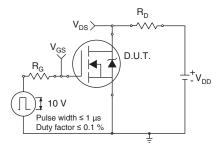


Fig. 10a - Switching Time Test Circuit

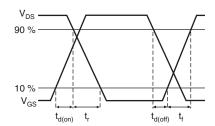


Fig. 10b - Switching Time Waveforms

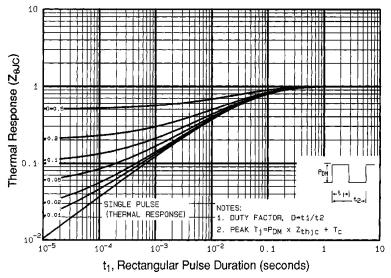


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



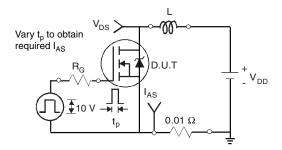


Fig. 12a - Unclamped Inductive Test Circuit

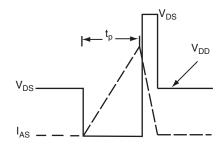


Fig. 12b - Unclamped Inductive Waveforms

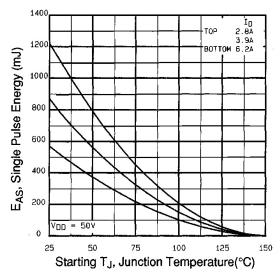


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

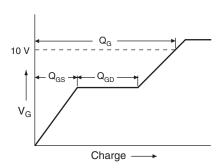


Fig. 13a - Basic Gate Charge Waveform

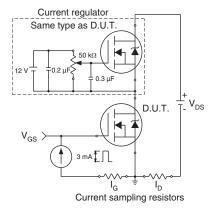
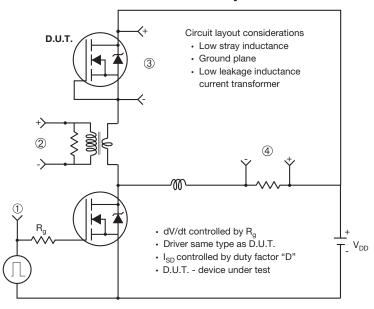


Fig. 13b - Gate Charge Test Circuit



Peak Diode Recovery dV/dt Test Circuit



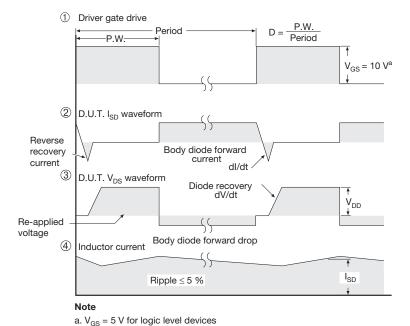


Fig. 14 - For N-Channel

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TO-220AB



	D2

	MILLIMETERS		INCHES	
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØΡ	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: T14-0413-Rev. P, 16-Jun-14 DWG: 5471				

Note

 $^{^{\}star}$ M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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Revision: 02-Oct-12 Document Number: 91000