



## Surface Mount General Purpose Silicon Rectifiers

Reverse Voltage - 50 to 1000 V

Forward Current - 3 A

### FEATURES

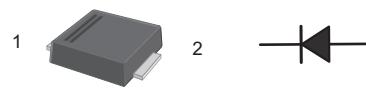
- For surface mounted applications
- Low profile package
- Glass Passivated Chip Junction
- Easy to pick and place
- Lead free in comply with EU RoHS 2011/65/EU directives

### MECHANICAL DATA

- Case: SMBF
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 57mg / 0.002oz

### PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode



Top View

Marking Code : S3AB-S3MB

Simplified outline SMBF and symbol

### Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

Parameter	Symbols	S3ABF	S3BBF	S3DBF	S3GBF	S3JBF	S3KBF	S3MBF	Units
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V <sub>RMS</sub>	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current at T <sub>c</sub> = 115 °C	I <sub>F(AV)</sub>	3							A
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load	I <sub>FSM</sub>	80							A
Maximum Instantaneous Forward Voltage at 3 A	V <sub>F</sub>	1.1							V
Maximum DC Reverse Current T <sub>a</sub> = 25 °C at Rated DC Blocking Voltage T <sub>a</sub> = 125 °C	I <sub>R</sub>	5 150							µA
Typical Junction Capacitance <sup>(1)</sup>	C <sub>j</sub>	35							pF
Typical Thermal Resistance <sup>(2)</sup>	R <sub>θJA</sub> R <sub>θJC</sub>	45 15							°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-55 ~ +150							°C

( 1 ) Measured at 1 MHz and applied reverse voltage of 4 V D.C

( 2 ) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.



Fig.1 Forward Current Derating Curve

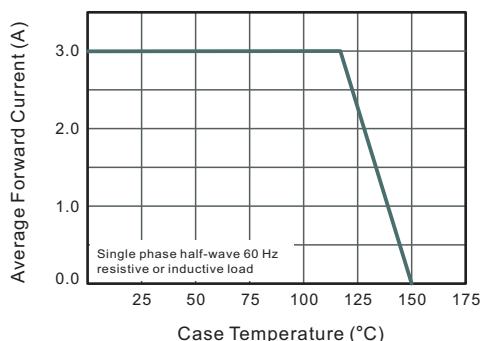


Fig.2 Typical Reverse Characteristics

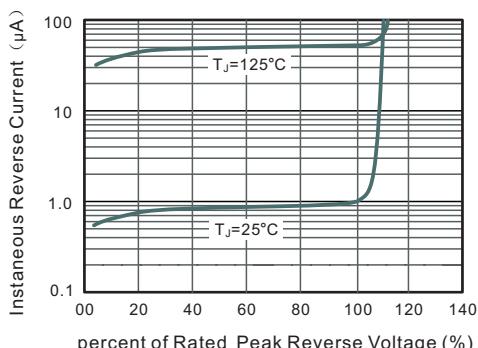


Fig.3 Typical Forward Characteristic

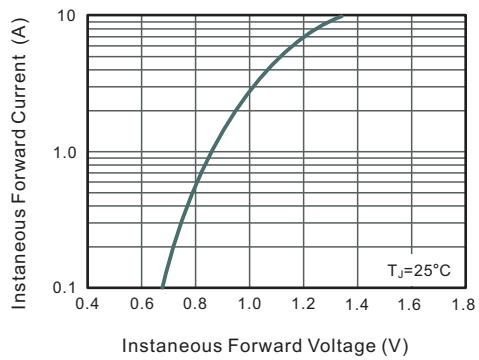


Fig.4 Typical Junction Capacitance

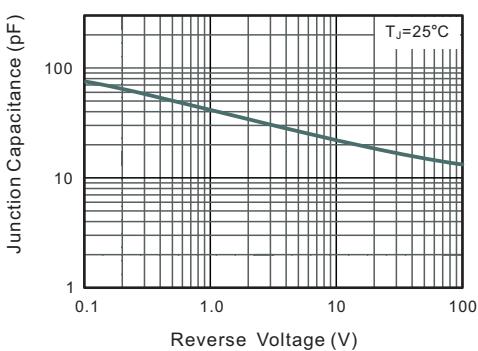
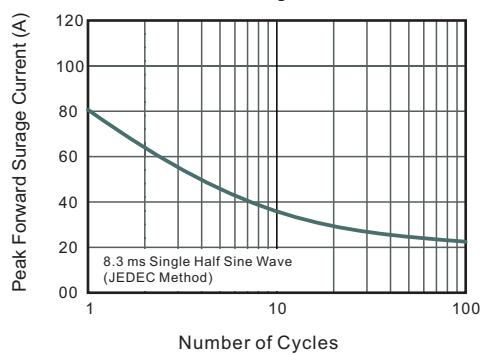


Fig.6 Maximum Non-Repetitive Peak Forward Surge Current

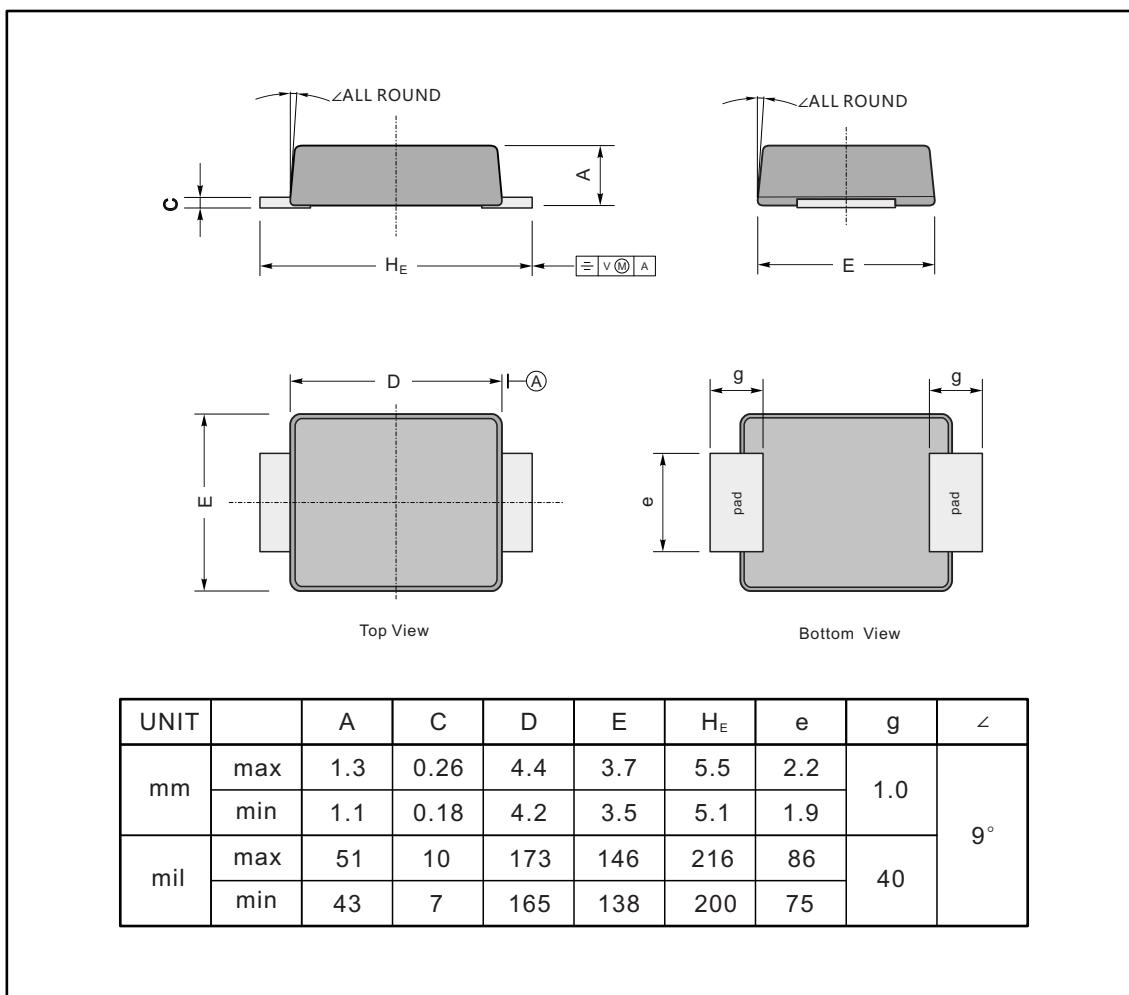




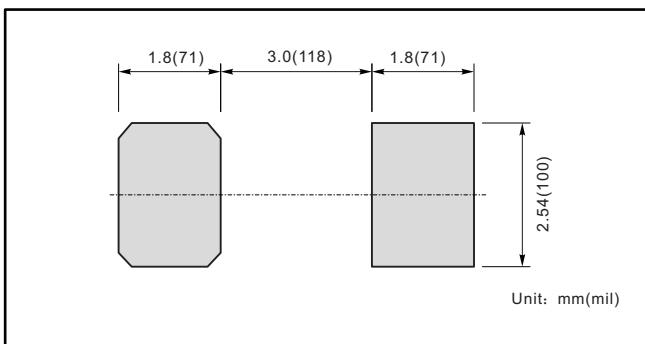
## PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SMBF



### The recommended mounting pad size



### Marking

Type number	Marking code
S3ABF	S3AB
S3BBF	S3BB
S3DBF	S3DB
S3GBF	S3GB
S3JBF	S3JB
S3KBF	S3KB
S3MBF	S3MB