## UNISONIC TECHNOLOGIES CO., LTD

8N60K-MT Power MOSFET

### 8A, 600V N-CHANNEL **POWER MOSFET**

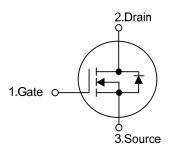
#### **DESCRIPTION**

The UTC 8N60K-MT is a high voltage and high current power MOSFET, designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

#### **FEATURES**

- \*  $R_{DS(ON)}$  < 1.20 @  $V_{GS}$  = 10V,  $I_{D}$  = 4.0A
- \* Fast switching capability
- \* Avalanche energy specified
- \* Improved dv/dt capability, high ruggedness

#### **SYMBOL**

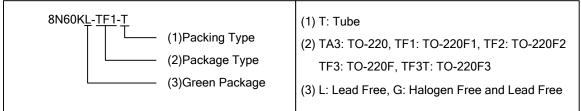


# TO-220 TO-220F TO-220F2 TO-220F1 TO-220F3

#### ORDERING INFORMATION

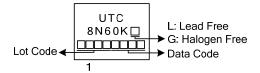
Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
8N60KL-TA3-T	8N60KG-TA3-T	TO-220	G	D	S	Tube	
8N60KL-TF1-T	8N60KG-TF1-T	TO-220F1	G	D	S	Tube	
8N60KL-TF2-T	8N60KG-TF2-T	TO-220F2	G	D	S	Tube	
8N60KL-TF3-T	8N60KG-TF3-T	TO-220F	G	D	S	Tube	
8N60KL-TF3T-T	8N60KG-TF3T-T	TO-220F3	G	D	S	Tube	

Note: Pin Assignment: G: Gate S: Source D: Drain



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#### ■ MARKING



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#### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>C</sub> = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	600	V
Gate-Source Voltage		$V_{GSS}$	±30	V
Drain Current	Continuous	I <sub>D</sub> 8		Α
	Pulsed (Note 2)	I <sub>DM</sub>	32	Α
Avalanche Energy	Single Pulsed (Note 3)	E <sub>AS</sub>	365	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220		147	W
	TO-220F/TO-220F1 TO-220F2/TO-220F3	P <sub>D</sub>	48	W
Junction Temperature		T <sub>J</sub>	+150	°C
Operating Temperature		T <sub>OPR</sub>	-55 ~ +150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating : Pulse width limited by  $T_{\text{J}}$ .
- 3. L = 11.4mH,  $I_{AS}$  = 8A,  $V_{DD}$  = 50V,  $R_G$  = 25  $\Omega$ , Starting  $T_J$  = 25°C.
- 4.  $I_{SD} \le 7.5 A$ , di/dt  $\le 200 A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25 ^{\circ} C$ .

#### **■ THERMAL DATA**

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient		$\theta_{JA}$	62.5	°C/W
	TO-220		0.85	°C/W
Junction to Case	TO-220F/TO-220F1 TO-220F2/TO-220F3	θ <sub>JC</sub>	2.6	°C/W

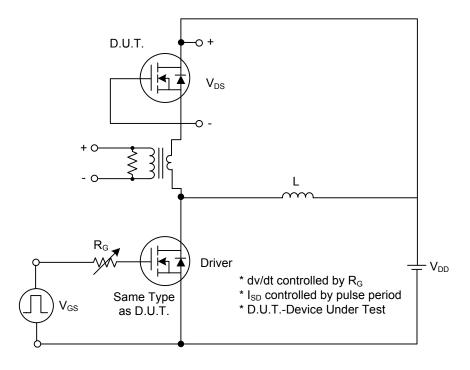
#### ■ **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> =25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	600			V	
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0V			10	μA	
Gate-Source Leakage Current	Forward	Icee	V <sub>GS</sub> = 30 V, V <sub>DS</sub> = 0V			100	nA	
	Reverse		$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{V}$			-100	nA	
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_{J}$	I <sub>D</sub> =250μA, Referenced to 25°C		0.7		V/°C	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V	
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	$V_{GS} = 10V, I_D = 4.0A$			1.2	Ω	
DYNAMIC CHARACTERISTICS								
Input Capacitance	put Capacitance		V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1MHz		1120	1255	pF	
Output Capacitance		C <sub>ISS</sub>			120	135	pF	
Reverse Transfer Capacitance		$C_{RSS}$	1 - 11VIM2		13	16	pF	
SWITCHING CHARACTERISTIC	S							
Total Gate Charge		$Q_G$	\/ -E0\/ \/ -1 0\/		28	36	nC	
Gate-Source Charge		$Q_GS$	V <sub>DS</sub> =50V, V <sub>GS</sub> =1.0V, I <sub>D</sub> =1.3A (Note 1, 2)		8		nC	
Gate-Drain Charge		$Q_GD$	1D=1.3A (Note 1, 2)		6		nC	
Turn-On Delay Time		t <sub>D(ON)</sub>			80	90	ns	
Turn-On Rise Time		$t_R$	$V_{DD} = 30V, I_D = 0.5A,$		89	100	ns	
Turn-Off Delay Time		$t_{D(OFF)}$	R <sub>G</sub> =25Ω (Note 1, 2)		125	160	ns	
Turn-Off Fall Time		$t_{F}$			64	80	ns	
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS								
Drain-Source Diode Forward Volta	age	$V_{SD}$	$V_{GS} = 0V$ , $I_S = 8A$			1.4	V	
Maximum Continuous Drain-Source Diode		I.				8		
Forward Current		I <sub>S</sub>				0	Α	
Maximum Pulsed Drain-Source Diode		I <sub>SM</sub>				32	Α	
Forward Current						52	^	

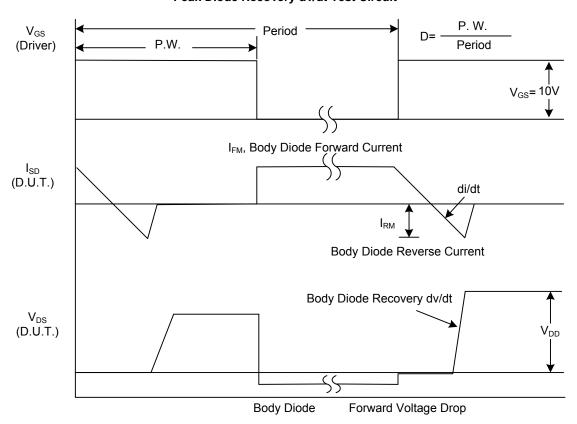
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%.

<sup>2.</sup> Essentially independent of operating temperature.

#### ■ TEST CIRCUITS AND WAVEFORMS



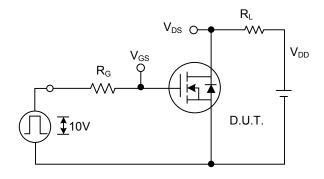
#### Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

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#### ■ TEST CIRCUITS AND WAVEFORMS (Cont.)



V<sub>DS</sub> 90%

V<sub>GS</sub> 10%

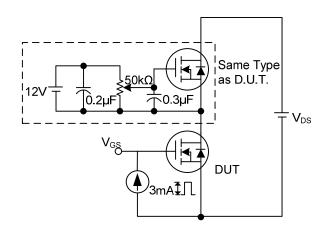
t<sub>D(ON)</sub>

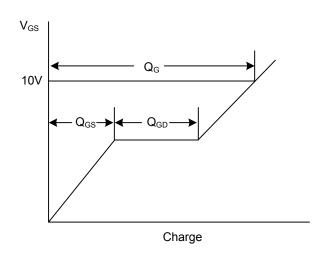
t<sub>R</sub> → |

t<sub>R</sub> + t<sub>R</sub> → |

**Switching Test Circuit** 

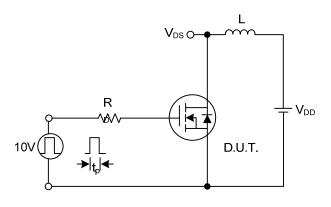
**Switching Waveforms** 

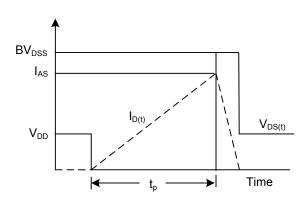




**Gate Charge Test Circuit** 

**Gate Charge Waveform** 





**Unclamped Inductive Switching Test Circuit** 

**Unclamped Inductive Switching Waveforms** 

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**Power MOSFET**