

## **Ultra Low Power Voltage Detector**

#### **FEATURES**

- Ultra-Low Quiescent Current.
- 1.5V to 10.0V Input Voltage Operation.
- Flexible Detection Voltage Setting
   0.1V Step in the Range of 1.6V to 6.0V
- High Detection Voltage Accuracy at ±2.5%.
- Built-In Detection Voltage Hysteresis.
- Three Output Types: N-ch, P-ch and CMOS.
- Space Saving Packages: TO-92, SOT-89, SOT-23 and SOT23-5.

#### **APPLICATIONS**

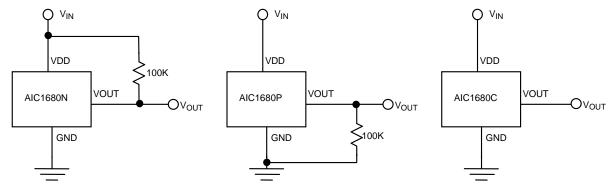
- Battery Checker for Battery-Operated Systems.
- CPU & Logic Circuit Reset.
- · Memory Back-up Circuit.
- · Level Discriminator.
- Power Failure Detector.

#### DESCRIPTION

The AIC1680 is an accurate, ultra-low power consumption voltage detector IC, fabricated with advanced CMOS process. The AIC1680 consists of a comparator, a voltage reference unit, a resistor divider, two output drivers, and a hysteresis circuit.

The consumption current is so low that it can often be ignored when compared to the battery self-discharge current. The values of detection and hysteresis voltage are set internally and are accurately controlled by trimming techniques. There are three types of output: N-ch open-drain, P-ch open-drain, and CMOS. Four types of package, TO-92, SOT-89, SOT-23 and SOT-23-5, are available to save board space.

## **■ TYPICAL APPLICATION CIRCUIT**



Voltage Level Indicator (N-ch Open-Drain Output)

Voltage Level Indicator (P-ch Open-Drain Output)

Voltage Level Indicator (CMOS Output)

**Analog Integrations Corporation** 

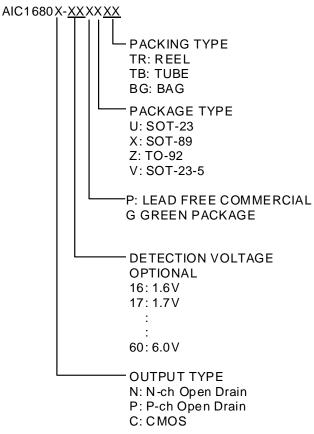
Si-Soft Research Center

DS-1680G-02 20120615

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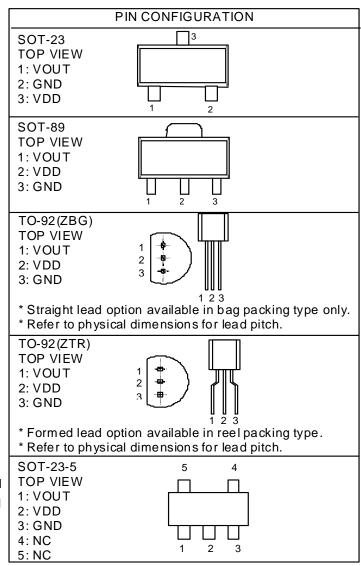


### ■ ORDERING INFORMATION



Example: AIC1680P-33PXTR

→ P-ch open drain 3.3V version, in Lead Free SOT-89 Package & Tape & Reel Packing Type



SOT-23 Marking

Part No.	PU	GU
AIC1680N-16XU	U16NP	U16NG
AIC1680P-16XU	U16PP	U16PG
AIC1680C-16XU	U16CP	U16CG
AIC1680N-17XU	U17NP	U17NG
AIC1680P-17XU	U17PP	U17PG
AIC1680C-17XU	U17CP	U17CG
(0.1V INCREMENT)		

Part No.	PU	GU
AIC1680N-59XU	U59NP	U59NG
AIC1680P-59XU	U59PP	U59PG
AIC1680C-59XU	U59CP	U59CG
AIC1680N-60XU	U60NP	U60NG
AIC1680P-60XU	U60PP	U60PG
AIC1680C-60XU	U60CP	U60CG



SOT-89 Marking

<u> </u>				
Part No.	PX	GX		
AIC1680N-16XX	X16NP	X16NG		
AIC1680P-16XX	X16PP	X16PG		
AIC1680C-16XX	X16CP	X16CG		
AIC1680N-17XX	X17NP	X17NG		
AIC1680P-17XX	X17PP	X17PG		
AIC1680C-17XX	X17CP	X17CG		
(0.1V				
INCREMENT)				

Part No.	PX	GX
AIC1680N-59XX	X59NP	X59NG
AIC1680P-59XX	X59PP	X59PG
AIC1680C-59XX	X59CP	X59CG
AIC1680N-60XX	X60NP	X60NG
AIC1680P-60XX	X60PP	X60PG
AIC1680C-60XX	X60CP	X60CG

● SOT-23-5 Marking

Part No.	PV	GV
AIC1680N-16XV	V16NP	V16NG
AIC1680P-16XV	V16PP	V16PG
AIC1680C-16XV	V16CP	V16CG
AIC1680N-17XV	V17NP	V17NG
AIC1680P-17XV	V17PP	V17PG
AIC1680C-17XV	V17CP	V17CG
(0.1V INCREMENT)		

Part No.	PV	G۷
AIC1680N-59XV	V59NP	V59NG
AIC1680P-59XV	V59PP	V59PG
AIC1680C-59XV	V59CP	V59CG
AIC1680N-60XV	V60NP	V60NG
AIC1680P-60XV	V60PP	V60PG
AIC1680C-60XV	V60CP	V60CG

### ■ ABSOLUTE MAXIMUM RATINGS

Supply Voltage (VDD)	10V
Output Voltage	V <sub>DD</sub> -0.3 to 10V
Output Current	
Operating Temperature Range	
Storage Temperature Range	- 65°C ~ 150°C
Junction Temperature	125°C
Lead Temperature (Soldering 10 sec)	

Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

## TEST CIRCUIT

Refer to "TYPICAL APPLICATION CIRCUIT".



### ■ ELECTRICAL CHARACTERISTICS

(T<sub>A</sub>=25°C, unless otherwise specified.) (Note 1)

PARAMETER	TEST CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Detector Voltage		$V_{DET}$	0.975	1.0	1.025	V <sub>DET</sub>
Detector Threshold Hysteresis		V <sub>HYS</sub>	0.03	0.05	0.07	$V_{DET}$
	V <sub>DD</sub> =2.0V			0.7	1.1	μΑ
	V <sub>DD</sub> =3.0V			0.9	1.5	
Supply Current	V <sub>DD</sub> =4.5V	I <sub>DD</sub>		1.3	2.0	
	V <sub>DD</sub> =6.0V			1.8	2.7	
	V <sub>DD</sub> =10.0V			3.2	4.8	
Operation Voltage		$V_{DD}$	1.5		10	V
	V <sub>DS</sub> =0.5V, V <sub>DD</sub> =2.4V			3.24		mA
Output Command	V <sub>DS</sub> =0.5V, V <sub>DD</sub> =3.6V	(Nlah)		5.85		
Output Current	V <sub>DS</sub> =0.5V, V <sub>DD</sub> =4.6V	I <sub>OUT</sub> (Nch)		7.74		
	V <sub>DS</sub> =0.5V, V <sub>DD</sub> =6.0V			10.44		
Output Current	V <sub>DS</sub> =-2.1V, V <sub>DD</sub> =4.5V	I <sub>OUT</sub> (Pch)	2	3.5		mA
Temperature Coefficience				±150		ppm/°C
Output Delay Time		T <sub>DELAY</sub>			200	μS

Note 1: Specifications are production tested at  $T_A=25^{\circ}C$ . Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with Statistical Quality Controls (SQC).

### ■ TYPICAL PERFORMANCE CHARACTERISTICS

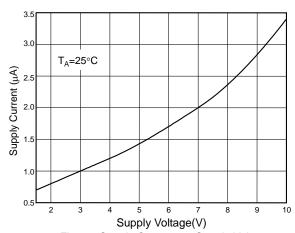
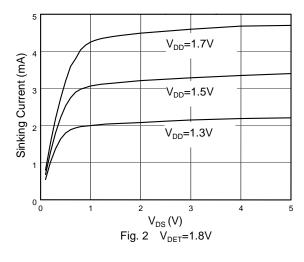


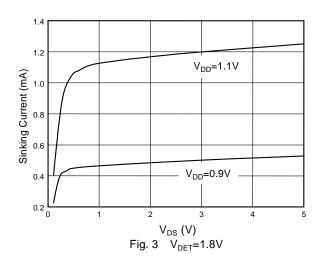
Fig. 1 Supply Current vs. Supply Voltage

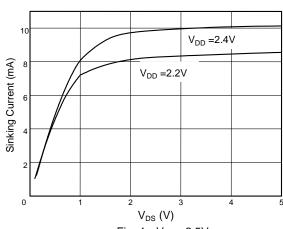


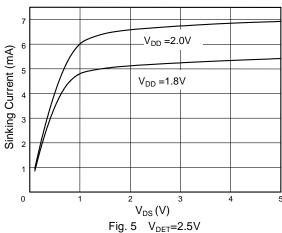
# ■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

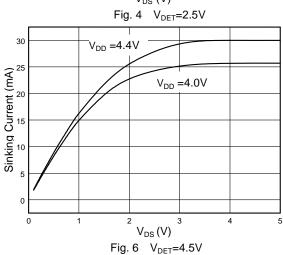
## N-ch Driver Sinking Current vs. V<sub>DS</sub>

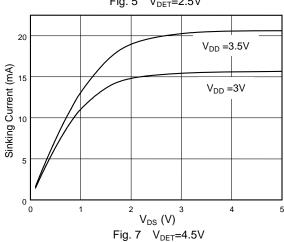








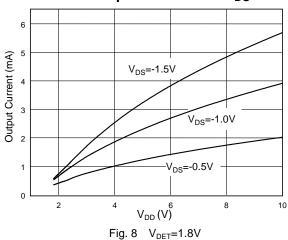


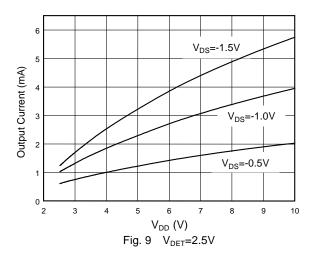


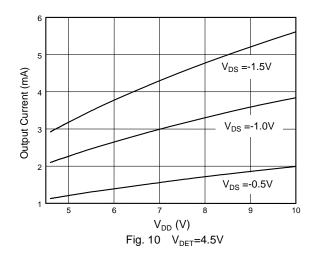


## **■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)**

## P-ch Driver Output Current vs. V<sub>DS</sub>

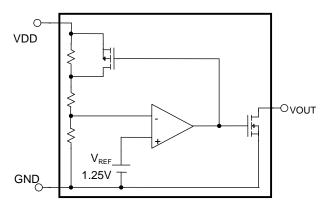




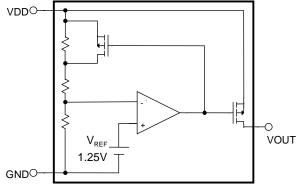




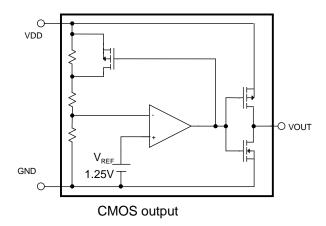
## ■ BLOCK DIAGRAMS



N-ch open drain output



P-ch open drain output



## ■ PIN DESCRIPTIONS

VDD - Power Supply and Detected Voltage Input.

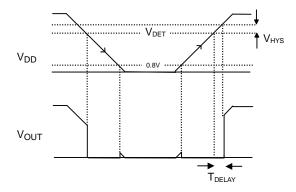
GND - Ground.

VOUT - Detector Output.



#### APPLICATION INFORMATION

The timing diagram of  $V_{DD}$  and  $V_{OUT}$  is shown as below:



The guaranteed minimum operation voltage of AIC1680 is 1.5V. As the supply voltage ( $V_{DD}$ ) is reduced below 1.5V, the performance degrades, N-ch or P-ch driving capacity degrades, and the supply current decreases. AIC1680 isn't functioning when  $V_{DD}$  down to 0.8V.

### APPLICATION EXAMPLE

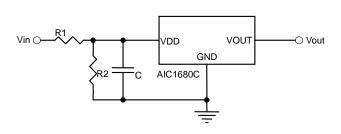


Fig.11 Detector threshold adjustable circuit

$$V_{DET-ADJ} = V_{DET} \frac{R1 + R2}{R2}$$
Hysteresis Voltage =  $V_{HYS} \frac{R1 + R2}{R2}$ 

In some application, divided resisters are used to obtain detector threshold voltage as figure11 shown. If the value of resister, R1, sets extremely large, voltage drop may occur resulting from the start-up current of IC, and detector threshold may oscillation.

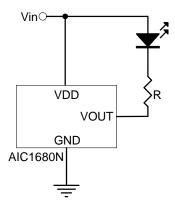


Fig.12 Voltage indicator circuit, lights when low power



## ■ APPLICATION EXAMPLE (Continued)

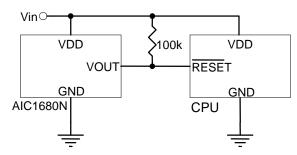


Fig.13 The input voltage of AIC1680 and CPU are the same

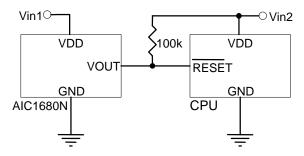


Fig.14 The input voltage of AIC1680 and CPU are not equal

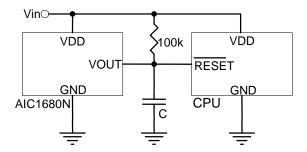


Fig.15 Output delay time-1

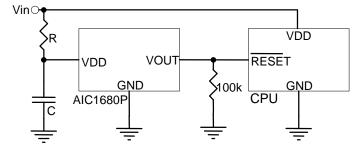
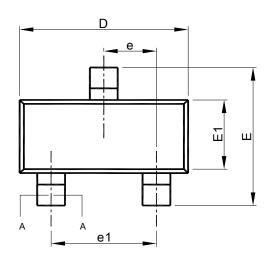


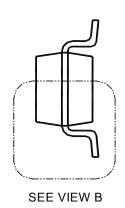
Fig.16 Output delay time-2

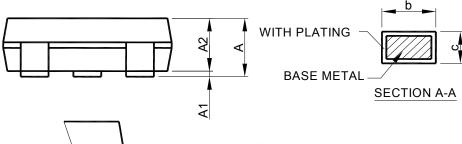


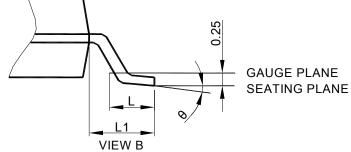
## ■ PHYSICAL DIMENSIONS (unit: mm)

#### SOT-23









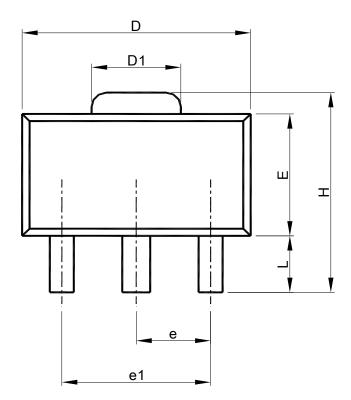
Note: 1. Refer to JEDEC MO-178.

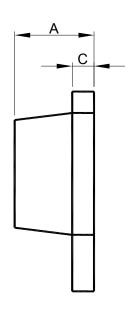
- 2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.
- 3. Dimension "E1" does not include inter-lead flash or protrusions.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

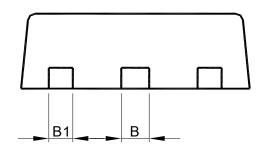
_					
S	SO1	Г-23			
S Y M B O	MILLIMETERS				
O L	MIN.	MAX.			
Α	0.95	1.45			
A1	0.00	0.15			
A2	0.90	1.30			
b	0.30	0.50			
С	0.08	0.22			
D	2.80	3.00			
Е	2.60	3.00			
E1	1.50	1.70			
е	0.95	BSC			
e1	1.90	BSC			
L	0.30	0.60			
L1	0.60 REF				
θ	0°	8°			
	•				



### • SOT-89







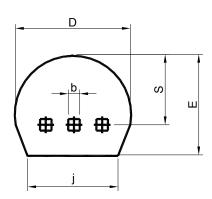
S Y	SOT-89			
M B O	MILLIMETERS			
O L	MIN.	MAX.		
Α	1.40	1.60		
В	0.44	0.56		
B1	0.36	0.48		
С	0.35	0.44		
D	4.40	4.60		
D1	1.50	1.83		
Е	2.29	2.60		
е	1.50 BSC			
e1	3.00 BSC			
Н	3.94	4.25		
L	0.89	1.20		

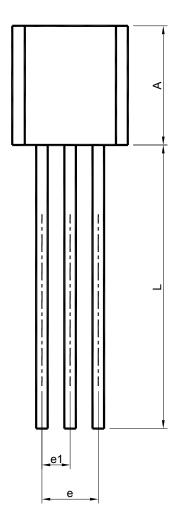
Note: 1. Refer to JEDEC TO-243AA.

- 2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 6 mil per side.
- 3. Dimension "E" does not include inter-lead flash or protrusions.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



TO-92 (Straight lead option available in Bag packing type only)





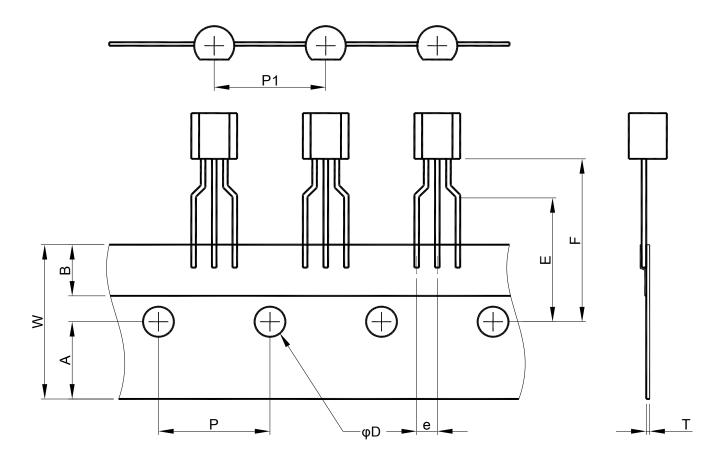
S Y	TO-92			
M	MILLIM	ETERS		
B O L	MIN.	MAX.		
Α	4.32	5.33		
b	0.36	0.47		
D	4.45	5.20		
Е	3.18	4.19		
е	2.42	2.66		
e1	1.15	1.39		
j	3.43			
L	12.70			
S	2.03	2.66		

Note: 1. Refer to JEDEC TO-226.

- 2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 6 mil per side .
- 3. Dimension "A" does not include inter-lead flash or protrusions.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



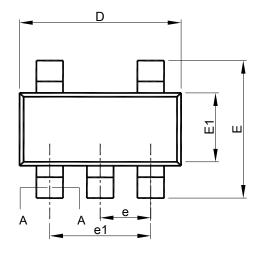
## ● TO-92 (Formed lead option available in Reel packing)

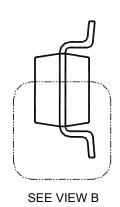


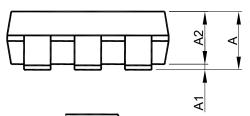
SYMBOL	W	А	В	Е	F
SPEC.	18.0±0.2	9.0±0.2	6.0±0.20	16.0±0.5	19.0±0.5
SYMBOL	Р	P 1	D	е	Т
SPEC.	12.7 BSC	12.7 BSC	4.0±0.2	2.5 BSC	0.6±0.1

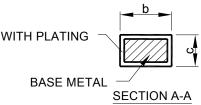


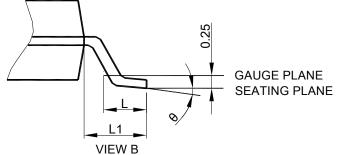
#### SOT-23-5











S Y M B O L	SOT-23-5	
	MILLIMETERS	
	MIN.	MAX.
Α	0.95	1.45
A1	0.00	0.15
A2	0.90	1.30
b	0.30	0.50
С	0.08	0.22
D	2.80	3.00
Е	2.60	3.00
E1	1.50	1.70
е	0.95 BSC	
e1	1.90 BSC	
L	0.30	0.60
L1	0.60 REF	
θ	0°	8°

Note: 1. Refer to JEDEC MO-178AA.

- 2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.
- 3. Dimension "E1" does not include inter-lead flash or protrusions.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

#### Note:

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