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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74LS245

Octal Bus Transceivers (with three-state outputs)

REJ03D0464-0300 Rev.3.00 Jul.15.2005

This octal bus transceiver is designed for synchronous two-way communication between data buses. The control function implementation minimizes external timing requirements. The device allows data transmission from the A bus to the B bus or from the B bus to the A bus depending upon the logic level at the direction control (DIR) input. The enable input (\overline{G}) can be used to disable the device so that the buses are effectively isolated.

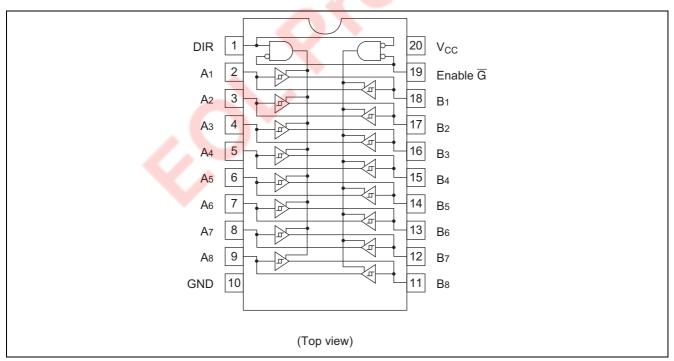
Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS245P	DILP-20 pin	PRDP0020AC-B (DP-20NEV)	Р	-
HD74LS245FPEL	SOP-20 pin (JEITA)	PRSP0020DD-B (FP-20DAV)	FP	EL (2,000 pcs/reel)
HD74LS245RPEL	SOP-20 pin (JEDEC)	PRSP0020DC-A (FP-20DBV)	RP	EL (1,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement

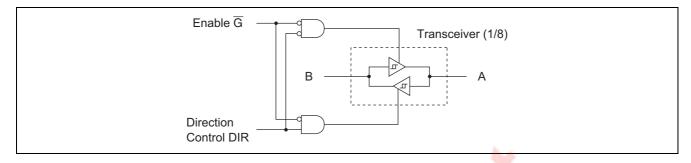


Function Table

Enable \overline{G}	Direction Control DIR	Operation
L	L	B data to A bus
L	Н	A data to B bus
Н	X	Isolation

Note: H; high level, L; low level, X; irrelevant

Block Diagram



Absolute Maximum Ratings

Item		Symbol	Ratings	Unit
Supply voltage		V _{CC}	7	V
Input voltage	DIR, G	V _{IN}	7	V
	A, B	V_{IN}	5.5	V
Power dissipation		P _T	400	mW
Storage temperature		Tstg	-65 to +150	°C
Operating temperature		Topr	−20 to +75	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	V _{CC}	4.75	5.00	5.25	V
Output ourrent	I _{OH}	_	_	-15	mA
Output current	I _{OL}	_	_	24	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item		Symbol	min.	typ.*	max.	Unit		Condition	
Input voltage		V_{IH}	2.0	_	_	V			
		V_{IL}	_	_	0.8	V			
Hysteresis		$V_T^+ - V_T^-$	0.2	0.4	_	V	$V_{CC} = 4.75 \text{ V}$		
		V	2.4	_	_	V	$I_{OH} = -3 \text{ mA}$	$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$	
Output valt	0.00	V _{OH}	2	_	_	V	$I_{OH} = -15 \text{ mA}$	$V_{IL} = 0.8 V$	
Output volta	aye	V	_	_	0.4	V	I _{OL} = 12 mA	$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$	
		V _{OL}	_	_	0.5	V	I _{OL} = 24 mA	$V_{IL} = 0.8 V$	
Off state or	Off-state output current		_	_	20	^	V _O = 2.7 V	V 505 V \(\overline{\over	
On-state of	atput current	I _{OZL}	_	_	-200	μΑ	V _O = 0.4 V	$V_{CC} = 5.25 \text{ V}, \overline{G} = 2 \text{ V}$	
		I _{IH}	_	_	20	μΑ	$V_{CC} = 5.25 \text{ V}, \text{ V}$	i = 2.7 V	
Input		I _{IL}	_	_	-0.2	mA	$V_{CC} = 5.25 \text{ V, V}$	i = 0.4 V	
current	A or B		_	_	0.1	mA	$V_{CC} = 5.25 \text{ V, V}$	i = 5.5 V	
	DIR or G	l _l	_	_	0.1	IIIA	V _{CC} = 5.25 V, V _I = 7 V		
Short-circuit output current		los	-40	_	-225	mA	V _{CC} = 5.25 V		
Supply current**		Іссн	_	48	70				
		I _{CCL}	_	62	90	mA	$V_{CC} = 5.25 \text{ V}$		
		I _{CCZ}	_	64	95				
Input clamp voltage		V _{IK}	_	_	-1.5	V	V _{CC} = 4.75 V, I _{II}	_N = −18 mA	

Notes: $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$

Switching Characteristics

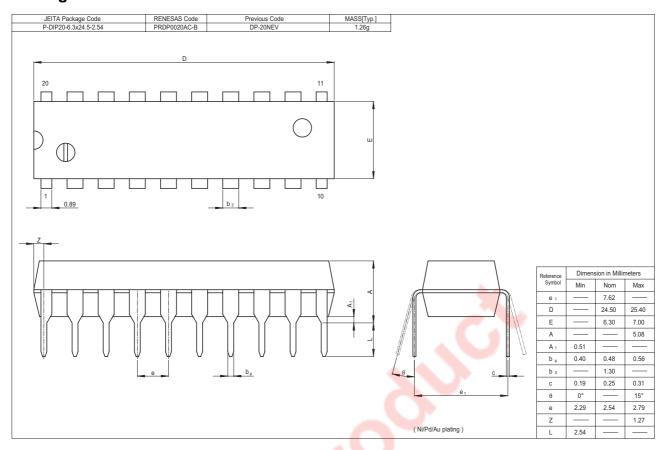
 $(V_{CC} = 5 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$

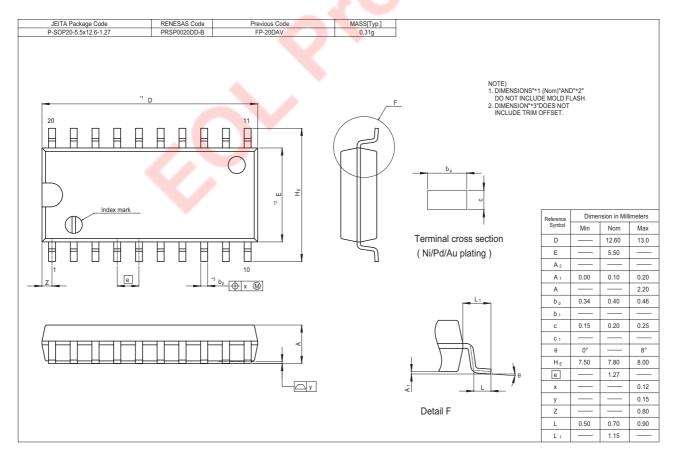
Item	Symbol	min.	typ.	max.	Unit	Condition	
Propagation delay time	t _{PLH}		8	15			
	t _{PHL}		11	15		C 45 p 667 O	
Output enable time	t _{ZL}		27	40	$C_L = 45 \text{ pF}, R_L = 667 \Omega$	$G_{L} = 45 \text{pr}, \text{RL} = 607 \Omega$	
	t _{zH}		25	40	ns		
Output disable time	t _{LZ}		15	25		C = 5 pE P = 667 O	
	t _{HZ}		15	25		$C_L = 5 \text{ pF}, R_L = 667 \Omega$	

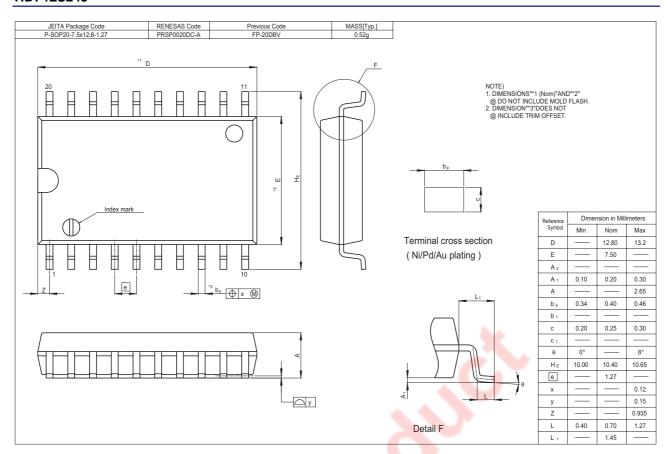
Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".

^{**} With all outputs open, I_{CC} is measured with transceivers enabled in one direction only, or with all transceivers disabled.

Package Dimensions







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Renesas Technology Malaysia Sdn. Bhd.

Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510
